

# LCD MODULE SPECIFICATION

Model: DF-TFC0695FB-M3

# This module uses ROHS materials

For customer acceptance

	<b>_</b>	
Customer		date
Approved		
Comments		

The standard product specification may change without prior notice in order to improve performance or quality. Please contact Display Future Ltd for updated specification and product status before design for the standard product or release of the order.

Revision	1.0
Engineering	
Date	2018/01/4
Our Reference	

# **REVISION RECORD**

REV NO.	REV DATE	CONTENTS	REVISED PAGE NO.
1.0	2015-08-25	First Release	

# **CONTENTS**

- GENERAL INFORMATION
- EXTERNAL DIMENSIONS
- ABSOLUTE MAXIMUM RATINGS
- ELECTRICAL CHARACTERISTICS
- BACKLIGHT CHARACTERISTICS
- ELECTRO-OPTICAL CHARACTERISTICS
- INTERFACE DESCRIPTION
- APPLICATION NOTES
- CTP SPECIFICATIONS
- RELIABILITY TEST
- INSPECTION CRITERION
- PRECAUTIONS FOR USING LCD MODULES
- USING LCD MODULES
- PRIOR CONSULT MATTER

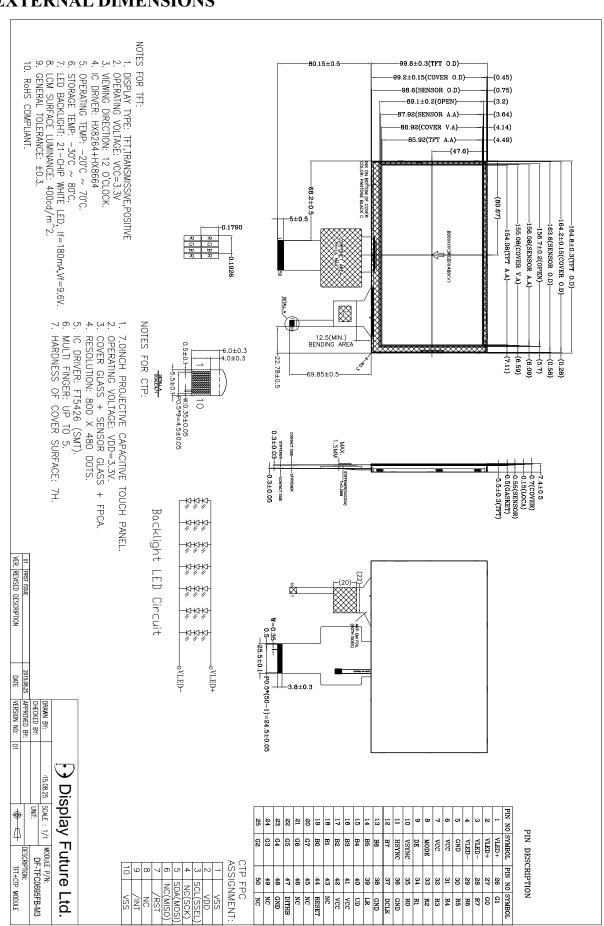
# **■ GENERAL INFORMATION**

Item	Contents	Unit
LCD type	TFT/Transmissive/Normally white	/
Size	7.0	Inch
Viewing direction	12:00(without image inversion and least brightness	O' Clock
	change)	
Gray scale inversion direction	6:00 (contrast peak located at)	O' Clock
$LCM(W \times H \times D)$	164.8×99.8×7.4	mm <sup>3</sup>
Active area (W×H)	154.08×85.92	$mm^2$
Pixel pitch (W×H)	0.0642×0.1790	$mm^2$
Number of dots	800 (RGB) × 480	/
Driver IC	HX8264+HX8664	/
Backlight type	21 LEDs	/
Interface type	24bit RGB	/
Color depth	16.7M	/
Pixel configuration	R.G.B vertical stripe	/
Top polarizer surface treatment	Anti-glare	/
Input voltage	3.3	V
With/Without TSP	With CTP	/
TP surface treatment	TBD	/
Weight	TBD	g

Note 1: RoHS compliant;

Note 2: LCM weight tolerance:  $\pm$  5%.

#### **■ EXTERNAL DIMENSIONS**



### ■ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit
Power voltage	VCC	-0.3	5.0	V
LED reverse voltage(each LED)	VR	-	1.2	V
LED forward current(each LED)	IF	-	30	mA
Operating temperature	Тор	-20	70	°C
Storage temperature	Tst	-30	80	°C

Note 1:The absolute maximum rating values of this product are not allowed to be exceeded at any times.should a module be used with any of the absolute maximum ratings exceeded,the characteristics of the module may not be recovered,or in an extreme case,the module may be permanently destroyed.

Note 2:VR conditions: Zener Diode 20mA.

#### ■ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Min	Тур	Max	Unit	Remark
Power voltage	VCC	3.0	3.3	3.6	V	Note 1
Current for driver	IVCC	-	71.4	-	mA	Vcc=3.3V
Input voltage 'H' level	Vih	0.7VCC	-	VCC	V	Note 2
Input voltage 'L' level	VIL	0	-	0.3VCC	V	

Note 1:VCC setting should match the signals output voltage (refer to Note 2) of customer's system bord.) Note 2:DCLK,HS,VS,RESET,U/D,L/R,DE,R0~R7,G0~G7,B0~B7,MODE,DITHB.

#### ■ BACKLIGHT CHARACTERISTICS

Item	Symbol	Min.	Тур.	Max.	Unit	Condition
Voltage for LED backlight	Vf	9.0	9.6	10.2	V	Note 1
Current for LED backlight	If	170	180	200	mA	
LED life time	-	30000	50000	-	Hrs	Note 2

Note 1:The LED supply voltage is defined by the number of LED at Ta=25°C and If=180mA.

Note 2:The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and If=180mA. The LED lifetime could be decreased if operating If is lager than 180mA.

Note 3:Typ.specification:Gray-level test pattern; Max.specification:Black-level test pattern.



(a) Gray-level test pattern



(b) Black-level test pattern

Item		Symbol	Condition	Min	Тур	Max	Unit	Remark	Note
Response	time	Tr+Tf		-	20	35	ms	FIG 1.	4
Contrast ratio		Cr	θ=0°	400	500	-		FIG 2.	1
Luminance uniformity		δ WHITE	Ø=0° Ta=25℃	70	75	-	%	FIG 2.	3
Surface Luminance		Lv		_	340	-	cd/m <sup>2</sup>	FIG 2.	2
	V:i1		Ø = 90°	40	50	-	deg	FIG 3.	
Viorvina anal			Ø = 270°	60	70	-	deg	FIG 3.	] _
Viewing angle range		nge θ		60	70	-	deg	FIG 3.	6
			Ø = 180°	60	70	-	deg	FIG 3.	]
CIE (x, y)	White	X	θ=0°, Ø=0°	-	0.280	-			
chromaticity	VV IIILE	v	Ta=25℃	_	0.310	_			

#### ■ELECTRO-OPTICAL CHARACTERISTICS

Note 1. Contrast Ratio(CR) is defined mathematically as For more information see FIG 2.

Contrast Ratio = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3. The uniformity in surface luminance  $, \delta$  WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see FIG 2.

δ WHITE = Minimum Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)

Maximum Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)

- Note 4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1. The test equipment is Autronic-Melchers's ConoScope. Series.
- Note 5. CIE (x, y) chromaticity, The x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.
- Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the conrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.
- Note 7. For viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments For contrast ratio, Surface Luminance, Luminance uniformity, CIE The test data is base on TOPCON's BM-5 photo detector.

#### FIG.1. The definition of Response Time

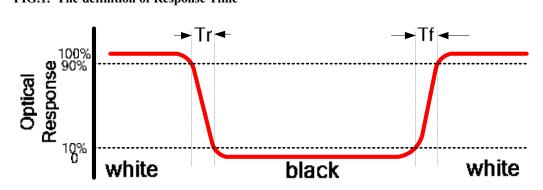


FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

A:5 mm

B:5 mm

H,V: Active Area

Light spot size  $\varnothing$ =5mm, 500mm distance from the

LCD surface to detector lens

measurement instrument is TOPCON's luminance

meter BM-5

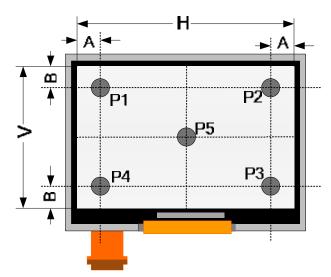
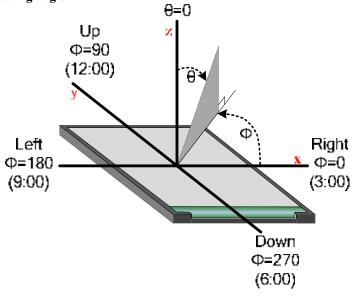


FIG.3. The definition of viewing angle



# ■ INTERFACE DESCRIPTION

# 1.TFT LCD Panel Driving Section

FPC connector is used for the module electronics interface. The recommended model is FH12A-50S-0.5SH manufactured by Hirose.

Pin No.	Symbol	I/0	Functi	Remark
1	V <sub>LED+</sub>	Р	Power for LED backlight (Anode)	
2	V <sub>LED+</sub>	Р	Power for LED backlight (Anode)	
3	V <sub>LED</sub> -	Р	Power for LED backlight (Cathode)	
4	V <sub>LED</sub> -	Р	Power for LED backlight (Cathode)	
5	GND	Р	Power ground	
6	VCC	Р	Power for Circuit	
7	VCC	Р	Power for Circuit	
8	MODE	I	DE/SYNC mode select	Note 1
9	DE	I	Data Input Enable	
10	VSYNC	I	Vertical Sync Input	
11	HSYNC	I	Horizontal Sync Input	
12	В7	I	Blue data(MSB)	
13	В6	I	Blue data	
14	В5	I	Blue data	
15	B4	I	Blue data	
16	В3	I	Blue data	
17	B2	I	Blue data	
18	B1	I	Blue data	Note 2
19	ВО	I	Blue data(LSB)	Note 2
20	G7	I	Green data(MSB)	
21	G6	I	Green data	
22	G5	I	Green data	
23	G4	I	Green data	
24	G3	I	Green data	
25	G2	I	Green data	
26	G1	I	Green data	Note 2
27	GO	I	Green data(LSB)	Note 2

28	R7	Ι	Red data(MSB)	
29	R6	Ι	Red data	
30	R5	Ι	Red data	
31	R4	Ι	Red data	
32	R3	I	Red data	
33	R2	Ι	Red data	
34	R1	I	Red data	Note 2
35	RO	I	Red data(LSB)	Note 2
36	GND	Р	Power Ground	
37	DCLK	I	Clock for input data	Note 3
38	GND	Р	Power Ground	
39	LR	I	Left / right selection	Note 4,5
40	UD	I	Up/down selection	Note 4,5
41	VCC	Р	Power for Circuit	
42	VCC	Р	Power for Circuit	
43	NC	_	No connection	
44	RESET	I	Global reset pin.	Note 6
45	NC	_	No connection	
46	NC	_	No connection	
47	DITHB	I	Dithering function	Note 7
48	GND	Р	Power Ground	
49	NC	_	No connection	
50	NC	_	No connection	

I: input, O: output, P: power

Note 1: DE/SYNC mode select. Normally pull high.

When select DE mode, MODE="1", VS and HS must pull high.

When select SYNC mode, MODE="0", DE must be grounded.

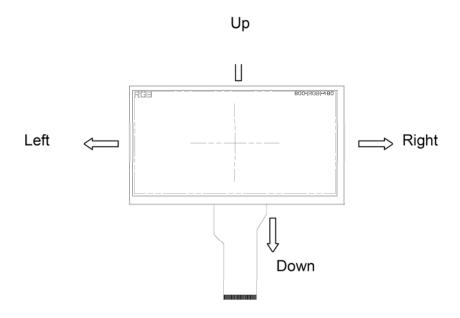
Note 2: When input 18bit RGB data, the two low bits of R,G and B data must be grounded.

Note 3: Data shall be latched at the falling edge of DCLK.

Note 4: Selection of scanning mode.

Set of scan co	ntrol input	Scanning direction		
UD	LR			
GND	VCC	Up to down, left to right		
VCC	GND	Down to up, right to left		
GND	GND	Up to down, right to left		
VCC	VCC	Down to up, left to right		

Note 5: Definition of scanning direction direction. Refer to the figure as below:



Note 6: Global reset pin.Active low to enter reset state.Suggest to connect with an RC reset circuit for stability. Normally pull high.

Note 7: Dithering function enable control, normally pull high. When DITHB="1", Disable internal dithering function,

When DITHB="0", Enable internal dithering function,

# ■ APPLICATION NOTES

# 1. Timing Characteristics

## 1.1 AC Electrical Characteristics

Parameter	Symbol	Symbol Spec.			
Parameter	Symbol	Min.	Тур.	Max.	Unit
HS setup time	$T_{hst}$	8	-	-	ns
HS hold time	$T_{hhd}$	8	-	-	ns
VS setup time	T <sub>vst</sub>	8	-	-	ns
VS hold time	$T_{vhd}$	8	-	-	ns
Data setup time	T <sub>dsu</sub>	8	-	-	ns
Data hold time	$T_{dhd}$	8	-	-	ns
DE setup time	T <sub>esu</sub>	8	-	-	ns
DE hold time	T <sub>ehd</sub>	8	-	_	ns
VDD Power On Slew rate	$T_POR$	-	-	20	ms
RSTB pulse width	T <sub>Rst</sub>	10	-	-	us
CLKIN cycle time	T <sub>cph</sub>	20	-	-	ns
CLKIN pulse duty	T <sub>cwh</sub>	40	50	60	%
Output stable time	T <sub>sst</sub>	-	-	6	us

# 1.2 Input clock and data timing waveform

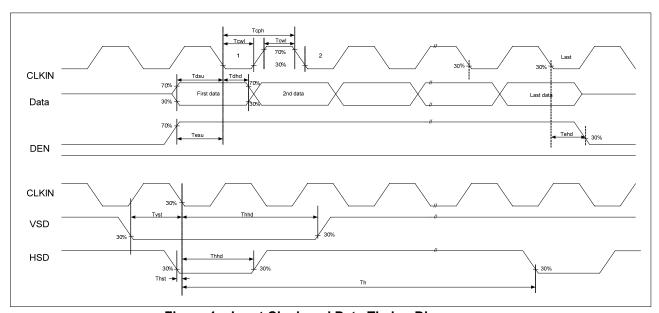


Figure 1 Input Clock and Data Timing Diagram

# 1.3. Data Input Format



Figure 2 Horizon input timing diagram

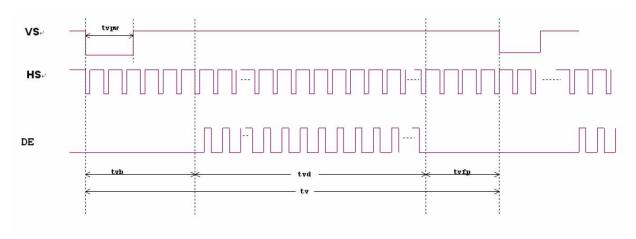


Figure 3 Vertical input timing diagram

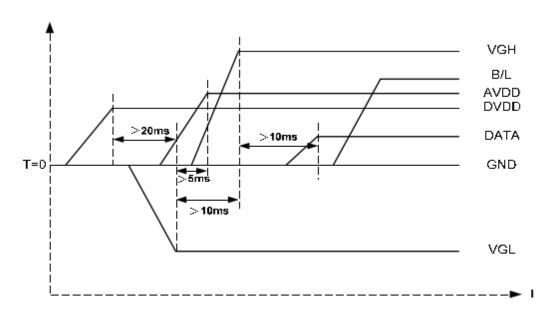
# 1.4 Timing

T.	C11		Values		TT14	D 1
Item	Symbol	Min.	Min. Typ. Max.		Unit	Remark
Horizontal Display Area	thd	-	800	-	DCLK	
DCLK Frequency	fclk	29.0	33.0	38.0	MHz	
One Horizontal Line	th	1026	1056	1086	DCLK	
HS pulse width	thpw	-	30	-	DCLK	
HS Blanking	thb	-	46	-	DCLK	
HS Front Porch	thfp	180	210	240	DCLK	_

	G 1 1	Values			TT '.	D 1
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Vertical Display Area	tvd	-	480	-	TH	
VS period time	tv	515	525	535	TH	
VS pulse width	tvpw	-	13	-	TH	
VS Blanking	tvb	-	23	-	TH	
VS Front Porch	tvfp	12	22	32	TH	

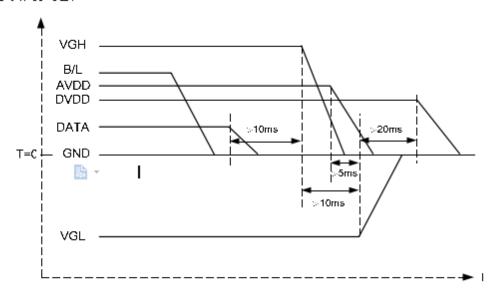
# 2. Power Sequence

#### a. Power on:



 $DV_{DD} \rightarrow VGL \rightarrow VGH \rightarrow Data \rightarrow B/L$ 

#### b. Power off:



$$B/L \rightarrow Data \rightarrow VGH \rightarrow VGL \rightarrow DV_{DD}$$

Note: Data include R0~R7, B0~B7, GO~G7, U/D, L/R, DCLK, HS, VS, DE.

# **■ CTP SPECIFICATIONS**

## 1. GENERAL SPECIFICATIONS

Item Specification		Unit
Туре	Projective capacitive type touch panel	
Strueture	Cover glass+Sensor glass+FPCA	
Input mode	Human's finger	
Finger	Up to 5	
Resolution	800 x 480	dots
Cover viewing area	155.08(W) x 86.92(H)	mm
Sensor Active Area	156.08(W) x 87.92(H)	mm
Hardness	7H	Pencil hardness
Driver IC	FT5426	/

## 2. ABSOLUTE MAXIMUM RATINGS

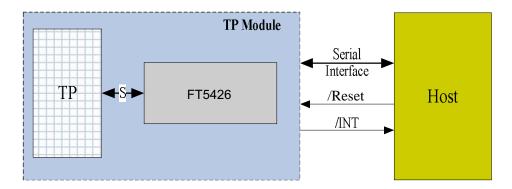
Symbol	Description	Min	Тур	Max	Unit	Notes
VDD	Supply voltage	2.8	3.3	3.6	V	
IDD	Supply current	-	TBD	-	mA	
VIH	Input high-level voltage	0.7VDD	-	VDD	V	
VIL	Input low-level voltage	-0.3	-	0.3VDD	V	
VOH	Output high-level voltage	0.7VDD	-	-	V	
VOL	Output low-level voltage	-	-	0.3VDD	V	

#### 3. PIN CONNECTIONS

No.	Name	I/O	Description
1	VSS	Р	Ground
2	VDD	Р	Power supply
3	SCL(SSEL)	I/O	I2C clock signal
4	NC(SCK)	-	No connection
5	SDA(MOSI)	I/O	I2C data signal
6	NC(MISO)	-	No connection
7	/RST	I	Reset.active low
8	NC	-	No connection
9	/INT	Р	Interrupt signal to host from CTP
10	VSS	Р	Ground

Note: SCL/SDA for I2C in terface internal pull up on pin  $3\&5(100K\Omega)$ 

#### 4. BLOCK DIAGRAM



#### 5. CTP TIMING

The I2C is always configured in the Slave mode. The data transfer format is shown in Figure 1-1.

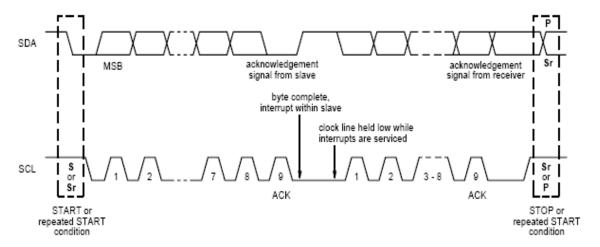


Figure 1-1 I2C Serial Data Transfer Format

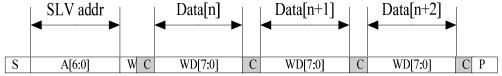


Figure 1-2 I2C master write, slave read

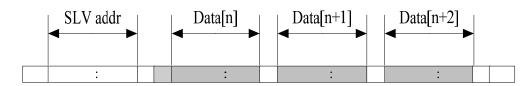


Figure 1-3 I2C master read, slave write

Table 1-1 lists the meanings of the mnemonics used in the above figures.

**Table 1-1 Mnemonics Description** 

Mnemonics	Description
S	I2C Start or I2C Restart
A[6:0]	Slave address A[6:4]: 3'b011 A[3:0]: data bits are identical to those of I2CCON[7:4] register.
W	1'b0: Write
R	1'b1: Read
С	ACK
P	STOP: the indication of the end of a packet (if this bit is missing, S will indicate the end of the current packet and the beginning of the next packet)

I2C Interface Timing Characteristics is shown in Table 1-2.

**Table 1-2 I2C Timing Characteristics** 

Parameter	Unit	Min	Max
SCL frequency	KHz	0	400
Bus free time between a STOP and START condition	us	4.7	\
Hold time (repeated) START condition	us	4.0	\
Data setup time	ns	250	\
Setup time for a repeated START condition	us	4.7	\
Setup Time for STOP condition	us	4.0	\

Note: More information pls refer to IC spec.

## **■ RELIABILITY TEST**

No.	Test Item	Test Condition	Inspection after test
1	High Temperature Storage	$80\pm2$ °C/240 hours	NOTE1,NOTE4
2	Low Temperature Storage	-30±2°C/240 hours	NOTE1,NOTE4
3	High Temperature Operating	70±2°C/240 hours	NOTE2,NOTE4
4	Low Temperature Operating	-20±2℃/240 hours	NOTE2,NOTE4
5	Temperature Cycle storage	$-30\pm2^{\circ}\text{C} \sim 25 \sim 80\pm2^{\circ}\text{C} \times 20 \text{cycles}$ (30min.) (5min.) (30min.)	NOTE4
6	Damp proof Test operating	$60^{\circ}\text{C} \pm 5^{\circ}\text{C} \times 90\%\text{RH/240 hours}$	NOTE4
7	Vibration Test	Frequency: 10~55Hz Stroke:1.5mm Sweep: 10Hz~55Hz~10Hz 2hours for each direction of X.Y.Z (6 hours for total)	
8	Package vibration test	Random vibration :0.15G*G/HZ from 5-200HZ,-6dB/Octave from 200-500HZ of each direction of X.Y. Z (6 hours for total)	
9	Package drop test	Height:60 cm 1 corner,3 edges,6 surfaces	
10	ESD test	±2KV, Human body mode,100pF	
11	Mechanical shock	100G 6ms,±X, ±Y,±Z 3 times for each direction	

- Note 1: Ta is the ambient temperature of samples.
- Note 2: Ts is the temperature of panel's surface.
- Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.
- Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

#### ■ INSPECTION CRITERION

OUTGOING QUALITY STANDARD	PAGE 1 OF 5
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA	

This specification is made to be used as the standard acceptance/rejection criteria for TFT module.

1 Sample plan

1.1 Lot size: Quantity per shipment lot per model

1.2 Sampling type: Normal inspection, Single sampling

1.3 Inspection level: II

1.4 Sampling table: MIL-STD-105D1.5 Acceptable quality level (AQL)

Major defect: AQL=0.65 Minor defect: AQL=1.50

2. Inspection condition

2.1 Ambient conditions:

a. Temperature: Room temperature  $25\pm5^{\circ}$ C

b. Humidity: (60± 10) %RH

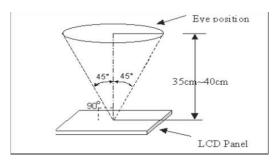
c. Illumination: Single fluoresænt lamp non-directive (300 to 700 Lux)

2.2 Viewing distance:

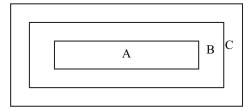
The distance between the LCD and the inspector's eyes shall be at least  $35\pm$  5cm.

2.3 Viewing Angle

U/D: 45° /45° , L/R: 45° /45°



- 3. Definition of Inspection Item.
  - 3.1 Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD.

OUTGOING QUALITY STANDARD	PAGE 2 OF 5
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA	

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.
4. Inspection standards

Defects are classified as majot defects and minor defects according to the degree of defectiveness defined herein.

## 4.1 Major defect

Item No	Items to be inspected	Inspection Standard	
4.1.1	All functional defects	<ol> <li>No display</li> <li>Display abnormally</li> <li>Short circuit</li> <li>Line defect</li> <li>Excess power consumption</li> </ol>	
4.1.2	Missing	Missing function component	
4.1.3	Crack	Glass crack	

#### 4.2 Minor defect

Item No	Items to be inspected	Inspection standard		
4.2.1	Spot Defect Including Black spot	For dark/white spot is def $\varphi = (\mathbf{x} + \mathbf{y}) / 2$ $\longrightarrow \mathbf{X} \qquad \qquad$	ined	
	White spot Pinhole Foreign particle	Size φ(mm)  φ≤0.15 2mm(min) apart	Acceptable Quantity  Ignore	
	Polarizer dirt	0.15 < φ≤ 0.25 5mm(min) apart	3	
		0.25< φ	Not allowed	

OUTGOING QUALITY STANDARD			PAGE 3 OF 5
TLE:FUN	CTIONAL TEST & II		
	Line Defect Including Black line White line Scratch	Define:	Vidth
4.2.2		Width(mm) Length(mm)	Acceptable Quantity
		W≤0.05 and L≤10	Ignore
		0.05 < W≤0.08 and L≤10 3mm(min) apart	3
		0.08 < W≤0.10 andL≤5 3mm(min) apart	1
		0.10< W or 10 <l< td=""><td>Not allowed</td></l<>	Not allowed
4.2.3		Size φ(mm)	Acceptable Quantity
	Polarizer Dent/Bubble	φ≤0.25	Ignore
		Non visible area	Ignore
		0.25<φ≤0.40 5mm(min) apart	2
		0.40< φ	Not allowed
4.2.4	Electrical Dot Defect	Bright and Black dot def	and
		Inspection pattern: Full white, Full black, Red, green and blue screens	
		Item	Acceptable Quantity
		Black dot defect	2
		Bright dot defect	0
		Total Dot	2

OUTGOING QUALITY STANDARD PAGE 4 OF 5  TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA						
	Touch panel chips	1.Corner chips:	X y			
		Size(mm)	Acceptable Quantity			
4.2.5		X≤3mm Y≤3mm Z≤T	Ignore T: Glass thickness X: Length Y: Width Z: thickness			
		2. Side chips:	X Z			
		Size(mm)	Acceptable Quantity			
		X≤5mm Y≤3mm Z≤T	Ignore T: Glass thickness X: Length Y: Width Z: thickness			

OUTGOING QUALITY STANDARD	PAGE 5 OF 5
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA	

Note: 1. Dot defect is defined as the defective area of the dot area is larger than 50% of the dot area.

- 2. The distance between black dot defects or black and bright dot defects should be more than 5mm apart. The distance between two bright dot defects should be more than 15mm apart
- 3. Polarizer bubble is defined as the bubble appears on active display area. The defect of polarizer bubble shall be ignored if the polarizer bubble appears on the outside of active display area.
- 4. Mura is checker by 6% ND filter.
- 5. Foreign particle on the surface of the LCM should be ignore.

#### ■ PRECAUTIONS FOR USING LCD MODULES

#### **Handing Precautions**

- (1) The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
  - Isopropyl alcohol
  - Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

- (6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
  - Water
  - Ketone
  - Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.

- (7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
  - (9) Do not attempt to disassemble or process the LCD module.
  - (10) NC terminal should be open. Do not connect anything.
  - (11) If the logic circuit power is off, do not apply the input signals.
- (12) Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
- Tools required for assembling, such as soldering irons, must be properly grounded. make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential
- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated

- (13) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
  - Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
  - Do not damage or modify the pattern writing on the printed circuit board.
  - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
  - Do not drop, bend or twist LCM.

#### Handling precaution for LCM

LCM is easy to be damaged.

Please note below and be careful for handling!

## Correct handling:



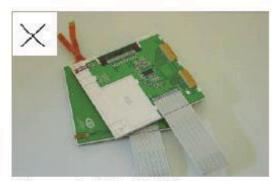


As above picture, please handle with anti-static gloves around LCM edges.

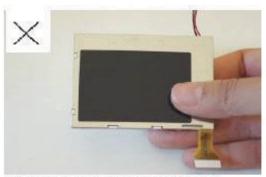
# Incorrect handling:



Please don't touch IC directly.



Please don't stack LCM.



Please don't hold the surface of panel.



Please don't stretch interface of output, such as FPC cable.

## Handling precaution for LCD

LCD is easy to be damaged.

Please note below and be careful for handling!

## Correct handling:





As above photo, please handle with anti-static gloves around LCD edges.

# Incorrect handling:



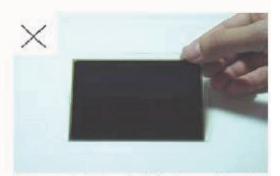
Please don't stack the LCDS.



Please don't operate with sharp stick such as pens.



Please don't hold the surface of LCD.



Please don't touch ITO glass without anti-static gloves.

#### **Storage Precautions**

When storing the LCD modules, the following precaution is necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the anti-static electricity container in which they were shipped. Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

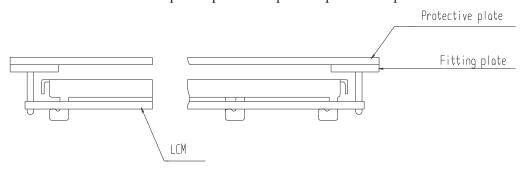
- Exposed area of the printed circuit board.
- -Terminal electrode sections.

#### USING LCD MODULES

#### **Installing LCD Modules**

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

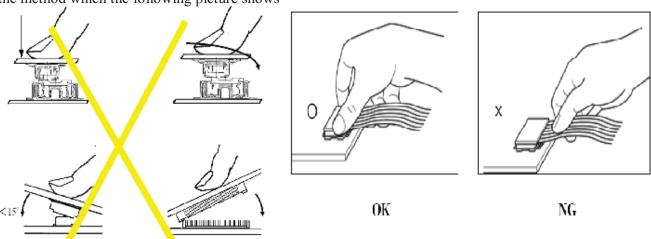
(1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



(2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be  $\pm 0.1$  mm.

#### Precaution for assemble the module with BTB connector:

Please note the position of the male and female connector position,don't assemble or assemble like the method which the following picture shows



#### Precaution for soldering to the LCM

	Hand soldering	Machine drag soldering	Machine press soldering
No ROHS product	290°C ~350°C.	330°C ~350°C.	300°C ~330°C.
	Time : 3-5S.	Speed: 4-8 mm/s.	Time : 3-6S.
		_	Press: 0.8~1.2Mpa
ROHS product	340°C ~370°C.	350°C ~370°C.	330°C ~360°C.
	Time : 3-5S.	Time: 4-8 mm/s.	Time : 3-6S.
			Press: 0.8~1.2Mpa

- (1) If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- (2) When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- (3) When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

#### **Precautions for Operation**

- (1) Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.
- (2) It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- (3) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, Which will come back in the specified operating temperature.
- (4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- (5) A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50%RH or less is required.
  - (6) Input each signal after the positive/negative voltage becomes stable.
- (7) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

#### Safety

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

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### **Limited Warranty**

Unless agreed otherwise between Display Future Ltd and customer, Display Future will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with LCD acceptance standards (copies available upon request) for a period of one year from date of production. Cosmetic/visual defects must be returned within 90 days of the shipment. Confirmation of such date shall be based on data code on the product.

The warranty liability of Display Future limited to repair and/or replacement on the terms set forth above. Display Future will not be responsible for any subsequent or consequential events.

#### Return LCM under warranty

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

- Broken LCD glass.
- PCB eyelet is damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- Soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet, conductors and terminals.

#### ■ PRIOR CONSULT MATTER

- 1. ①For Display Future standard products, we keep the right to change material and processes for improving the product, without notice to our customers.
  - ②For OEM products, if any change is needed, which may affect the product property, we will consult with our customer in advance.
- 2. If you have special requirement about reliability condition, please let us know before you start the test on our samples.

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