Display Future Ltd

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LCD MODULE SPECIFICATION

Model: DF-SSC0509---M2

This module uses ROHS materials

For customer acceptance

Customer		date
Approved		
Comments		

The standard product specification may change without prior notice in order to improve performance or quality. Please contact Display Future Ltd for updated specification and product status before design for the standard product or release of the order.	Revision	1.0
	Engineering	
	Date	2018/01/4
	Our Reference	

REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2018-01-04	First Release	

CONTENTS

■ **GENERAL SPECIFICATIONS**

The projected capacitive touch technology applied to this product is an ITO-based touch technology. It consists of one glass substrate layers with ITO coating patterned into a grid of rows/columns and cover lens that are laminated together. During a touch, the capacitance of the finger changes the capacitive coupling between the grid elements on the location of the touch. This location is calculated from the change in electrical characteristics of the sensor grid. Mathematical processing, programmed in the Touch Controller chip, is used to recognize this distortion. Capacitive sensors can be touched with a bare finger or a conductive device being held by a bare hand. They are not affected by outside elements and have high clarity.

The purpose of this specification is to define the general provisions and quality requirements that apply to the supply of capacitive touch sensor or capacitive touch panel (CTP) module manufactured by Display Future. This document, together with the Module Drawing, is the highest-level specification for this product. It describes the product and contains specifications.

Features	Details	Unit	Note
Operation Technology	Projected capacitive	--	1
Product structure	Glass Lens－Glass Sensor	--	2
Input Method	Bare finger	--	--
Number of simultaneous touches	2 points multi-touch	--	--
Minimum Touch Area	Φ7	mm	--
Surface Treatment	-	H	
Finger Pitch	≥13	mm	
Connection Type	FPC,10pin, Pitch0.5mm	--	—
Customer Application	Industry	--	--
CTP and LCD Assembly	Optical bonding/DST	--	
FG Weight	TBD	g	--

Note 1: Mutual mode.
Note 2: RoHS compatible.

■ **PRODUCTION DESCRIPTION**

1 General description

Item	Contents	Unit	Note
Product size	5.0	inch	
TP outline	123.26 (W) x 80.06 (H) x 1.35(T)	mm	
TP active area	110.40(W) x 67.20(H)	mm	
Resolution	640*480		
Operation temperature	-20~70	℃	
Storage temperature	-30~80	℃	
Control IC	NT11003QG-48/A		
Interface	I2C		1

Note 1:   It can be compatible with Andriod .

2 Structure description

Top Lens

Lamination

Sensor Glass
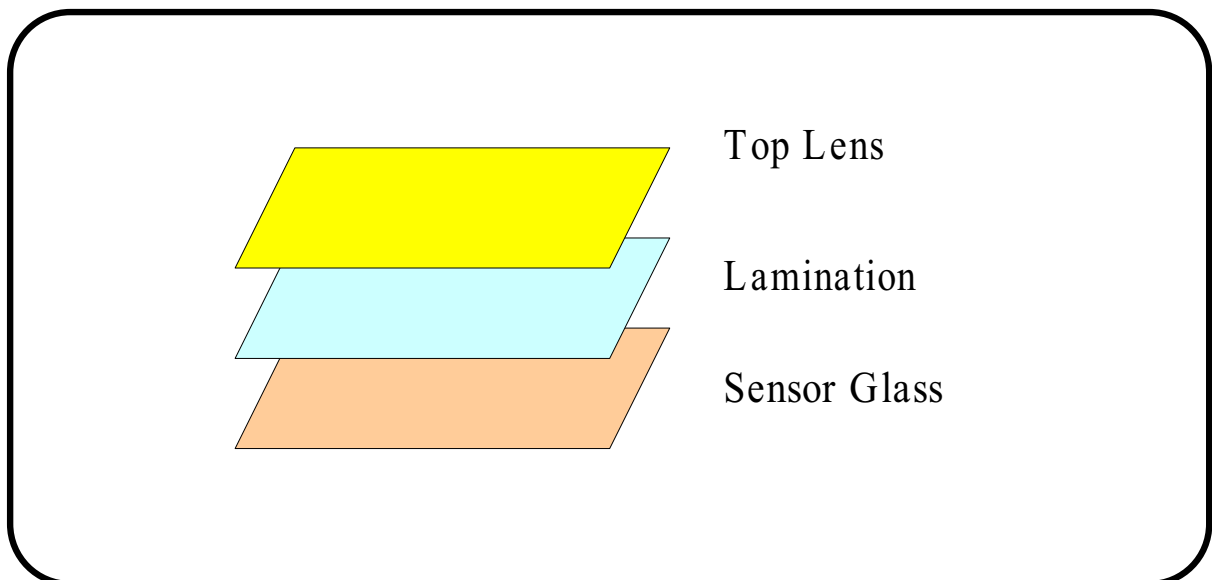
Structure of touch panel

3 DC Characteristics

(T$_A$= 25℃,VDD=3.3V)

Item	Min	Typ	Max	Unit	Note
power supply voltage	2.7	--	3.6	V	DC(noise should be under 100mV)
IOVDD	1.65	--	3.6	V	
Power Consumption	--	--	10	mW	One finger on sensor
Sleep mode	--	--	20	uA	1
Respond time	--	--	12	ms	

Note1: All current measurement is average current.

4 Interface Timing Chart

4.1 IIC Timing
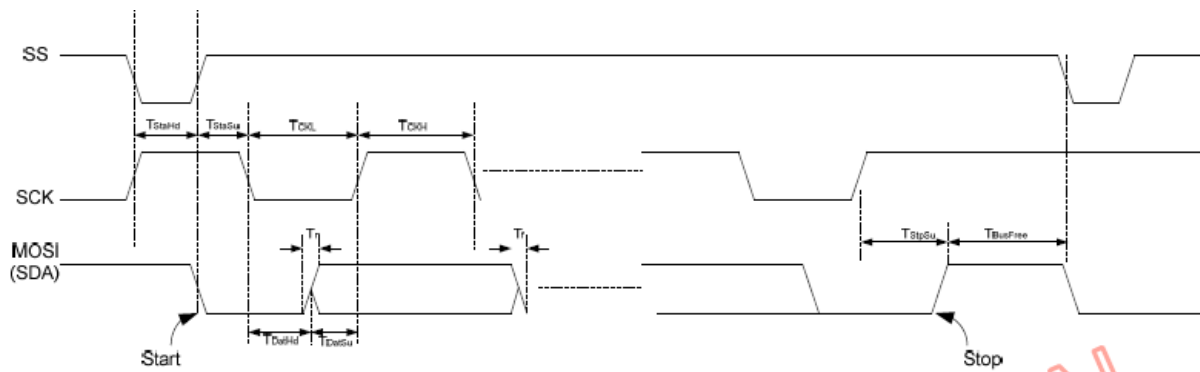
Fig4: IIC Timing

The IIC Timing Table as follows.

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Working Frequency	Fclk	0		400	KHz	
I2C Clock Low	TCKL	1250			ns	VDD =3.3V, TA=25 .
I2C Clock High	TCKH	1250			ns	VDD =3.3V, TA=25 .
I2C Data Rising Time	Tr			300	ns	VDD =3.3V, TA=25 .
I2C Data Falling Time	Tf			300	ns	VDD =3.3V, TA=25 .

I2C Data Hold Time	TDatHd	0			ns	VDD = 3.3V, TA=25 .
I2C Data Setup Time	TDatSu	100			ns	VDD = 3.3V, TA=25 .
I2C Start Condition Hold Time	TStaHd	600			ns	VDD = 3.3V, TA=25 . VDD =3.3V, TA=25℃.
I2C Start Condition Setup Time	TStaSu	600			ns	VDD = 3.3V, TA=25 .
I2C Stop Condition Setup Time	TStpSu	600			ns	VDD = 3.3V, TA=25 .
I2C Bus Free Time	TBusFree	1300			ns	VDD = 3.3V, TA=25 .

4.2

Register Definition

We reserve 42 bytes I2C buffer for recording gesture information and 4 bytes system control register for system designer to control touch panel appropriate for your requirement.

Address	I2C Buffer Definition							
00H	GID 1	GID 2	P1_D1	P1_D2	P1_D3	P1_D4	P2_D1	P2_D2
08H	P2_D3	P2_D4	P3_D1	P3_D2	P3_D3	P3_D4	P4_D1	P4_D2
10H	P4_D3	P4_D4	P5_D1	P5_D2	P5_D3	P5_D4	P6_D1	P6_D2
18H	P6_D3	P6_D4	P7_D1	P7_D2	P7_D3	P7_D4	P8_D1	P8_D2
20H	P8_D3	P8D4	P9_D1	P9_D2	P9_D3	P9_D4	P10_D1	P10_D2
28H	P10_D3	P10_D4	F/W Ver.	Pwr_Ctl_1	Pwr_Ctl_2	Read_Pnt	Reserve	Reserve

5 Mechanical Characteristics

No.	Item	Requirement	Verification method
1	Surface hardness	6H	JIS-K5600
2	Drop ball test	No crack after test.	Use the 64g steel (∅ 25) ball is dropped on the Glass surface from 70cm height at 1time(Glass side)
3	Surface pressure Test	No crack after test.	15 Kgf pressure in the center of the display using a rubber test head with a diameter of 15mm, 1 time,1 minute, non-operation
4	Terminal Pull Test	Function is OK	±90º direction, weight:500g, non-operation

6 Electrical Characteristics

condition (Ta=25℃,VDD=3.3V)

No.	Item	Specification	NOTE
1	Linearity	±1mm	1.5mm at the border
2	Veracity	±1mm	1.5mm at the border
3	Sensivity	±1mm	1..5mm at the border
4	ESD	TBD	C=150pF、R=330Ω Air=±8KV 5times; contact::±4KV 5times （Environment：15℃~35℃、30%~60%,86Kpa~106Kpa）

7 Optical Characteristics   (Ta = 25 ℃)

No.	Item	Min.	Typ.	Max.	Unit	Remark
1	Transmission	86	88		%	Note 1
2	Reflectivity				%	Note 1,Note 2
3	HAZE				%	

Note1: Measuring equipments: DMS-501, PR-705. @550nm

Measuring condition:

- After stabilizing and leaving the panel alone at a given temperature for 30   min, the measurement should be executed,

- Measuring surroundings: a stable, windless and dark room,

- Measuring temperature: Ta=25°C,

- 30 min after lighting the back-light.
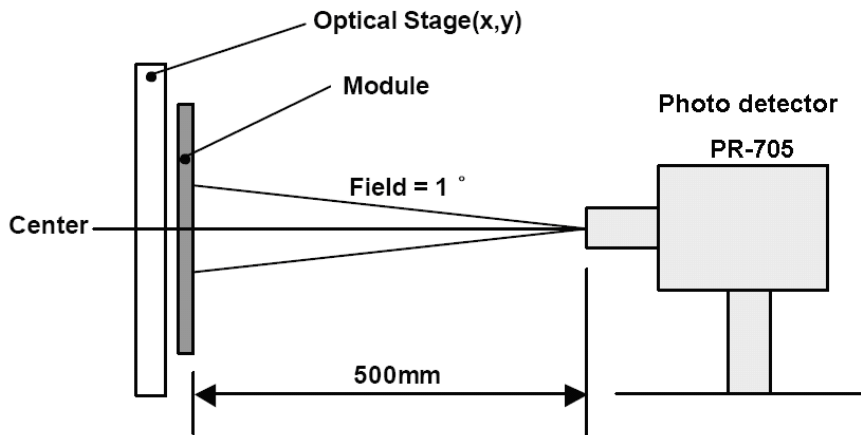
Note2:   conform to National standard GB2410—80 /ASTM D1003—61(1997)
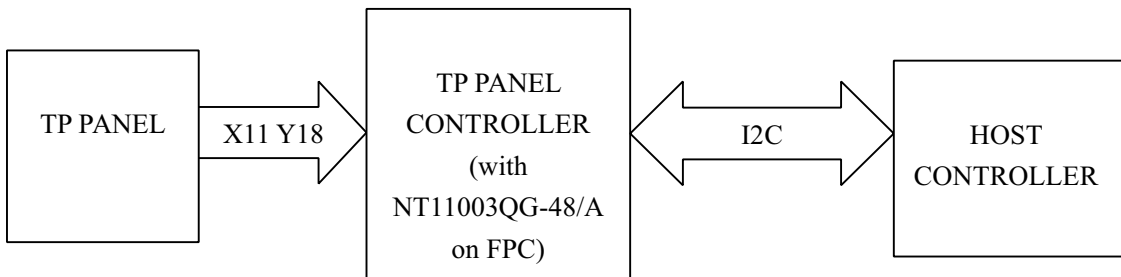
■ CIRCUIT BLOCK DIAGRAM

Fig2. System Block Diagram

■ PIN CONNECTIONS

Pin No.	Symbol	I/O	Description	Remark
1	GND	P	Ground	
2	RESET	I/O	Active Low	
3	VDD	P	Power	
4	INT	P	Active Low	
5	SCL	I/O	CLOCK	
6	SDA	I/O	Data I/O	
7	TP_SYNC	I/O	GPIO from LCD	
8	NC	-	No connection	
9	NC	-	No connection	
10	NC	-	No connection	

■ **RELIABILITY**

No	Test Item	Test condition	Criterion
1	High Temperature Storage	+80℃±2℃ Power off	
2	Low Temperature Storage	-30℃±2℃ Power off	
3	High Temperature Operation	+70℃±2℃ Power on	
4	Low Temperature Operation	-20℃±2℃ Power on	
5	High Temperature & Humidity Storage	+60℃±2℃ 90%RH±2%RH,	
6	Thermal Shock Test(storage)	-20℃（30min）⇔70℃（30min）,Change Time:5min	
7	Package Drop Test	Height:60cm, 1corner,3edges,6surfaces	
8	Package Vibration Test	Half Sine Wave 50G 6ms, ±X,±Y,±Z 3times for each direction	

Note: Additional test Item proposed by customer shall be determined by mutual agreement between customer and Multi-Inno

For consumer production uses, we recommended the temperature operation range of 0~60 degree, beyond this temperature range can still be used, but the performance may be decrease, the difference with the production will be different.

■ **SPECIFICATION OF QUALITY ASSURANCE**

1 Inspection condition

a. Inspected Temperature：20～25℃,Inspected Distance：30±5cm.

b. Viewing Angle:

When inspecting, keep the eyesight perpendicular to the product surface:90± 30 degree,as below.
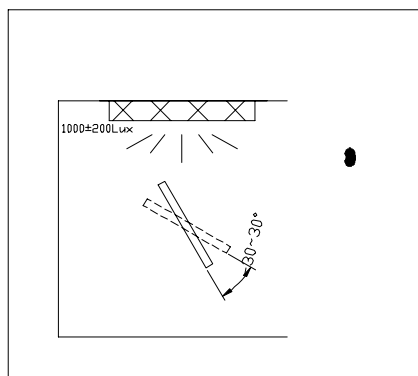
c. Inspected illumination:1000±200Lux.

d. Inspected background：Under black background
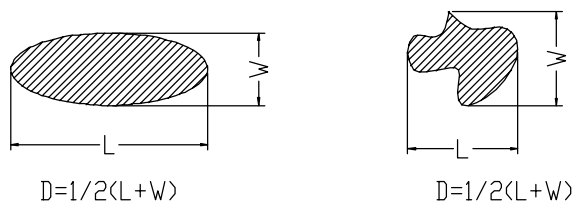
2   Definiton for the appearance area.

A area: The front area of the sample and the transparent area from the backside; as below;

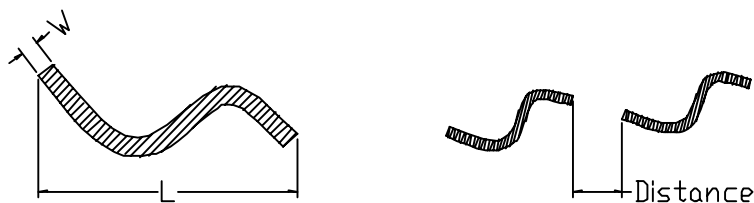B area: The backside printing area. As below.

FPC

COMPONENT AREA

COVER

SENSOR

B area

touch windows

transparent area

A area

front

back
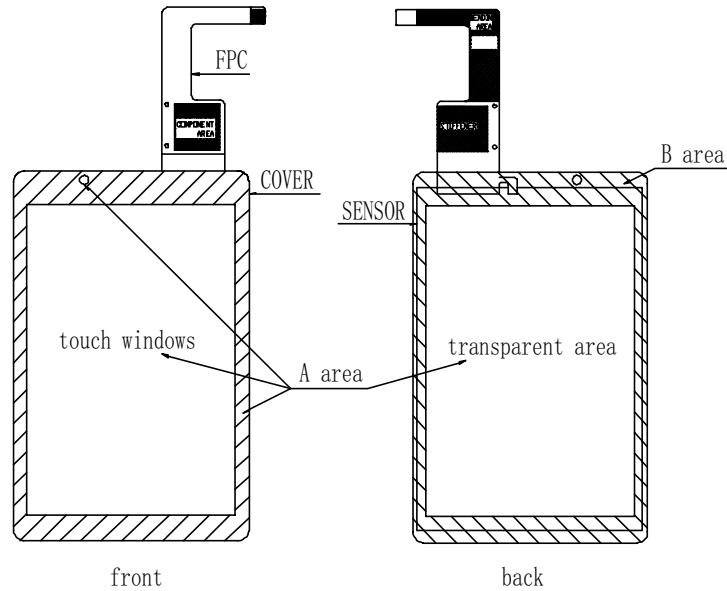
3   Definiton for the defects.

a. Circular Defects:

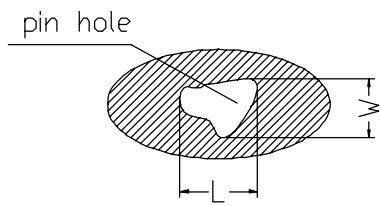$D=1/2(L+W)$ $D=1/2(L+W)$

D：diameter   W:width   L: length (the same as below)
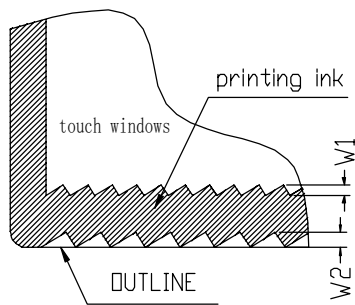
b.   Linear Defects:

L

Distance

c.   Pin hole(Translucidus)

pin hole

$$D=1/2(L+W)$$

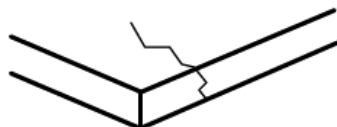d.   Zigzag for the printing ink

printing ink

touch windows

W1

OUTLINE

W2

e.   Edge Crack Chip

X

Y

Z
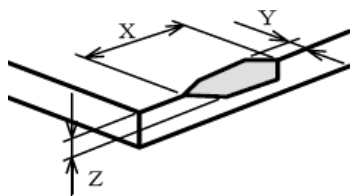
f.   Corner Crack Chip
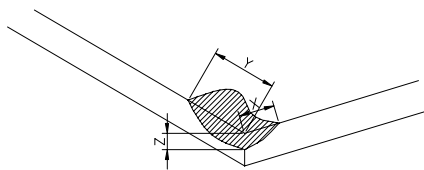
g.   Bad Crack

4 Foreign object

No.	Inspection items	Judgment standard(Unit：mm）		
1	Circular Defects(Dot、Impurity、Dust、Bubble)	Defect Size	Judgment（A Area）	Judgment（B Area）
		D≤0.20	Neglected(distance≥10）	Neglected
		0.20≤D≤0.3	N≤5，(distance≥10）	
		D＞0.3	Not allowed	
		Notes：1、The circular defects which can be removed is ignored. 2、The circular defects of B area should not affect to assembly,functionality or final look of the product. 3, The circular defects of A area does not include the protective film, TThe circular defects of B area does not include the adhesive tapes.		
2	Linear Defects(Scratch Line\Foreign material)	W(width)/L(length)		Judgment
		W≤0.10，L≤3.0		Neglected(distance≥10）
		0. 10＜W≤0.20，L≤3.0		N≤3，(distance≥10）
		W＞0.2，L＞3.0		Not allowed
		Notes：The foreign meterial which can be removed is ignored.		
3	Dent	Defect Size	Judgment（A Area）	Judgment（B Area）
		D≤0.15	Neglected(distance≥10）	Neglected
		0.15≤D≤0.3	N≤3, (distance≥10）	
		D＞0.3	NG	
		Notes：1、The foreign meterial which can be removed is ignored. 2、The foreign meterial of B area should not affect to assembly,functionality or final look of the product.		

| 4	Dirt/Fingerprint/ Smokes/Snake/Rainbow effect	A area：not allowed； B area：neglected（Not affect to assembly,functionality or final look of the product.）
5	Printing ink color	The printing ink color should be consistent with design drawing. (or client standard sample).
6	Printing ink color difference	1、 The judgement area is the front non-translucent zone of the sample. 2、 Accept the same series ink color printing shift. 3、 Color difference of IR hole,light sensor hole is not inspected. 4、If there is customer's inspection criteria or sample,determining by customer's inspection criteria or standard sample.
7	Transmittance-rate （ IR hole\light sensor hole)	Meet design drawing.
8	Printing pin hole	Not allowed
9	Font / Logo	Font / Logo should be printed smooth, no jagged, shadow, penetration, wear and tear, displacement, disconnection and connection defects
10	Breakage on edge or corner	A area(front side)：not allowed B area(back side)：X≤0.2,Y≤0.2,Z≤1/5T; N≤2，(distance≥20)，Neglected
11	Crack	Not allowed
12	Printing ink Edges burrs / Printing ink Zigzag	1、the front (back) printing ink edge of the touch window region : W1≤0.2，OK；W1＞0.2，NG。 2、the front (back) printing ink edge of the sample: W2≤0.3，OK；W2＞0.3，NG。
13	Foreign material of printing area	(1).The touch windows + 0～2.0mm：the criteria is same to Circular Defects； (2). The touch windows + 2.0 ～ 5.0mm ： D≤0.3 ，Neglected(distance≥5.0） (3).The other areas:D≤0.5，Neglected(distance≥5.0）
14	Circular Defects for LED hole/ IR hole/sensor hole	Sensor hole、LED hole：D≤0.1mm，N≤1，allowed ； D＞0.1mm, not allowed； IR hole：D≤0.1mm，neglected； D＞0.1mm，not allowed。
15	Bad cutting section for cover/sensor	1.Cutting section allow the wave-like phenomenon, but the cutting edge level of view must be a smooth line; 2, cutting section does not allow any cracks appearance.

16	Surface Dirt	1, the process dirt which can not wipe with alcohol is not allowed; 2, The dirty can be wiped with a clean cloth or with clean cloth &alcohol , and the dirt is less than 10% of the total area of the product, and the dirt is less than two points each piece of product, allowed.
17	FPC	1.The component soldering can not be cold soldering, short, open circuit, burrs, tin ball; 2.The shape of FPC can not been broken, died off; 3.FPC stiffener of the component area can not drain back paste or damaged; 4.FPC version number should be consistent with the design drawing;
18	Tape (foam / double-sided adhesive, etc.) Judgement	Tape attached should be consistent with the design drawing;, not missing , unbreakable, non-attached side.

■ PRECAUTIONS FOR USE OF CTP MODULES

1 Mounting Precaution

1.1 B e z e Mounting

When mounting the CTP underneath a bezel,the CTP assembly should be mounted using a configuration that supports the back surface of the TFT module.The bezel edge must be positioned outside the active area of the CTP.A gap of 0.5mm to 1.0mm is needed between the bezel and the CTP surface.A foam gasket or similar material should be used to compensate for the tolerance of the enclosure,compression for the screw,etc.

Bezel   Gaskets(typically 1.0mm)

Faceplate(optional)

Touch Panel

TFT Module

Bracket
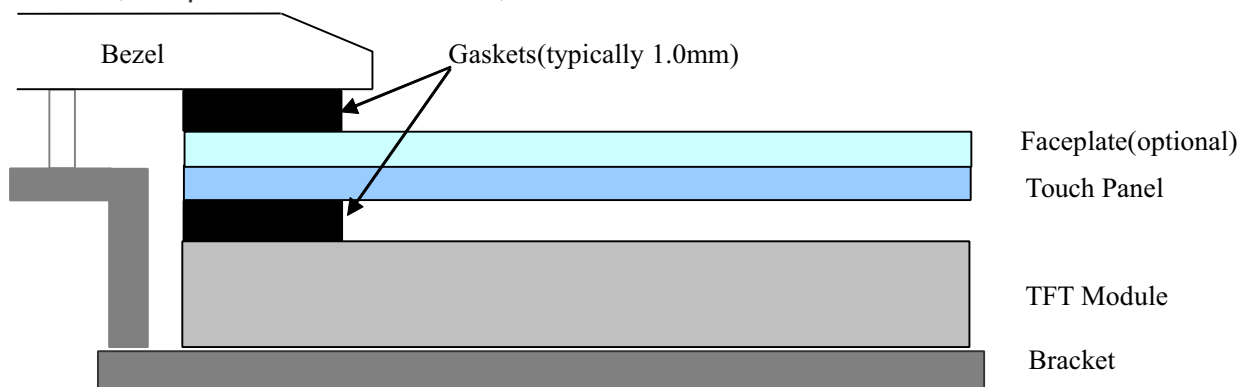
1.2 Flush Mounting

When flush mounting the faceplate with the top of the enclosure,the enclosure must have a ledge for attaching the overhang of the faceplate as well as a ledge for supporting the back of the TFT module.

Double Side Adhesive   Gaskets(typically 1.0mm)

Faceplate(optional)

Touch Panel

Enclosure

TFT Module
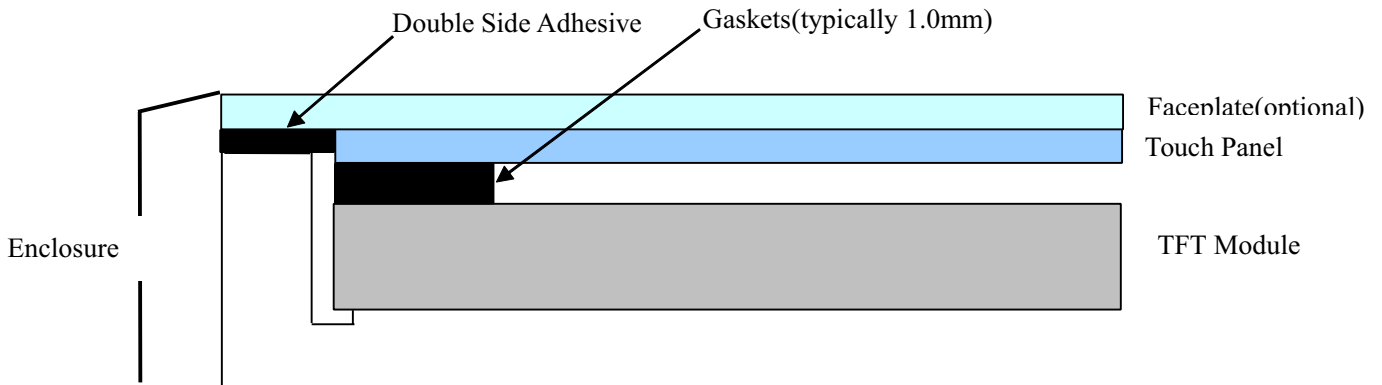
1.3 Optical Bonding

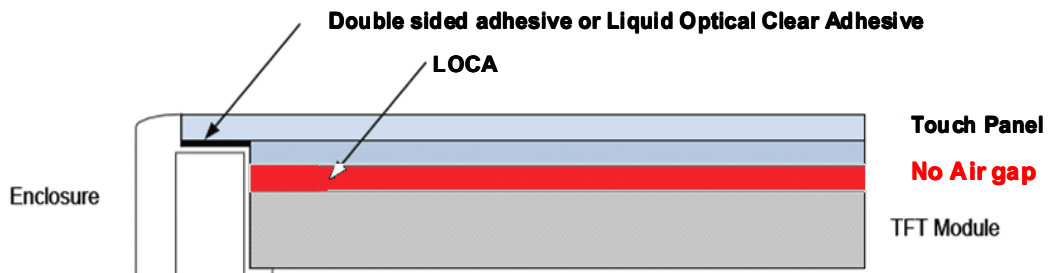The airgap between the TFT and CTP can be eliminated by using an optical bonding .Elimination of the air gap improves the electrical performance of the CTP and enhances the clarity of the TFT image.

Double sided adhesive or Liquid Optical Clear Adhesive

LOCA

Touch Panel

No Air gap

Enclosure

TFT Module

2 Handling Precautions

2.1 The product is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

2.2 Do not apply excessive force to the product since this may damage to the performance;

2.3 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

— Isopropyl alcohol
— Ethyl alcohol

Solvents other than those mentioned above may damage the product. Especially, do not use the following:

— Water
— Ketone
— Aromatic solvents

2.4 Do not attempt to disassemble the CTP Module.

2.5 If the logic circuit power is off, do not apply the input signals.

2.6 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

    a. Be sure to ground the body when handling theCTP Modules.

    b. Tools required for assembly, such as soldering irons, must be properly ground.

    c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

    d. The CTP Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

3 Storage precautions

3.1 When storing the CTP modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

3.2 The CTP modules should be stored under the storage temperature range. If the CTP modules will be stored for a long time, the recommend condition is:

    Temperature :   0℃ ～ 40℃

    Relatively humidity: ≤80%

3.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

4 notes

The CTP modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

■ OUTLINE DRAWING

CTP FRONT VIEW

detail A
scale 5:1

PIN 1

123.26±0.15(COVER O.D.)
118.00(SENSOR O.D.)
110.40(SENSOR A.A.)
109.00±0.15(COVER V.A.)
108.00(LCD A.A.)

80.06±0.15(COVER O.D.)
75.20(SENSOR O.D.)
67.20(SENSOR A.A.)
65.80±0.15(COVER V.A.)
64.80(LCD A.A.)
4.56
7.13
2.63
47.20±0.5
7.13
109.95±0.5

TOP PIN
REAR PIN
P.i Block C
5.00±0.3
5.50±0.05

p0.5±0.03
w0.3±0.03
2-0.5±0.1
p0.5*(10-1)=4.5±0.05
3.5±0.2
0.2

STIFFENER
CONTACT SIDE
1.0mm(Max.)
0.30±0.05

0.70±0.05(COVER)
0.15±0.05(OCR)
0.50±0.05(SENSOR)
1.35±0.15

CTP BACK VIEW

10
1
A
FR-4 (0.3mm)

NOTES:
1. UNMARKED TOLERANCE: ±0.2;
2. REQUIREMENTS ON ENVIRONMENTAL PROTECTION: RoHS.

PIN-DEFINE	
NO.	PIN
1	GND
2	RESET
3	VCC
4	INT
5	SCL
6	SDA
7	H_SYNC
8~10	NC

CUSTOMER APVL	CUSTOMER			
DRAWN		SCALE		DATE
DFTG CHK		UNIT		
ENGR CHK		mm		
APPROVAL				

Display Future Ltd.

TITLE			
	DF-SSC0509---M2		
MODEL			
DWG NO		PAGE	1/1