



MULTI-INNO TECHNOLOGY CO., LTD.

LCD MODULE SPECIFICATION

Model : MI320240Y-G-1

| | |
|---------------|-----|
| Revision | 1.0 |
| Engineering | |
| Date | |
| Our Reference | |



REVISION RECORD

| REV NO. | REV DATE | CONTENTS | REMARKS |
|---------|-----------|---------------|---------|
| 0.1 | 2010-2-12 | First Release | |
| 1.0 | 2010-5-3 | Full spec | |
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| WRITTEN BY | CHECKED BY | APPROVED BY |
|------------|------------|-------------|
| | | |

**■ GENERAL INFORMATION**

| Item | Contents | Unit |
|-------------------------------|-----------------------------------|-----------------|
| LCD type | FSTN/POSITIVE/TRANFLECTIVE | / |
| Recommended Viewing Direction | 6:00 | O' Clock |
| Module area (W × H×T) | 108.00×93.00×8.20 | mm ³ |
| Viewing area (W×H) | 102.00×75.50 | mm ² |
| Active area (W×H) | 95.97×71.97 | mm ² |
| Number of Dots | 320×240 | / |
| Pixel pitch (W × H) | 0.30×0.30 | mm ² |
| Driver IC | TR7707 | / |
| Interface Type | System Parallel /Serial interface | / |
| Input voltage | 3.0 | V |
| Module Power consumption | 461 | mw |
| Backlight Type | LED | / |

EXTERNAL DIMENSIONS

NOTES:

1. DISPLAY TYPE:
Main LCD: FSTN/Positive/Transflective
2. Controller IC: TR7707
3. OPERATING TEMP: -20°C~70°C
4. STORAGE TEMP: -30°C~80°C
5. DUTY: 1/240 BIAS: 1/16, VCC: 3.0V, Vop: 23.0V
6. RECOMMEND CONNECTOR: FD-0520-28-FRSL(##)
7. BACKLIGHT: SIDE LIGHT, 6LEDS, WHITE
8. RDS COMPLIANT

SECTION B-B

SECTION A-A

PCB LEVEL

BACKLIGHT CIRCUIT DIAGRAM
5.1V / 90mA (TYP)

PART NUMBER 43-023003
CAVITY NUMBER

| PIN | NAME |
|-----|----------|
| 1 | GND |
| 2 | VDDT2 |
| 3 | Power_Sm |
| 4 | RS |
| 5 | RESETB |
| 6 | EMER_SCL |
| 7 | IM0 |
| 8 | IM1 |
| 9 | IM2 |
| 10 | CSB |
| 11 | VCC |
| 12 | VPP |
| 13 | DB0 |
| 14 | DB1 |
| 15 | DB2 |
| 16 | DB3 |
| 17 | DB4 |
| 18 | DB5 |
| 19 | DB6 |
| 20 | DB7 |
| 21 | DB8 |
| 22 | DB9 |
| 23 | DB10 |
| 24 | DB11 |
| 25 | DB12 |
| 26 | DB13 |
| 27 | DB14 |
| 28 | DB15 |

| | |
|---------------|----------|
| CUSTOMER APPL | CUSTOMER |
| DRAWN | SCALE |
| DTG CHK | UNIT |
| ENGR CHK | mm |
| APPROVAL | MODEL |

| | |
|------|---------------|
| DATE | TITLE |
| | MI320240Y-G-1 |
| | MODEL |

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|--------------------------------|
| MULTI-INNO TECHNOLOGY CO.,LTD. |
| DWG NO |

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| PAGE |
| 1/1 |

**■ ABSOLUTE MAXIMUM RATINGS**

| Parameter | Symbol | Min | Max | Unit |
|--------------------------|--------|------|---------------|------|
| Supply voltage for logic | VCC | -0.3 | 8.0 | V |
| Operating temperature | Top | -20 | 70 | °C |
| Storage temperature | TST | -30 | 80 | °C |
| Humidity | RH | - | 90%(Max60 °C) | RH |

■ ELECTRICAL CHARACTERISTICS**DC CHARACTERISTICS**

| Parameter | Symbol | Min | Typ | Max | Unit |
|--------------------------|-----------------|--------|-----|---------|------|
| Supply voltage for logic | VCC | 2.4 | 3.0 | 3.3 | V |
| Input Current | I _{dd} | - | 0.3 | 0.75 | mA |
| Input voltage 'H' level | V _{IH} | 0.8VCC | - | VCC | V |
| Input voltage 'L' level | V _{IL} | GND | - | 0.2 VCC | V |
| Output voltage 'H' level | V _{OH} | 0.8VCC | - | VCC | V |
| Output voltage 'L' level | V _{OL} | GND | - | 0.2 VCC | V |

■ BACKLIGHT CHARACTERISTICS

| Item | Symbol | Min. | Typ. | Max. | Unit | Condition |
|-----------------|----------------|------|----------|------|-------------------|----------------------|
| Forward current | I _f | 60 | 90 | 102 | mA | V _f =5.1V |
| Luminance | L _v | 500 | 600 | 850 | cd/m ² | T _a =25°C |
| Number of LED | - | - | 6 | - | Piece | - |
| Connection mode | P | - | Parallel | - | - | - |

Using condition: constant voltage driving method V_f=5.1V (+/-10%).

■ ELECTRO-OPTICAL CHARACTERISTICS

| Item | Symbol | Condition | Min | Typ | Max | Unit | Remark | Note |
|----------------------|----------------|---|-----|-----|-----|-------------------|--------|------|
| Response time | Tr+ Tf | $\theta=0^\circ$ $\varnothing=0^\circ$ $T_a=25^\circ\text{C}$ | – | 648 | 972 | ms | Fig.1 | 4 |
| Contrast ratio | Cr | | 1.3 | 2.6 | – | --- | FIG 2. | 1 |
| Luminance uniformity | δ WHITE | | 70 | 84 | – | % | FIG 2. | 3 |
| Surface Luminance | Lv | | 110 | 138 | – | cd/m ² | FIG 2. | 2 |
| Viewing angle range | θ | $\varnothing = 90^\circ$ | 16 | 26 | – | deg | FIG 3. | 6 |
| | | $\varnothing = 270^\circ$ | 30 | 40 | – | deg | FIG 3. | |
| | | $\varnothing = 0^\circ$ | 22 | 32 | – | deg | FIG 3. | |
| | | $\varnothing = 180^\circ$ | 24 | 34 | – | deg | FIG 3. | |

Note1. Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.:

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P 1,P2, P 3,P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P 3,P4, P5)}}$$

Note2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

$$L_v = \text{Average Surface Luminance with all white pixels (P1, P2, P 3,P4, P5)}$$

Note3. The uniformity in surface luminance (δ WHITE) is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see FIG 2.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P}_1, \text{P}_2, \text{P}_3, \text{P}_4, \text{P}_5)}{\text{Maximum Surface Luminance with all white pixels (P}_1, \text{P}_2, \text{P}_3, \text{P}_4, \text{P}_5)}$$

Note4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1..

Note5. CIE (x, y) chromaticity ,The x,y value is determined by screen active area position 5. For more information see FIG 2.

Note6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

Note7. For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on TOPCON's BM-5 photo detector.

Note8. For TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1. The definition of Response Time

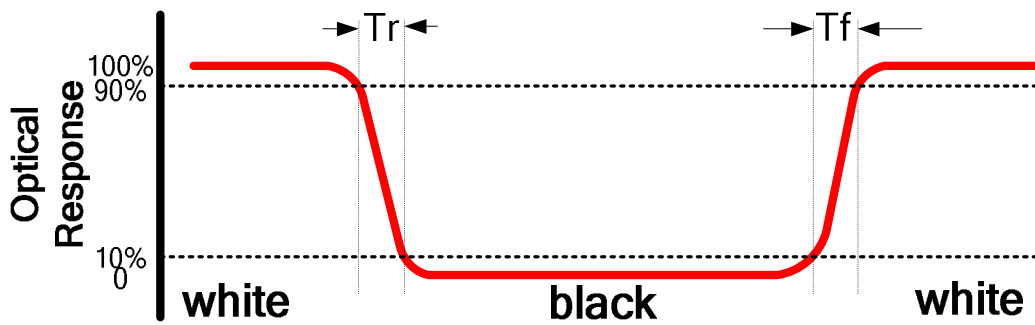


FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

A : 5 mm
 B : 5 mm
 H, V : Active Area
 Light spot size $\varnothing=5\text{mm}$, 500mm distance from the LCD surface to detector lens
 measurement instrument is TOPCON's luminance meter BM-5

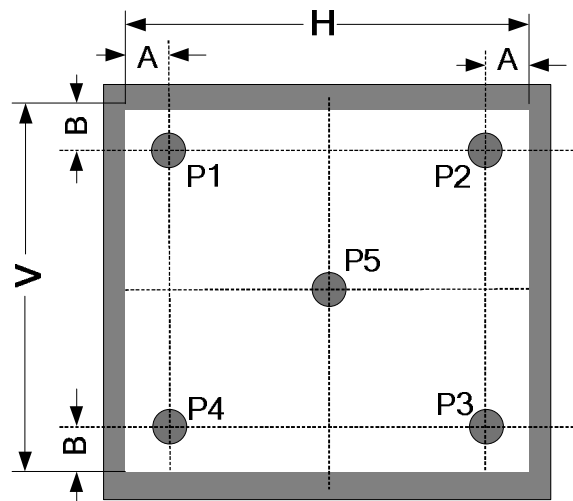
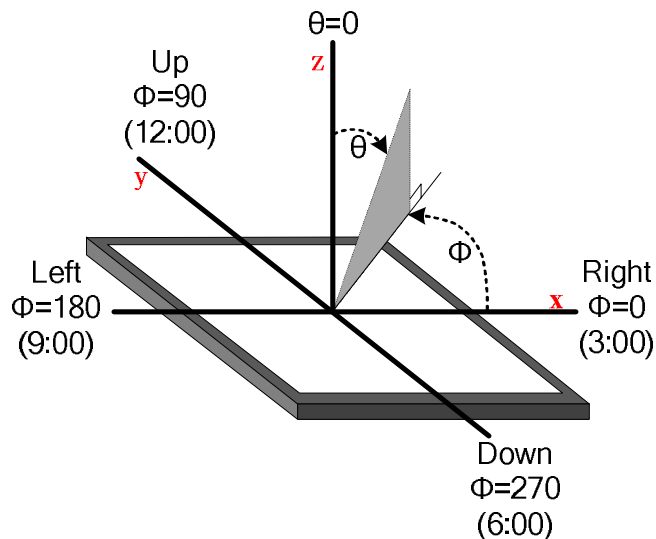


FIG.3. The definition of viewing angle



■ INTERFACE DESCRIPTION

| NO. | Symbol | I/O or connect to | DESCRIPTION | When not in use | | | | | | | | | | | | | | | | | | | | | | | | |
|-----|-----------|-------------------|--|--------------------------------|-----|-----|--------------------|-----|-----|-----|--------------------------------|-----|-----|-----|-------------------------------|-----|-----|-----|--------------------------------|-----|-----|-----|-------------------------------|-----|-----|-----|-------------------------|---|
| 1 | GND | Power supply | Ground | - | | | | | | | | | | | | | | | | | | | | | | | | |
| 2 | VOUT2 | Power supply | The internal booster output, step-up voltage based on the difference voltage of VCI & GND. (VOUT2<=32V). A external Voltage of 22.1v is needed for the module. | - | | | | | | | | | | | | | | | | | | | | | | | | |
| 3 | RWRDB_SDI | I Host processor | For a 68-system bus interface, serves as a signal to select data read/write operation. Low: Write High: Read For an 80-system bus interface, serves as a read strobe signal and reads data at the low level. For 4-line serial interface, serves as the serial data pin for data transformation. | - | | | | | | | | | | | | | | | | | | | | | | | | |
| 4 | RS | I Host processor | Register select Low: Index/status select High: Command select | - | | | | | | | | | | | | | | | | | | | | | | | | |
| 5 | RESETB | I Host processor | Reset pin, Low active. The chip must be reset after power-on. | - | | | | | | | | | | | | | | | | | | | | | | | | |
| 6 | EWRB_SCL | I Host processor | For 68-system bus interface, serves as an enable signal to activate data read/write operation. For 80-system bus interface, serves as a write strobe signal and writes data at the low level. For 4-line serial interface, serves as a synchronized clock signal. | - | | | | | | | | | | | | | | | | | | | | | | | | |
| 7 | IM0 | I | Selects the MPU interface mode <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>IM2</th> <th>IM1</th> <th>IM0</th> <th>MPU interface mode</th> </tr> </thead> <tbody> <tr> <td>GND</td> <td>GND</td> <td>GND</td> <td>68-system 16-bit bus interface</td> </tr> <tr> <td>GND</td> <td>GND</td> <td>VCC</td> <td>68-system 8-bit bus interface</td> </tr> <tr> <td>GND</td> <td>VCC</td> <td>GND</td> <td>80-system 16-bit bus interface</td> </tr> <tr> <td>GND</td> <td>VCC</td> <td>VCC</td> <td>80-system 8-bit bus interface</td> </tr> <tr> <td>VCC</td> <td>GND</td> <td>GND</td> <td>4-line serial interface</td> </tr> </tbody> </table> | IM2 | IM1 | IM0 | MPU interface mode | GND | GND | GND | 68-system 16-bit bus interface | GND | GND | VCC | 68-system 8-bit bus interface | GND | VCC | GND | 80-system 16-bit bus interface | GND | VCC | VCC | 80-system 8-bit bus interface | VCC | GND | GND | 4-line serial interface | - |
| IM2 | IM1 | | IM0 | MPU interface mode | | | | | | | | | | | | | | | | | | | | | | | | |
| GND | GND | | GND | 68-system 16-bit bus interface | | | | | | | | | | | | | | | | | | | | | | | | |
| GND | GND | | VCC | 68-system 8-bit bus interface | | | | | | | | | | | | | | | | | | | | | | | | |
| GND | VCC | GND | 80-system 16-bit bus interface | | | | | | | | | | | | | | | | | | | | | | | | | |
| GND | VCC | VCC | 80-system 8-bit bus interface | | | | | | | | | | | | | | | | | | | | | | | | | |
| VCC | GND | GND | 4-line serial interface | | | | | | | | | | | | | | | | | | | | | | | | | |
| 8 | IM1 | - | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 9 | IM2 | - | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 10 | CSB | I Host processor | Chip select; Low: chip enabled High: Chip disabled Must be fixed at VCC level when chip is not active. | VCC | | | | | | | | | | | | | | | | | | | | | | | | |
| 11 | VCC | Power supply | Power supply.AMP:2.4V~3.6V,TYPE:3.0V | - | | | | | | | | | | | | | | | | | | | | | | | | |



| | | | | |
|-------|----------|--------------------|---|------------|
| 12 | VPP | Power supply | VPP is the power pin of embedded OTP (One-Time-Programming)non-volatile memory circuit.Only during OTP programming cycle VPP should connect to an external power source (about 7.5V). On the other cases, just keep this pin open. (Please reserve ITO contact pad for OTP programming if using OTP function) | Open |
| 13~28 | DB[0:15] | I/O Host processor | Serves as a 16-bit bi-directional data bus. For an 8-bit bus interface, data transfer uses DB15-DB8, fix unused DB7~DB0 to the VCC or GND level. When in serial interface, fix unused DB15-DB0 to the VCC or GND level | VCC or GND |

■ REFERENCE APPLICATION CIRCUIT

Please consult our technical department for detail information.

■ RELIABILITY TEST CONDITIONS

| No. | Test Item | Test Condition | Inspection after test |
|-----|----------------------------|---|--|
| 1 | High Temperature Storage | $80 \pm 2^{\circ}\text{C}/200$ hours | Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 1.Air bubble in the LCD; 2.Sealleak; 3.Non-display; 4.missing segments; 5.Glass crack; 6.Current Idd is twice higher than initial value. |
| 2 | Low Temperature Storage | $-30 \pm 2^{\circ}\text{C}/200$ hours | |
| 3 | High Temperature Operating | $70 \pm 2^{\circ}\text{C}/120$ hours | |
| 4 | Low Temperature Operating | $-20 \pm 2^{\circ}\text{C}/120$ hours | |
| 5 | Temperature Cycle | $-20 \pm 2^{\circ}\text{C} \sim 25 \sim 70 \pm 2^{\circ}\text{C} \times 10$ cycles (30min.) (5min.) (30min.) | |
| 6 | Damp Proof Test | $50^{\circ}\text{C} \pm 5^{\circ}\text{C} \times 90\%RH/120$ hours | |
| 7 | Vibration Test | Frequency: 10Hz~55Hz~10Hz Amplitude: 1.5mm, X, Y, Z direction for total 3hours (Packing condition) | |
| 8 | Dropping test | Drop to the ground from 1m height, one time, every side of carton. (Packing condition) | |
| 9 | ESD test | Voltage: $\pm 8KV$ R: 330Ω C: 150pF Air discharge, 10time | |

Remark:

- 1.The test samples should be applied to only one test item.
- 2.Sample size for each test item is 5~10pcs.
- 3.For Damp Proof Test, Pure water(Resistance $>10M\Omega$) should be used.
- 4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
- 5.EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
- 6.Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

■ INSPECTION CRITERION

This specification is made to be used as the standard acceptance/rejection criteria for Normal LCM Product.

1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993, normal level 2 and based on:

Major defect: AQL 0.65

Minor defect: AQL 1.5

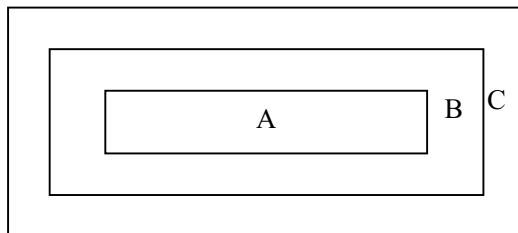
2. Inspection condition

- Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45° against perpendicular line. (Normal temperature 20~25°C and normal humidity 60±15%RH).

- Driving voltage

The Vop value from which the most optimal contrast can be obtained near the specified Vop in the specification (Within ±0.5V of the typical value at 25°C.).

3. Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD.

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

4. Inspection Standard

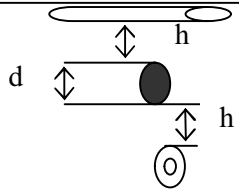
4.1 Major Defect

| Item No | Items to be inspected | Inspection Standard | Classification of defects |
|---------|------------------------|---|---------------------------|
| 4.1.1 | All functional defects | 1) No display 2) Display abnormally 3) Missing vertical, horizontal segment 4) Short circuit 5) Back-light no lighting, flickering and abnormal lighting. | Major |
| 4.1.2 | Missing | Missing component | |
| 4.1.3 | Outline dimension | Overall outline dimension beyond the drawing is not allowed. | |

4.2 Cosmetic Defect

4.2.1 Module Cosmetic Criteria

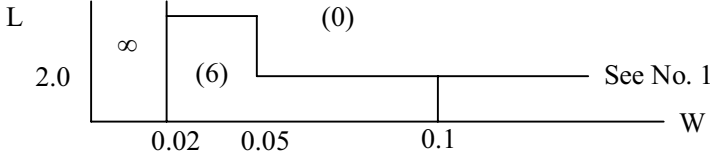
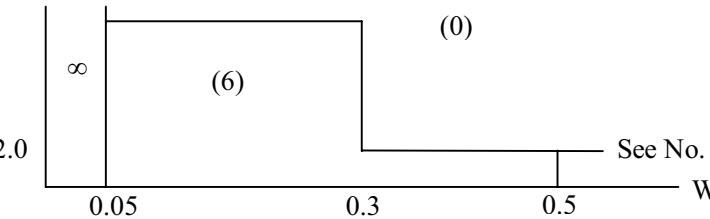
| No. | Item | Judgement Criterion | Partition |
|-----|---------------------------------------|---|-------------------------|
| 1 | Difference in Spec. | None allowed | Major |
| 2 | Pattern peeling | No substrate pattern peeling and floating | Major |
| 3 | Soldering defects | No soldering missing No soldering bridge No cold soldering | Major Major Minor |
| 4 | Resist flaw on Printed Circuit Boards | visible copper foil ($\varnothing 0.5\text{mm}$ or more) on substrate pattern | Minor |
| 5 | Accretion of metallic Foreign matter | No accretion of metallic foreign matters (Not exceed $\varnothing 0.2\text{mm}$) | Minor Minor |
| 6 | Stain | No stain to spoil cosmetic badly | Minor |
| 7 | Plate discoloring | No plate fading, rusting and discoloring | Minor |
| 8 | Solder amount | a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly. (too much) | Minor |
| | 1. Lead parts | b. Components side (In case of 'Through Hole PCB') Solder to reach the Components side of PCB. | |
| | 2. Flat packages | Either 'Toe' (A) or 'Seal' (B) of the lead to be covered by 'Filet'. Lead form to be assume over solder. | Minor |
| | 3. Chips | $(3/2) H \geq h \geq (1/2) H$ | Minor |

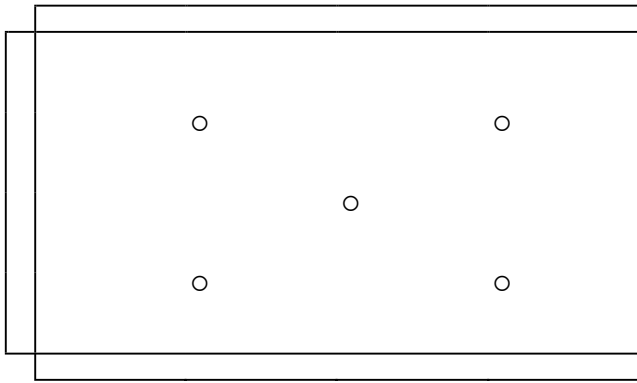
| | | | |
|---|---------------------------|---|--|
| 9 | Solder splash ball/Solder | <p>a. The spacing between solder ball and the conductor or solder pad $h \geq 0.13\text{mm}$ The diameter of solder ball $d \leq 0.15\text{mm}$.</p> <p>b. The quantity of solder balls or solder Splashes isn't beyond 5 in 600mm^2.</p> <p>c. Solder balls/Solder splashes do not violate minimum electrical clearance.</p> <p>d. Solder balls/Solder splashes must be entrapped/encapsulated Or attached to the metal surface.</p> <p>NOTE: Entrapped/encapsulated/attached is intended to mean that normal service environment of the product will not cause a solder ball to become dislodged.</p> |  <p>Minor</p> <p>Minor</p> <p>Major</p> <p>Minor</p> |
|---|---------------------------|---|--|

4.2.2Cosmetic Criteria (Non-Operating)

| No. | Defect | Judgment Criterion | Partition | | | | | | | | | | |
|--------------------|-------------------------------|--|-------------|-------------------------------|--------------|-----------|--------------------|---|--------------------|---|-----------|---|-------|
| 1 | Spots | In accordance with <i>Screen Cosmetic Criteria (Operating) No.1.</i> | Minor | | | | | | | | | | |
| 2 | Lines | In accordance with <i>Screen Cosmetic Criteria (Operating) No.2.</i> | Minor | | | | | | | | | | |
| 3 | Bubbles in polarizer | <table border="1" data-bbox="494 907 1268 1086"> <thead> <tr> <th>Size : d mm</th> <th>Acceptable Qty in active area</th> </tr> </thead> <tbody> <tr> <td>$d \leq 0.3$</td> <td>Disregard</td> </tr> <tr> <td>$0.3 < d \leq 1.0$</td> <td>3</td> </tr> <tr> <td>$1.0 < d \leq 1.5$</td> <td>1</td> </tr> <tr> <td>$1.5 < d$</td> <td>0</td> </tr> </tbody> </table> | Size : d mm | Acceptable Qty in active area | $d \leq 0.3$ | Disregard | $0.3 < d \leq 1.0$ | 3 | $1.0 < d \leq 1.5$ | 1 | $1.5 < d$ | 0 | Minor |
| Size : d mm | Acceptable Qty in active area | | | | | | | | | | | | |
| $d \leq 0.3$ | Disregard | | | | | | | | | | | | |
| $0.3 < d \leq 1.0$ | 3 | | | | | | | | | | | | |
| $1.0 < d \leq 1.5$ | 1 | | | | | | | | | | | | |
| $1.5 < d$ | 0 | | | | | | | | | | | | |
| 4 | Scratch | In accordance with spots and lines operating cosmetic criteria. When the light reflects on the panel surface, the scratches are not to be remarkable. | Minor | | | | | | | | | | |
| 5 | Allowable density | Above defects should be separated more than 30mm each other. | Minor | | | | | | | | | | |
| 6 | Coloration | Not to be noticeable coloration in the viewing area of the LCD panels. Back-lit type should be judged with back-lit on state only. | Minor | | | | | | | | | | |
| 7 | Contamination | Not to be noticeable. | Minor | | | | | | | | | | |

4.2.3 Cosmetic Criteria (Operating)

| No. | Defect | Judgment Criterion | Partition | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
|-----------------|---------------|--|-----------|-------------|-------------------------------|-----------------|---------|-----------|---------------|---|---------------|---|---------|---|-----------------|---------|-----------|---------------|----|---------------|---|---------|---|----------|-------------|-------------------------------|-----------------|---------|-----------|---------------|---|---------------|---|---------|---|-----------------|---------|-----------|---------------|----|---------------|---|---------------|---|---------|---|-------|
| 1 | Spots | <p>A) Clear</p> <table border="1" data-bbox="443 383 1278 712"> <thead> <tr> <th>Lcd size</th> <th>Size : d mm</th> <th>Acceptable Qty in active area</th> </tr> </thead> <tbody> <tr> <td rowspan="4">Lcd size ≤ 8.0'</td> <td>d ≤ 0.1</td> <td>Disregard</td> </tr> <tr> <td>0.1 < d ≤ 0.2</td> <td>6</td> </tr> <tr> <td>0.2 < d ≤ 0.3</td> <td>2</td> </tr> <tr> <td>0.3 < d</td> <td>0</td> </tr> <tr> <td rowspan="4">Lcd size > 8.0'</td> <td>d ≤ 0.1</td> <td>Disregard</td> </tr> <tr> <td>0.1 < d ≤ 0.3</td> <td>10</td> </tr> <tr> <td>0.3 < d ≤ 0.5</td> <td>5</td> </tr> <tr> <td>0.5 < d</td> <td>0</td> </tr> </tbody> </table> <p>Note : Including pin holes and defective dots which must be within one pixel size; Total defective point shall not exceed 6 pcs no more than 8 inch LCD and 10PCS for more than 8 inch LCD.</p> <p>B) Unclear</p> <table border="1" data-bbox="459 898 1289 1272"> <thead> <tr> <th>Lcd size</th> <th>Size : d mm</th> <th>Acceptable Qty in active area</th> </tr> </thead> <tbody> <tr> <td rowspan="4">Lcd size ≤ 8.0'</td> <td>d ≤ 0.2</td> <td>Disregard</td> </tr> <tr> <td>0.2 < d ≤ 0.5</td> <td>6</td> </tr> <tr> <td>0.5 < d ≤ 0.7</td> <td>2</td> </tr> <tr> <td>0.7 < d</td> <td>0</td> </tr> <tr> <td rowspan="5">Lcd size > 8.0'</td> <td>d ≤ 0.2</td> <td>Disregard</td> </tr> <tr> <td>0.2 < d ≤ 0.5</td> <td>10</td> </tr> <tr> <td>0.5 < d ≤ 0.7</td> <td>3</td> </tr> <tr> <td>0.7 < d ≤ 1.0</td> <td>1</td> </tr> <tr> <td>1.0 < d</td> <td>0</td> </tr> </tbody> </table> <p>Note : Total defective point shall not exceed 6 pcs for no more than 8 inch LCD and 10PCS for more than 8 inch LCD.</p> | Lcd size | Size : d mm | Acceptable Qty in active area | Lcd size ≤ 8.0' | d ≤ 0.1 | Disregard | 0.1 < d ≤ 0.2 | 6 | 0.2 < d ≤ 0.3 | 2 | 0.3 < d | 0 | Lcd size > 8.0' | d ≤ 0.1 | Disregard | 0.1 < d ≤ 0.3 | 10 | 0.3 < d ≤ 0.5 | 5 | 0.5 < d | 0 | Lcd size | Size : d mm | Acceptable Qty in active area | Lcd size ≤ 8.0' | d ≤ 0.2 | Disregard | 0.2 < d ≤ 0.5 | 6 | 0.5 < d ≤ 0.7 | 2 | 0.7 < d | 0 | Lcd size > 8.0' | d ≤ 0.2 | Disregard | 0.2 < d ≤ 0.5 | 10 | 0.5 < d ≤ 0.7 | 3 | 0.7 < d ≤ 1.0 | 1 | 1.0 < d | 0 | Minor |
| Lcd size | Size : d mm | Acceptable Qty in active area | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Lcd size ≤ 8.0' | d ≤ 0.1 | Disregard | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | 0.1 < d ≤ 0.2 | 6 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | 0.2 < d ≤ 0.3 | 2 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | 0.3 < d | 0 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Lcd size > 8.0' | d ≤ 0.1 | Disregard | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | 0.1 < d ≤ 0.3 | 10 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | 0.3 < d ≤ 0.5 | 5 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | 0.5 < d | 0 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Lcd size | Size : d mm | Acceptable Qty in active area | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Lcd size ≤ 8.0' | d ≤ 0.2 | Disregard | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | 0.2 < d ≤ 0.5 | 6 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | 0.5 < d ≤ 0.7 | 2 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | 0.7 < d | 0 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Lcd size > 8.0' | d ≤ 0.2 | Disregard | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | 0.2 < d ≤ 0.5 | 10 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | 0.5 < d ≤ 0.7 | 3 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | 0.7 < d ≤ 1.0 | 1 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | 1.0 < d | 0 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 2 | Lines | <p>A) Clear</p>  <p>Note : () - Acceptable Qty in active area L - Length (mm) W - Width (mm) ∞ - Disregard</p> <p>B) Unclear</p>  <p>‘Clear’ = The shade and size are not changed by V_{op}. ‘Unclear’ = The shade and size are changed by V_{op}.</p> | Minor | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

| | | | |
|---|--|---|-------|
| 3 | Rubbing line | Not to be noticeable. | Minor |
| 4 | Allowable density | Above defects should be separated more than 10mm each other. | Minor |
| 5 | Rainbow | Not to be noticeable. | Minor |
| 6 | Dot size | To be 95% ~ 105% of the dot size (Typ.) in drawing. Partial defects of each dot (ex. pin-hole) should be treated as 'Spot'. (see <i>Screen Cosmetic Criteria (Operating) No.1</i>) | Minor |
| 7 | Uneven brightness (only back-lit type module) | <p>Uneven brightness must be $B_{MAX} / B_{MIN} \leq 2$</p> <ul style="list-style-type: none"> - B_{MAX} : Max. value by measure in 5 points - B_{MIN} : Min. value by measure in 5 points <p>Divide active area into 4 vertically and horizontally. Measure 5 points shown in the following figure.</p>  <p style="text-align: center;">○ : Measuring points</p> | Minor |

Note :

- (1) Size : $d = (\text{long length} + \text{short length}) / 2$
- (2) The limit samples for each item have priority.
- (3) Complex defects are defined item by item, but if the numbers of defects are defined in above table, the total number should not exceed 10.
 - (4) In case of 'concentration', even the spots or the lines of 'disregarded' size should not allowed. Following three situations should be treated as 'concentration'.
 - 7 or over defects in circle of $\varnothing 5\text{mm}$.
 - 10 or over defects in circle of $\varnothing 10\text{mm}$.
 - 20 or over defects in circle of $\varnothing 20\text{mm}$.

■ PRECAUTIONS FOR USING LCD MODULES

1 Handling Precautions

- 1.1 The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- 1.2 If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- 1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- 1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in to contact with room temperature air.
- 1.5 If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcoholDo not scrub hard to avoid damaging the display surface.
- 1.6 Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solventsWipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contact with oil and fats.
- 1.7 Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- 1.8 Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- 1.9 Do not attempt to disassemble or process the LCD module.
- 1.10 NC terminal should be open. Do not connect anything.
- 1.11 If the logic circuit power is off, do not apply the input signals.
- 1.12 Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Before removing LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
 - Tools required for assembling, such as soldering irons, must be properly grounded. Make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
 - To reduce the amount of static electricity generated, do not conduct assembling

and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dry. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.

- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

1.13 Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- Do not alter, modify or change the shape of the tab on the metal frame.

- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.

- Do not damage or modify the pattern writing on the printed circuit board.

- Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.

- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.

- Do not drop, bend or twist the LCM.

2 Handling precaution for LCM

2.1 LCM is easy to be damaged. Please note below and be careful for handling.

2.2 Correct handling:

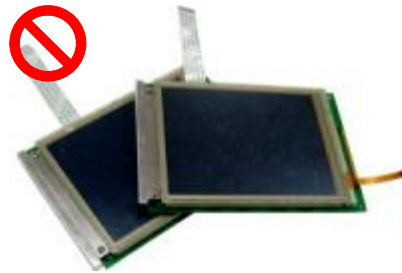


As above picture, please handle with anti-static gloves around LCM edges.

2.3 Incorrect handling:



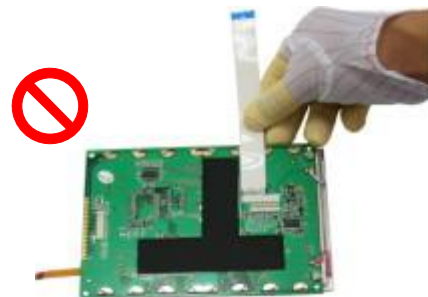
Please don't touch IC directly.



Please don't stack LCM.



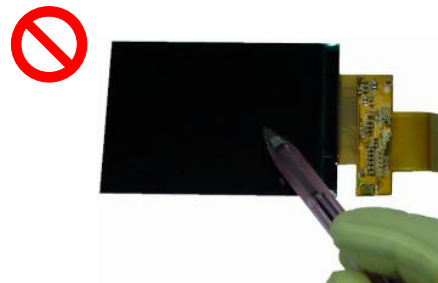
Please don't hold the surface of panel.



Please don't stretch interface of output, such as FPC cable.



Please don't hold the surface of IC.



Please don't operate with sharp stick such as pens.

3 Storage Precautions

- 3.1 When storing the LCD modules, the following precaution are necessary.
- 3.1.1 Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.
 - 3.1.2 Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
 - 3.1.3 The polarizer surface should not come in contact with any other objects (We advise you to store them in the anti-static electricity container in which they were shipped).
- 3.2 Others
- 3.2.1 Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.
 - 3.2.2 If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
 - 3.2.3 To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.
 - 3.2.3.1 - Exposed area of the printed circuit board.
 - 3.2.3.2 -Terminal electrode sections.

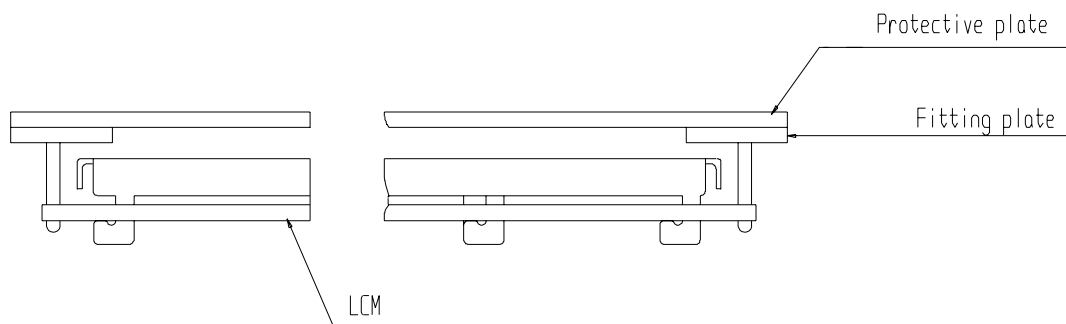
4 USING LCD MODULES

4.1 Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below.

Attend to the following items when installing the LCM.

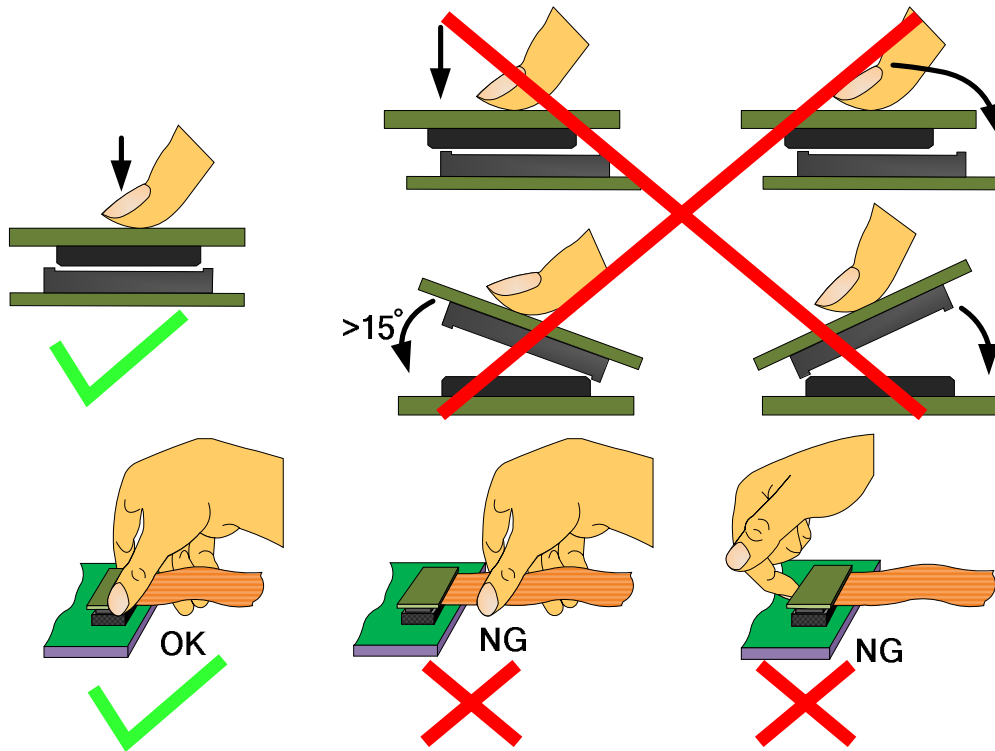
- 4.1.1 Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



- 4.1.2 When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be ± 0.1 mm.

4.2 Precaution for assemble the module with BTB connector:

Please note the position of the male and female connector position, don't assemble or assemble like the method which the following picture shows



4.3 Precaution for soldering the LCM

| | Manual soldering | Machine drag soldering | Machine press soldering |
|-----------------|-------------------------------|--------------------------------------|--|
| No RoHS Product | 290°C ~350°C. Time : 3-5S. | 330°C ~350°C. Speed : 15-17 mm/s. | 300°C ~330°C. Time : 3-6S. Press: 0.8~1.2Mpa |
| RoHS Product | 340°C ~370°C. Time : 3-5S. | 350°C ~370°C. Speed : 15-17 mm/s. | 330°C ~360°C. Time : 3-6S. Press: 0.8~1.2Mpa |

- 4.3.1 If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation (This does not apply in the case of a non-halogen type of flux). It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- 4.3.2 When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- 4.3.3 When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

4.4 Precautions for Operation

- 4.4.1 Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.
- 4.4.2 It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- 4.4.3 Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operating temperature.
- 4.4.4 If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- 4.4.5 A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50%RH or less is required.
- 4.4.6 Input logic voltage before apply analog high voltage such as LCD driving voltage when power on. Remove analog high voltage before logic voltage when power off the module. Input each signal after the positive/negative voltage becomes stable.
- 4.4.7 Please keep the temperature within the specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

4.5 Safety

- 4.5.1 It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- 4.5.2 If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

4.6 Limited Warranty

Unless agreed between Multi-Inno and the customer, Multi-Inno will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with Multi-Inno LCD acceptance standards (copies available upon request) for a period of one year from date of production. Cosmetic/visual defects must be returned to Multi-Inno within 90 days of shipment. Confirmation of such date shall be based on data code on product. The warranty liability of Multi-Inno limited to repair and/or replace on the terms set forth above. Multi-Inno will not be responsible for any subsequent or consequential events.

4.7 Return LCM under warranty

4.7.1 No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

4.7.1.1 - Broken LCD glass.

4.7.1.2 - PCB eyelet is damaged or modified.

4.7.1.3 -PCB conductors damaged.

4.7.1.4 - Circuit modified in any way, including addition of components.

4.7.1.5 - PCB tampered with by grinding, engraving or painting varnish.

4.7.1.6 - Soldering to or modifying the bezel in any manner.

4.7.2 Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet, conductors and terminals.

■ PACKING SPECIFICATION

Please consult our technical department for detail information.

■ PRIOR CONSULT MATTER

- 1 For Multi-Inno standard products, we keep the right to change material, process ... for improving the product property without prior notice to our customer.
- 2 For OEM products, if any changes are needed which may affect the product property, we will consult with our customer in advance.
- 3 If you have special requirement about reliability condition, please let us know before you start the test on our samples.