MULTI-INNO TECHNOLOGY CO., LTD.

www.multi-inno.com

LCD MODULE SPECIFICATION

Model : MI12048A-G

For Customer's Acceptance:

Customer	
Approved	
Comment	

Revision	1.0
Engineering	
Date	2012-01-08
Our Reference	

REVISION RECORD

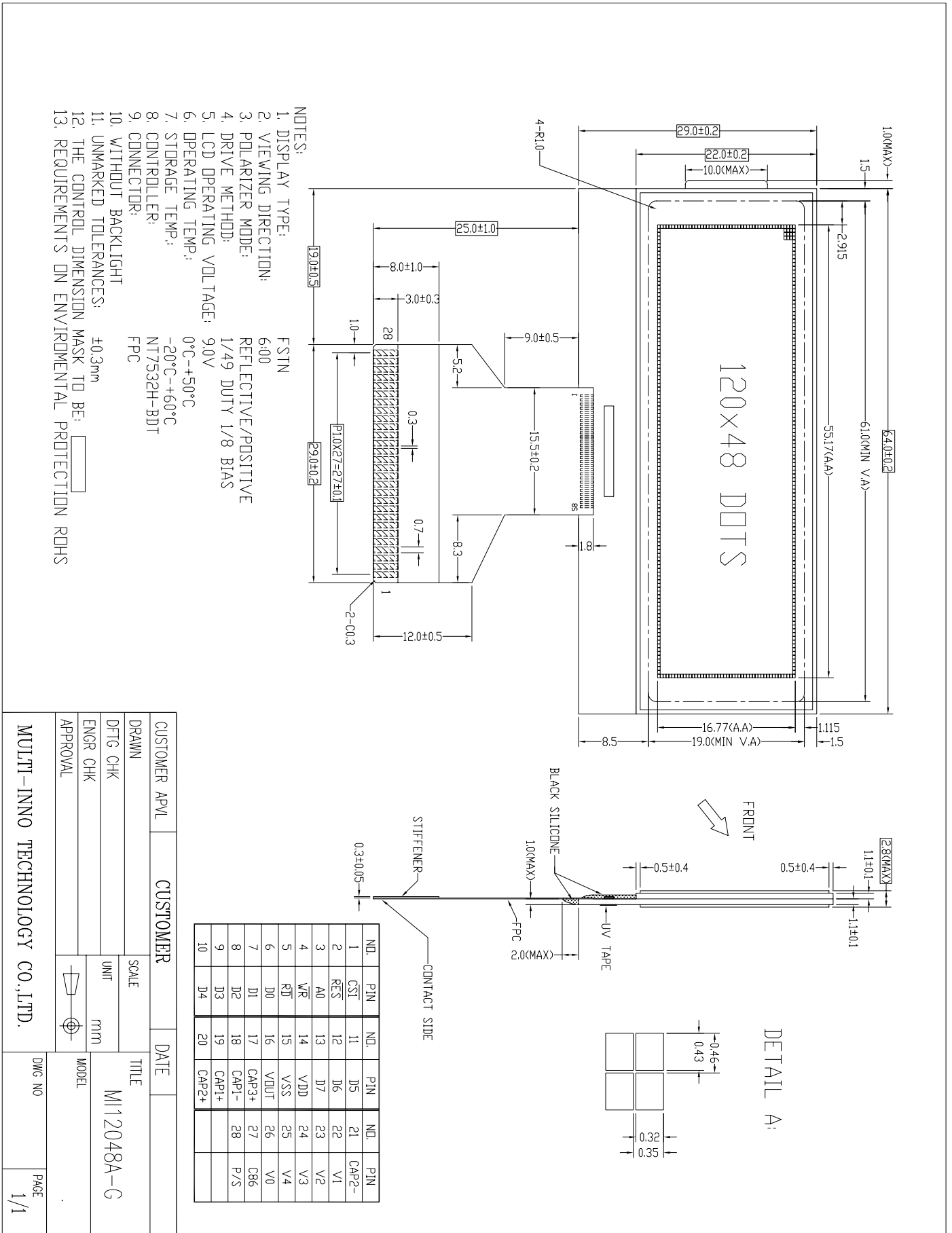| REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2012-01-08	First release	/

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■ **GENERAL INFORMATION**

Item of general information	Contents	Unit
LCD type	FSTN mode/Reflective/Positive	/
Recommended Viewing Direction	6:00	O' Clock
Module area (W × H)	64.00×29.00×2.80	mm^3
Viewing area (W×H)	61.00×19.00	mm^2
Active area (W×H)	55.17×16.77	mm^2
Number of Dots	120×48	/
Pixel pitch (W × H)	0.43×0.32	mm^2
Driver IC	NT7532H-BDT	/
Interface Type	8-bit Parallel,4-wrie ,SPI Interface	/
Input voltage	3.0	V
Module Power consumption	--	mw
Backlight Type	Without B/L	/

■ EXTERNAL DIMENSIONS

NOTES:
1. DISPLAY TYPE: FSTN
2. VIEWING DIRECTION: 6:00
3. POLARIZER MODE: REFLECTIVE/POSITIVE
4. DRIVE METHOD: 1/49 DUTY 1/8 BIAS
5. LCD OPERATING VOLTAGE: 9.0V
6. OPERATING TEMP.: 0°C~+50°C
7. STORAGE TEMP.: -20°C~+60°C
8. CONTROLLER: NT7532H-BDT
9. CONNECTOR: FPC
10. WITHOUT BACKLIGHT
11. UNMARKED TOLERANCES: ±0.3mm
12. THE CONTROL DIMENSION MASK TO BE:
13. REQUIREMENTS ON ENVIROMENTAL PROTECTION ROHS

120x48 DOTS

DETAIL A:

FRONT

STIFFENER
CONTACT SIDE
BLACK SILICONE
FPC
UV TAPE

NO.	PIN	NO.	PIN	NO.	PIN	NO.	PIN
1	CS1	11	D5	21	CAP2-		
2	RES	12	D6	22	V1		
3	A0	13	D7	23	V2		
4	WR	14	VDD	24	V3		
5	RD	15	VSS	25	V4		
6	D0	16	VOUT	26	V0		
7	D1	17	CAP3+	27	C86		
8	D2	18	CAP1-	28	P/S		
9	D3	19	CAP1+				
10	D4	20	CAP2+				

MULTI-INNO TECHNOLOGY CO.,LTD.

CUSTOMER APVL
DRAWN
DFTG CHK
ENGR CHK
APPROVAL

CUSTOMER
SCALE
UNIT   mm
DATE
TITLE
MODEL   MI12048A-G
DWG NO
PAGE 1/1

■ **ABSOLUTE MAXIMUM RATINGS**

Parameter of absolute maximum ratings	Symbol	Min	Max	Unit
Supply voltage for logic	VDD	0.3	3.6	V
Input voltage	VIN	-	-	V
Operating temperature	Top	0	50	°C
Storage temperature	TST	-20	60	°C
Humidity	RH	-	90%(Max60 °C)	RH

■ **ELECTRICAL CHARACTERISTICS**

DC CHARACTERISTICS

Parameter of DC characteristics	Symbol	Min	Typ	Max	Unit
Supply voltage for logic	VDD	2.9	3.0	3.1	V
Input Current	Idd	-	-	-	mA
Input voltage 'H' level	VIH	0.8VDD	-	VDD	V
Input voltage 'L' level	VIL	VSS	-	0.2VDD	V
Output voltage 'H' level	VOH	0.8VDD	-	VDD	V
Output voltage 'L' level	VOL	VSS	-	0.2VDD	V

■ ELECTRO-OPTICAL CHARACTERISTICS

Item of electro-optical characteristics	Symbol	Condition	Min	Typ	Max	Unit	Remark	Note
Response time	Tr+ Tf	θ=0° ∅=0° Ta=25℃	–	479	719	ms	Fig.1	4
Contrast ratio	Cr		3	6	–	---	FIG 2.	1
Luminance uniformity	δ WHITE		-	-	–	%	-	-
Surface Luminance	Lv		-	-	–	cd/m²	-	
Viewing angle range	θ	∅ = 90°	70	80	–	deg	FIG 3.	6
		∅ = 270°	42	52	–	deg	FIG 3.	
		∅ = 0°	70	80	–	deg	FIG 3.	
		∅ = 180°	70	80	–	deg	FIG 3.	

Note1.   Contrast Ratio(CR) is defined mathematically by the following formula.   For more information see FIG 2.:

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P 1,P2, P 3,P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P 3,P4, P5)}}$$

Note2.   Surface luminance is the LCD surface   from the surface with all pixels displaying white. For more information see FIG 2.

$$Lv = \text{Average Surface Luminance with all white pixels   (P1, P2, P 3,P4, P5)}$$

Note3.   The uniformity in surface luminance（δ WHITE）is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance   of 5 points luminance.   For more information see FIG 2.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels } (P_1, P_2, P_3, P_4, P_5)}{\text{Maximum Surface Luminance with all white pixels } (P_1, P_2, P_3, P_4, P_5)}$$

Note4.   Response time is the time required for the display to transition from White to black(Rise Time, Tr)   and from black to white(Decay Time, Tf). For additional information see FIG 1..

Note5.   CIE (x, y) chromaticity ,The x,y value is determined by screen active area position 5.   For more information see FIG 2.

Note6.   Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the conrast ratio is greater than 10. The angles are   determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface.   For more information see FIG 3.

Note7.   For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE，   the testing data is base on TOPCON's BM-5 photo detector.

Note8.   For TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1.   The definition of Response Time
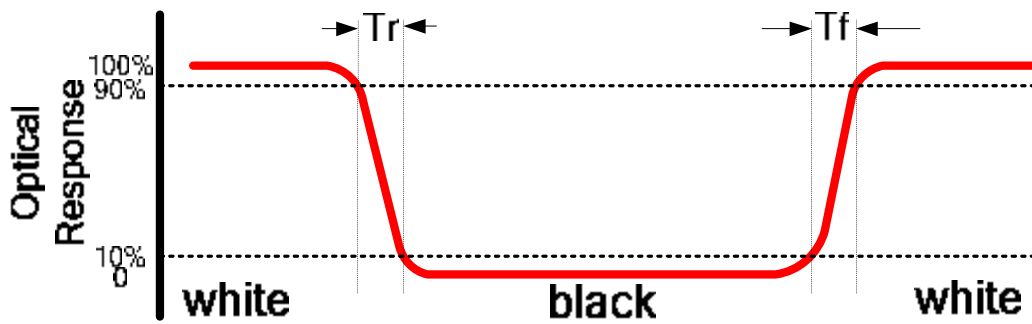
FIG.2.   Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

A : 5 mm
B : 5 mm
H,V : Active Area
Light spot size $\varnothing$=5mm, 500mm distance from the LCD surface to detector lens
measurement instrument is TOPCON's luminance meter BM-5
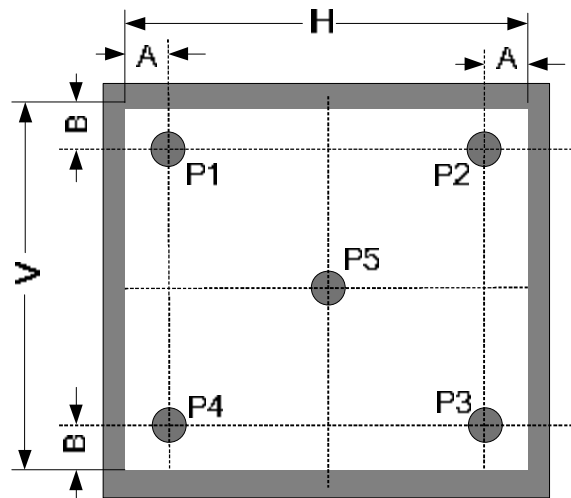
FIG.3.   The definition of viewing angle
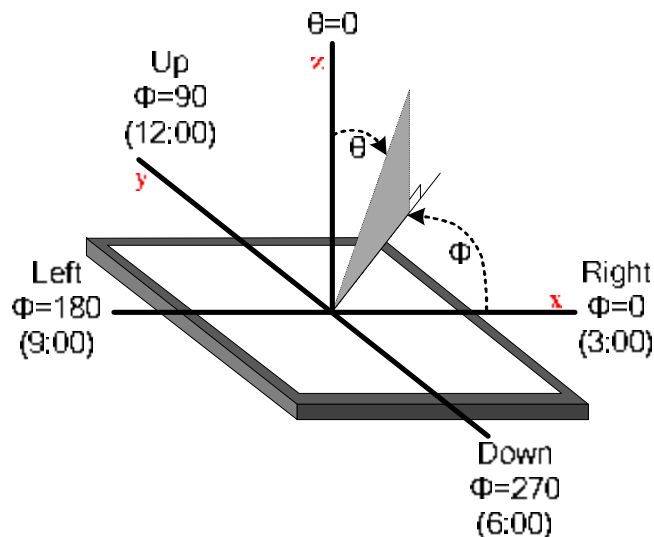
■ INTERFACE DESCRIPTION

Interface NO.	Symbol	I/O or connect to	Description	When not in use
1	$\overline{CS}1$	H/L	Chip selects signal.L:Active	/
2	$\overline{RES}$	H/L	Reset signal	/
3	A0	H/L	H: D0-D7 are display data L: D0-D7 are control data	/
4	$\overline{WR}$	I	Write execution control pin	/
5	$\overline{RD}$	I	Read execution control pin	/
6	D0	I/O	8 bit data bus (MLB)	/
7	D1	I/O	8 bit data bus	/
8	D2	I/O	8 bit data bus	/
9	D3	I/O	8 bit data bus	/
10	D4	I/O	8 bit data bus	/
11	D5	I/O	8 bit data bus	/
12	D6	I/O	8 bit data bus	/
13	D7	I/O	8 bit data bus (MSB)	/
14	VDD	P	Power supply	/
15	VSS	P	Ground	/
16	VOUT	-	DC/DC voltage converter output	/
17	CAP3+	-	Capacitor pin for voltage converter	/
18	CAP1-	-	Capacitor pin for voltage converter	/
19	CAP1+	-	Capacitor pin for voltage converter	/
20	CAP2+	-	Capacitor pin for voltage converter	/
21	CAP2-	-	Capacitor pin for voltage converter	/
22	V1	-	Power supply voltage for LCD	/
23	V2	-	Power supply voltage for LCD	/
24	V3	-	Power supply voltage for LCD	/
25	V4	-	Power supply voltage for LCD	/
26	V0	-	Power supply voltage for LCD	/
27	C86	-	MPU interface switch terminal,C86="H":6800 series MPU interface,C86="L":8080 MPU interface.	/
28	P/S	-	This is the parallel data input/serial data input switch termial,P/S="H" Parallel data input P/S="L" Serial data input	/

■ REFERENCE APPLICATION NOTES

1 Instruction Code & Timing characteristics

1.1 COMMAND

Command		A0	$\overline{RD}$	$\overline{WR}$	D7	D6	D5	D4	D3	D2	D1	D0	Function
(1)	Display ON/OFF	0	1	0	1	0	1	0	1	1	1	D	Turns on LCD panel when goes high, and turns off when goes low
(2)	Set Display Start Line	0	1	0	0	1	Display start address						Specifies RAM display line for COM0
(3)	Set Page Address	0	1	0	1	0	1	1	Page address				Sets the display RAM page in Page Address register
(4)	Set Column Address 4 higher bits	0	1	0	0	0	0	1	Higher column address				Sets 4 higher bits of column address of display RAM in register
(4)	Set column Address 4 lower bits	0	1	0	0	0	0	0	Lower column address				Sets 4 lower bits of column address of display RAM in register
(5)	Read Status	0	0	1	Status				0	0	0	0	Reads the status information
(6)	Write Display Data	1	1	0	Write data								Writes data in display RAM
(7)	Read Display Data	1	0	1	Read data								Reads data from display RAM
(8)	ADC select	0	1	0	1	0	1	0	0	0	0	D	Sets the display RAM address SEG output correspondence
(9)	Normal/Reverse Display	0	1	0	1	0	1	0	0	1	1	D	Normal indication when low, but full indication when high
(10)	Entire Display ON/OFF	0	1	0	1	0	1	0	0	1	0	0 / 1	Selects normal display (0) or Entire Display ON (1)
(11)	Set LCD Bias	0	1	0	1	0	1	0	0	0	1	D	Sets LCD drive voltage bias ratio
(12)	Read-Modify-Write	0	1	0	1	1	1	0	0	0	0	0	Increments Column Address counter during each write
(13)	End	0	1	0	1	1	1	0	1	1	1	0	Releases the Read-Modify-Write
(14)	Reset	0	1	0	1	1	1	0	0	0	1	0	Resets internal functions
(15)	Common output mode select	0	1	0	1	1	0	0	D	*	*	*	Selects COM output scan direction. * Invalid data
(16)	Set Power Control	0	1	0	0	0	1	0	1	Operation status			Selects the power circuit operation mode
(17)	V0 voltage regulator internal resistor ratio set	0	1	0	0	0	1	0	0	Resistor ratio			Select internal resistor ratio (Rb / Ra) mode
(18)	Electronic volume mode set	0	1	0	1	0	0	0	0	0	0	1	Set the V0 output voltage electronic volume register
	Electronic Volume Register set	0	1	0	*	*	Electronic control value						
(19)	Set static indicator On/Off	0	1	0	1	0	1	0	1	1	0	D	Set static indicator On/Off 0: OFF 1: ON
	Set Static indicator register	0	1	0	*	*	*	*	*	*	Mode		Set the flashing mode
(20)	Power Save	-	-	-	-	-	-	-	-	-	-	-	Compound command of display OFF and entire display ON
(21)	NOP	0	1	0	1	1	1	0	0	0	1	1	Command for non-operation
(22)	Test Command	0	1	0	1	1	1	1	*	*	*	*	IC Test command. Do not use!
(23)	Test Mode Reset	0	1	0	1	1	1	1	0	0	0	0	Command of test mode reset

Note: Do not use any other command, or the system malfunction may result.

1.2 Interface Timing characteristics

Note: Please refer to IC: _____NT7532H_____ data sheet for more details.

**System buses Read / Write characteristics 1 (For the 8080 Series MPU)**
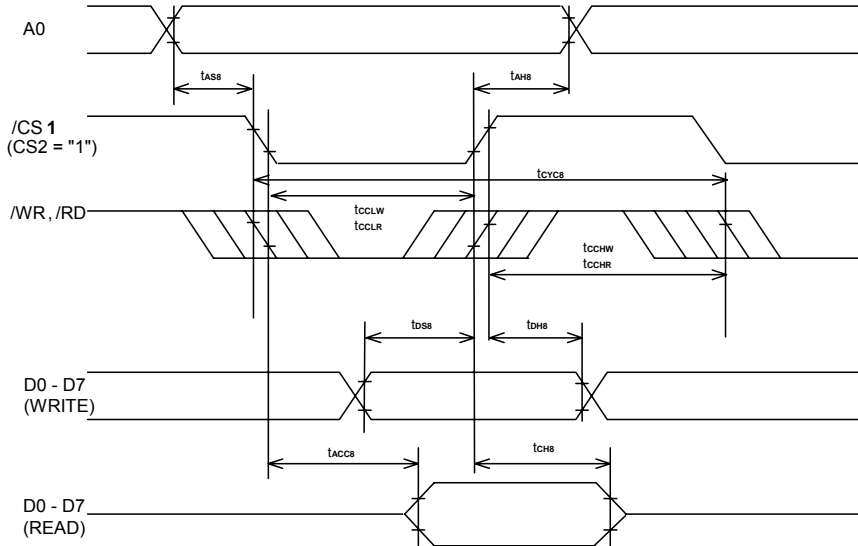
$(V_{DD} = 2.4 - 3.5V, T_A = -40 - 85°C)$

Symbol	Parameter	Min.	Typ.	Max.	Unit	Conditions
t$_{AH8}$	Address hold time	0			ns	
t$_{AS8}$	Address setup time	0			ns	
t$_{CYC8}$	System cycle time	300			ns	
t$_{CCLW}$	Control L pulse width (/WR)	90			ns	
t$_{CCLR}$	Control L pulse width (/RD)	120			ns	
t$_{CCHW}$	Control H pulse width (/WR)	120			ns	
t$_{CCHR}$	Control H pulse width (/RD)	60			ns	
t$_{DS8}$	Data setup time	40			ns	
t$_{DH8}$	Data hold time	15			ns	
t$_{ACC8}$	/RD access time			140	ns	C$_L$ = 100pF
t$_{CH8}$	Output disable time	10		100	ns	C$_L$ = 100pF

*1. The input signal rise time and fall time (tr, tf) is specified at 15ns or less. When the system cycle time is extremely fast, (tr+tf) ≤ (tCYC8-tCCLW-tCCHW) for (tr+tf) ≤ (tCYC8-tCCLR-tCCHR) are specified.

*2. All timing is specified using 20% and 80% of VDD as the reference.

*3. tCCLW and tCCLR are specified as the overlap between CS1 being "L" (CS2 = "H") and /WR and /RD being at the "L" level.

System buses Read/Write Characteristics 2 (6800 Series MPU)
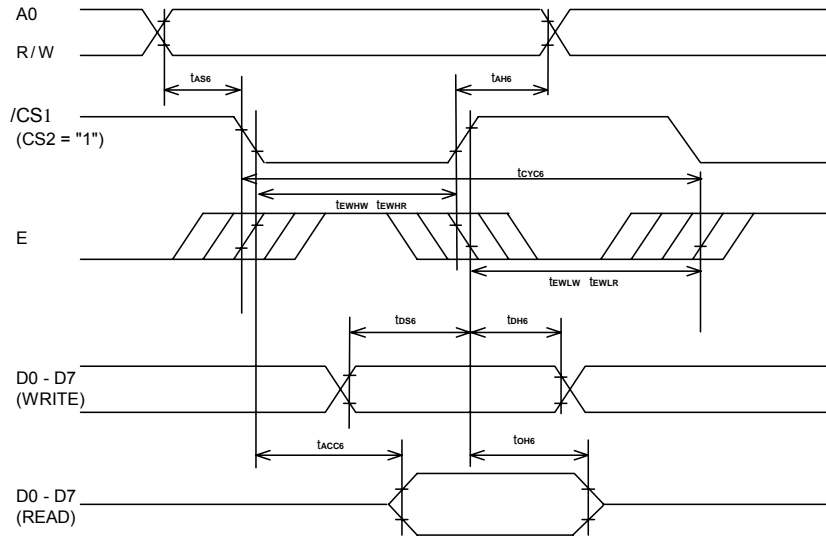
$(V_{DD} = 2.4 - 3.5V, TA = -40 - 85°C)$

Symbol	Parameter	Min.	Typ.	Max.	Unit	Conditions
t_{CYC6}	System cycle time	300			ns	
t_{AS6}	Address setup time	0			ns	
t_{AH6}	Address hold time	0			ns	
t_{DS6}	Data setup time	40			ns	
t_{DH6}	Data hold time	15			ns	
t_{OH6}	Output disable time	10		100	ns	$C_L = 100pF$
t_{ACC6}	Access time			140	ns	$C_L = 100pF$
t_{EWHR}	Enable H pulse width (Read)	120			ns	
t_{EWHW}	Enable H pulse width (Write)	90			ns	
t_{EWLR}	Enable L pulse width (Read)	60			ns	
t_{EWLW}	Enable L pulse width (Write)	120			ns	

*1. The input signal rise time and fall time (tr, tf) is specified at 15ns or less. When the system cycle time is extremely fast, $(tr+tf) \leq (t_{CYC6}-t_{EWLW}-t_{EWHW})$ for $(tr+tf) \leq (t_{CYC6}-t_{EWLR}-t_{EWHR})$ are specified.

*2. All timing is specified using 20% and 80% of V_{DD} as the reference.

*3. t_{EWLW} and t_{EWLR} are specified as the overlap between /CS1 being "L" (CS2 = "H") and E.

Serial interface
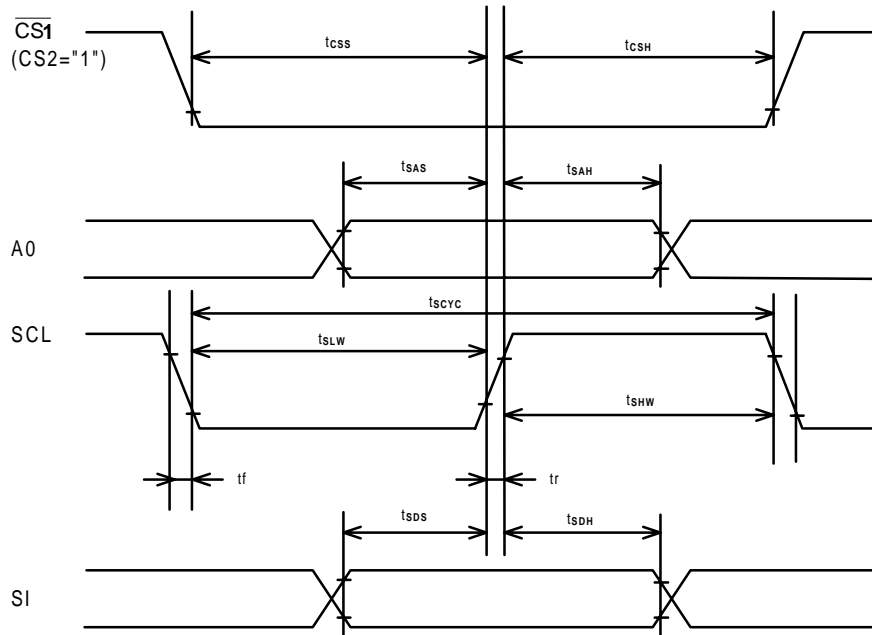
Symbol	Parameter	Min.	Typ.	Max.	Unit	Condition
T_{SCYC}	Serial clock cycle	250			nS	
T_{SHW}	Serial clock H pulse width	100			nS	
T_{SLW}	Serial clock L pulse width	100			nS	
T_{SAS}	Address setup time	150			nS	
T_{SAH}	Address hold time	150			nS	
T_{SDS}	Data setup time	100			nS	
T_{SDH}	Data hole time	100			nS	
T_{CSS}	$\overline{cs}$ serial clock time	150			nS	
T_{CSH}	$\overline{cs}$ serial clock time	150			nS	

■ RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition	Inspection after test
1	High Temperature Storage	60±2℃/200 hours	Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 1.Air bubble in the LCD; 2.Sealleak; 3.Non-display; 4.missing segments; 5.Glass crack; 6.Current Idd is twice higher than initial value.
2	Low Temperature Storage	-20±2℃/200 hours	
3	High Temperature Operating	50±2℃/120 hours	
4	Low Temperature Operating	0 ±2℃/120 hours	
5	Temperature Cycle	-20±2℃~25~60±2℃×10cycles (30min.)　(5min.)　(30min.)	
6	Damp Proof Test	50℃±5℃×90%RH/120 hours	
7	Vibration Test	Frequency：10Hz~55Hz~10Hz Amplitude：1.5mm, X，Y，Z direction for total 3hours (Packing condition)	
8	Dropping test	Drop to the ground from 1m height, one time, every side of carton. (Packing condition)	
9	ESD test	Voltage:±8KV　R: 330Ω　C: 150pF Air discharge, 10time	

Remark:

1.The test samples should be applied to only one test item.

2.Sample size for each test item is 5~10pcs.

3.For Damp Proof Test, Pure water(Resistance＞10MΩ) should be used.

4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.

5.EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.

6.Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

■ **INSPECTION CRITERION**

This specification is made to be used as the standard acceptance/rejection criteria for Normal LCM Product.

1 Sample plan

　Sampling plan according to GB/T2828.1-2003/ISO 2859-1：1999 and ANSI/ASQC Z1.4-1993, normal level 2 and based on:

　　　　Major defect: AQL 0.65

　　　Minor defect: AQL 1.5

2. Inspection condition

　●Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45°against perpendicular line. (Normal temperature 20~25°C and normal humidity 60±15%RH).

　　● Driving voltage

　The Vop value from which the most optimal contrast can be obtained near the specified Vop in the specification (Within ±0.5V of the typical value at 25°C.).

3. Definition of inspection zone in LCD.
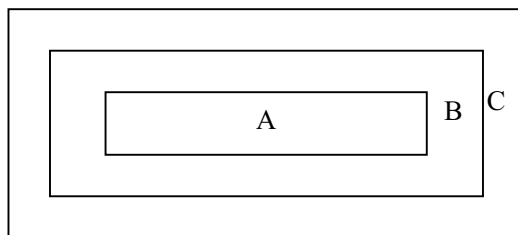
Zone A: character/Digit area

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD.

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.
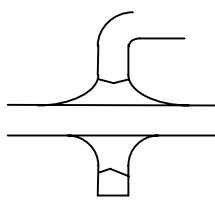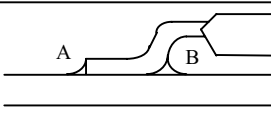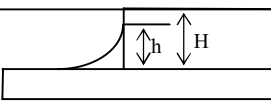
4.Inspection Standard
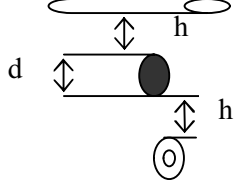
4.1 Major Defect

Item No	Items to be inspected	Inspection Standard	Classification of defects
4.1.1	All functional defects	1) No display 2) Display abnormally 3) Missing vertical，horizontal segment 4) Short circuit 5) Back-light no lighting, flickering and abnormal lighting.	Major
4.1.2	Missing	Missing component	
4.1.3	Outline dimension	Overall outline dimension beyond the drawing is not allowed.	

4.2 Cosmetic Defect
4.2.1 Module Cosmetic Criteria
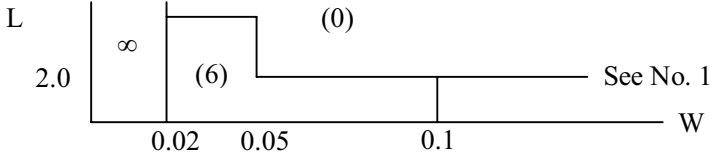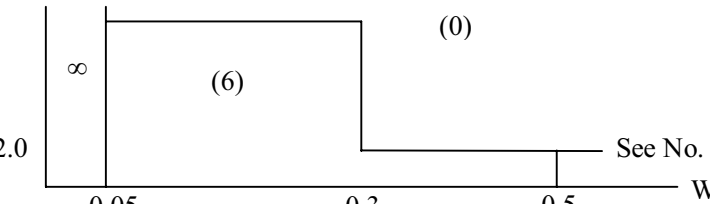
No.	Item	Judgement Criterion	Partition
1	Difference in Spec.	None allowed	Major
2	Pattern peeling	No substrate pattern peeling and floating	Major
3	Soldering defects	No soldering missing No soldering bridge No cold soldering	Major Major Minor
4	Resist flaw on Printed Circuit Boards	visible copper foil ($\varnothing$0.5mm or more) on substrate pattern	Minor
5	Accretion of metallic Foreign matter	No accretion of metallic foreign matters (Not exceed $\varnothing$0.2mm)	Minor Minor
6	Stain	No stain to spoil cosmetic badly	Minor
7	Plate discoloring	No plate fading, rusting and discoloring	Minor

8	Solder amount
	
	1. Lead parts
		a. Soldering side of PCB
		   Solder to form a 'Filet'
		   all around the lead.
		   Solder should not hide the
		   lead form perfectly. (too much)
		b. Components side
		   (In case of 'Through Hole PCB')
		
		Solder to reach the Components side of PCB.
			Minor

	2. Flat packages	Either 'Toe' (A) or 'Seal' (B) of the lead to be covered by 'Filet'. Lead form to be assume over solder.	Minor
	3. Chips	(3/2) H ≥ h ≥ (1/2) H	Minor
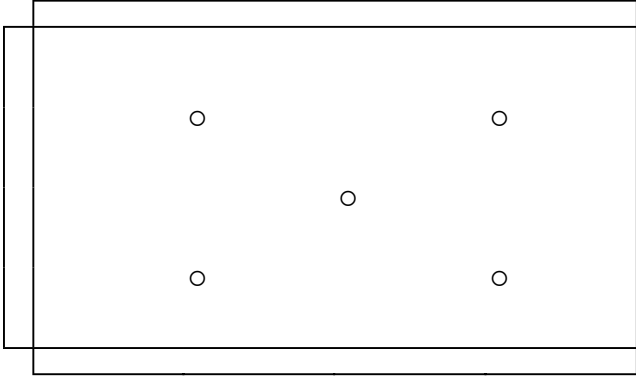
9	Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad　h ≥0.13mm. The diameter of solder ball d ≤0.15mm. b. The quantity of solder balls or solder Splashes isn't beyond 5 in 600 mm^2. c. Solder balls/Solder splashes do not violate minimum electrical clearance. 　d. Solder balls/Solder splashes must be entrapped/encapsulated Or attached to the metal surface. NOTE: Entrapped/encapsulated/attached is intended to mean that normal service environment of the product will not cause a solder ball to become dislodged.	Minor Minor Major Minor

4.2.2 Cosmetic Criteria (Non-Operating)

No.	Defect	Judgment Criterion	Partition
1	Spots	In accordance with *Screen Cosmetic Criteria (Operating) No.1*.	Minor
2	Lines	In accordance with *Screen Cosmetic Criteria (Operating) No.2*.	Minor
3	Bubbles in polarizer	<table><tr><td>Size : d mm</td><td>Acceptable Qty in active area</td></tr><tr><td>d ≤ 0.3</td><td>Disregard</td></tr><tr><td>0.3 < d ≤ 1.0</td><td>3</td></tr><tr><td>1.0 < d ≤ 1.5</td><td>1</td></tr><tr><td>1.5 < d</td><td>0</td></tr></table>	Minor
4	Scratch	In accordance with spots and lines operating cosmetic criteria. When the light reflects on the panel surface, the scratches are not to be remarkable.	Minor
5	Allowable density	Above defects should be separated more than 30mm each other.	Minor
6	Coloration	Not to be noticeable coloration in the viewing area of the LCD panels. Back-lit type should be judged with back-lit on state only.	Minor
7	Contamination	Not to be noticeable.	Minor

4.2.3   Cosmetic Criteria (Operating)

No.	Defect	Judgment Criterion	Partition
1	Spots	A) Clear 	Minor

A) Clear

Lcd size	Size : d mm	Acceptable Qty in active area
Lcd size≤8.0'	d≤0.1	Disregard
	0.1＜d≤0.2	6
	0.2＜d≤0.3	2
	0.3 < d	0
Lcd size＞8.0'	d ≤0.1	Disregard
	0.1＜d≤0.3	10
	0.3＜d≤0.5	5
	0.5 < d	0

Note :   Including pin holes and defective dots which must be within one pixel size; Total defective point shall not exceed 6 pcs no more than 8 inch LCD and 10PCS for more than 8 inch LCD.

B) Unclear

Lcd size	Size : d mm	Acceptable Qty in active area
Lcd size≤ 8.0'	d≤0.2	Disregard
	0.2＜d≤0.5	6
	0.5＜d≤0.7	2
	0.7＜d	0
Lcd size ＞8.0'	d≤0.2	Disregard
	0.2＜d≤0.5	10
	0.5＜d≤0.7	3
	0.7＜d≤1.0	1
	1.0< d	0

Note :   Total defective point shall not exceed 6 pcs for no more than 8 inch LCD and 10PCS for more than 8 inch LCD.

No.	Defect	Judgment Criterion	Partition
2	Lines	A) Clear	Minor

A) Clear


```
L
        ∞              (0)
2.0          (6)                      See No. 1
                                        W
     0.02   0.05        0.1
```

Note :   ( )   - Acceptable Qty in active area
      L   - Length (mm)
      W   - Width (mm)
      ∞   - Disregard

B) Unclear


```
L
        ∞                     (0)
              (6)
2.0                              See No. 1
                                    W
     0.05          0.3         0.5
```

'Clear'   = The shade and size are not changed by V_{op}.
'Unclear' = The shade and size are changed by V_{op}.

3	Rubbing line	Not to be noticeable.	Minor
4	Allowable density	Above defects should be separated more than 10mm each other.	Minor
5	Rainbow	Not to be noticeable.	Minor
6	Dot size	To be 95% ~ 105% of the dot size (Typ.) in drawing. Partial defects of each dot (ex. pin-hole) should be treated as 'Spot'. (see *Screen Cosmetic Criteria (Operating) No.1*)	Minor
7	Uneven brightness (only back-lit type module)	Uneven brightness must be BMAX / BMIN $\leq$ 2 - BMAX : Max. value by measure in 5 points - BMIN : Min. value by measure in 5 points Divide active area into 4 vertically and horizontally. Measure 5 points shown in the following figure. 〇 : Measuring points	Minor

Note :
(1) Size : d = (long length + short length) / 2
(2) The limit samples for each item have priority.
(3) Complex defects are defined item by item, but if the numbers of defects are defined in above table, the total number should not exceed 10.
(4) In case of 'concentration', even the spots or the lines of 'disregarded' size should not allowed. Following three situations should be treated as 'concentration'.
- 7 or over defects in circle of $\varnothing$5mm.
- 10 or over defects in circle of $\varnothing$10mm.
- 20 or over defects in circle of $\varnothing$20mm.

■ PRECAUTIONS FOR USING LCD MODULES

1   Handing Precautions

1.1   The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.

1.2   If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.

1.3   Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).

1.4   The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in to   contact with room temperature air.

1.5   If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
  - Isopropyl alcohol
  - Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

1.6   Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
  - Water
  - Ketone
  - Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contact with oil and fats.

1.7   Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.

1.8   Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.

1.9   Do not attempt to disassemble or process the LCD module.

1.10   NC terminal should be open. Do not connect anything.

1.11   If the logic circuit power is off, do not apply the input signals.

1.12   Electro-Static Discharge Control，Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
    - Before removing LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
    - Tools required for assembling, such as soldering irons, must be properly grounded. Make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
    - To reduce the amount of static electricity generated, do not conduct assembling

and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dry. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.

      - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

1.13 Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

      - Do not alter, modify or change the shape of the tab on the metal frame.

      - Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.

      - Do not damage or modify the pattern writing on the printed circuit board.

      - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.

      - Except for soldering the interface, do not make any alterations or modifications with a soldering iron.

      - Do not drop, bend or twist the LCM.

2   Handling precaution for LCM
  2.1   LCM is easy to be damaged. Please note below and be careful for handling.
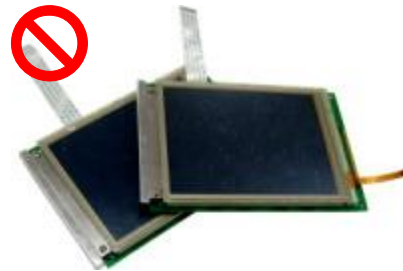 **2.2   Correct handling:**

As above picture, please handle with anti-static gloves around LCM edges.
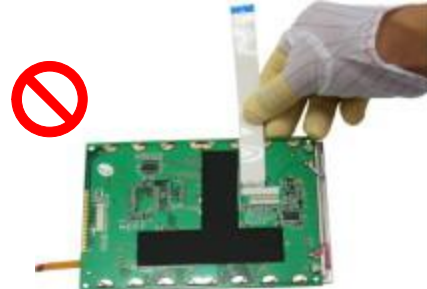
  2.3   Incorrect handling:

Please don't touch IC directly.

Please don't stack LCM.

Please don't hold the surface of panel.

Please don't stretch interface of output, such as FPC cable.

Please don't hold the surface of IC.

Please don't operate with sharp stick such as pens.

3   Storage Precautions

3.1   When storing the LCD modules, the following precaution are necessary.

3.1.1 Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.

3.1.2 Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.

3.1.3 The polarizer surface should not come in contact with any other objects (We advise you to store them in the anti-static electricity container in which they were shipped).

3.2   Others

3.2.1 Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

3.2.2 If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

3.2.3 To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

3.2.3.1 - Exposed area of the printed circuit board.

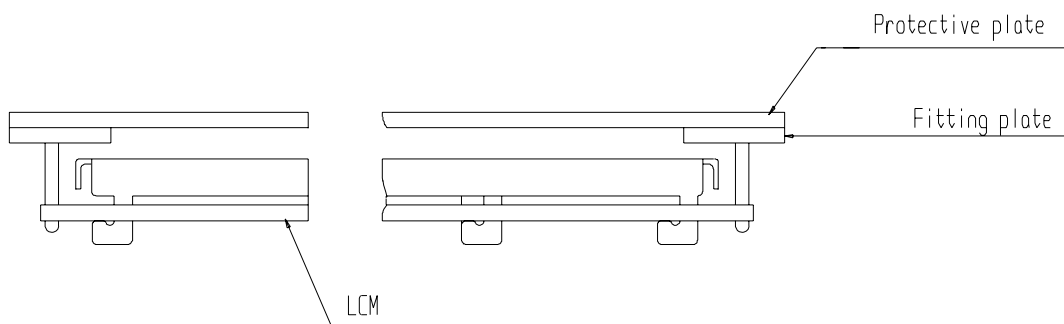3.2.3.2 -Terminal electrode sections.

4   USING LCD MODULES

4.1   Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

4.1.1 Cover the surface with a transparent protective plate to protect the polarizer and LC cell.

4.1.2 When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be ±0.1mm.

4.2   Precaution for assemble the module with BTB connector:
       Please note the position of the male and female connector position, don't assemble or
       assemble like the method which the following picture shows
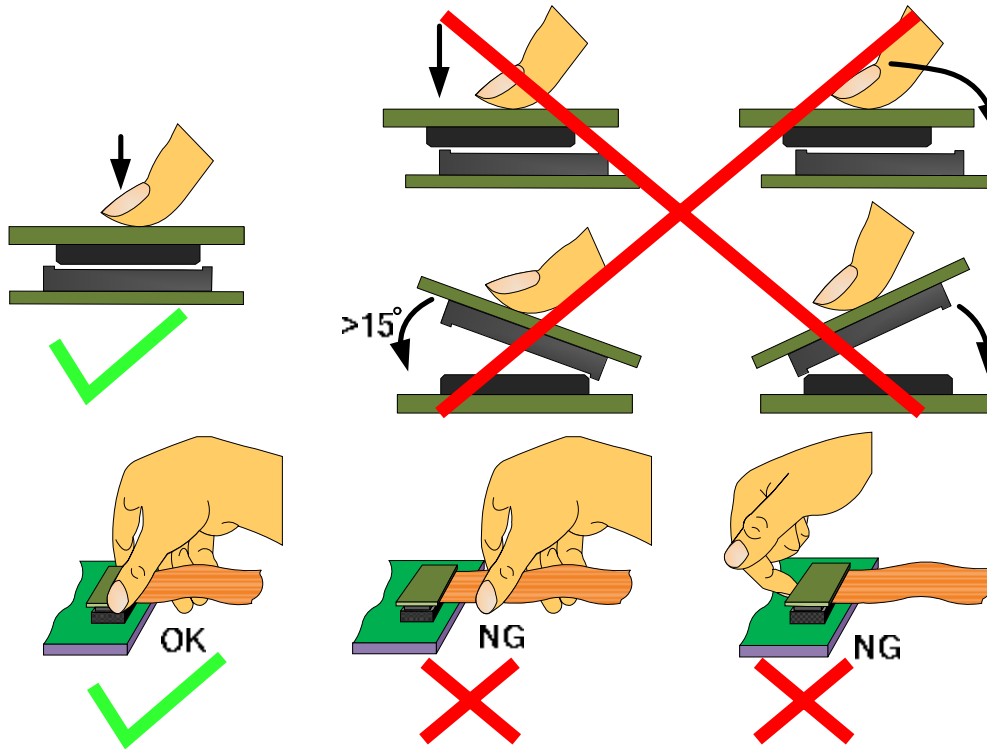
4.3   Precaution for soldering the LCM

	Manual soldering	Machine drag soldering	Machine press soldering
No RoHS Product	290°C ~350°C. Time : 3-5S.	330°C ~350°C. Speed : 15-17 mm/s.	300°C ~330°C. Time : 3-6S. Press: 0.8~1.2Mpa
RoHS Product	340°C ~370°C. Time : 3-5S.	350°C ~370°C. Speed : 15-17 mm/s.	330°C ~360°C. Time : 3-6S. Press: 0.8~1.2Mpa

4.3.1 If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation (This does not apply in the case of a non-halogen type of flux). It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.

4.3.2 When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.

4.3.3 When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

4.4   Precautions for Operation

4.4.1 Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.

4.4.2 It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.   An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.

4.4.3 Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them.   However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operating temperature.

4.4.4 If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.

4.4.5 A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50%RH or less is required.

4.4.6 Input logic voltage before apply analog high voltage such as LCD driving voltage when power on.   Remove analog high voltage before logic voltage when power off the module. Input each signal after the positive/negative voltage becomes stable.

4.4.7 Please keep the temperature within the specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

4.5   Safety

4.5.1 It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.

4.5.2 If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

4. 6   Limited Warranty

Unless agreed between Multi-Inno and the customer, Multi-Inno will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with Multi-Inno LCD acceptance standards (copies available upon request) for a period of one year from date of production. Cosmetic/visual defects must be returned to Multi-Inno within 90 days of shipment. Confirmation of such date shall be based on data code on product. The warranty liability of Multi-Inno limited to repair and/or replace on the terms set forth above. Multi-Inno will not be responsible for any subsequent or consequential events.

4.7   Return LCM under warranty

4.7.1 No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

4.7.1.1 - Broken LCD glass.

4.7.1.2 - PCB eyelet is damaged or modified.

4.7.1.3 -PCB conductors damaged.

4.7.1.4   - Circuit modified in any way, including addition of components.

4.7.1.5 - PCB tampered with by grinding, engraving or painting varnish.

4.7.1.6   - Soldering to or modifying the bezel in any manner.

4.7.2   Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet, conductors and terminals.

■ PACKING SPECIFICATION

Please consult our technical department for detail information.

■ PRIOR CONSULT MATTER

1   For Multi-Inno standard products, we keep the right to change material, process ... for improving the product property without prior notice to our customer.

2   For OEM products, if any changes are needed which may affect the product property, we will consult with our customer in advance.

3   If you have special requirement about reliability condition, please let us know before you start the test on our samples.