

# MULTI-INNO TECHNOLOGY CO., LTD.

# **LCD MODULE SPECIFICATION**

# Model : MI1602K1

Revision	1.3
Engineering	
Date	
Our Reference	

Address	: Room 10J,Xin HaoFang Building, No.188 Shennan Road,
	Nanshan Drstrict, ShenZhen, China.
Tel	: (86-755)2643 9937
Fax	: (86-755)8613 4241
Email	: sales@multi-inno.com
Web	: http://www.multi-inno.com



# **RECORDS OF REVISION**

Date	Ver.	Description	Page	Design by
2007/1/9	1.0	NEW SAMPLE Used white LED B/L Based		
2008/3/14	1.1	Used Gray LCD: D-SC00022B-01 Changed viewing Direction to 12 O'clock		
2009/4/12	1.2	Mass Production		
2010/8/14	1.3	Update Timing Characteristics and Display Command	12,14	



# Contents

# **1. SPECIFICATIONS**

- 1.1 Features
- **1.2 Mechanical Specifications**
- **1.3 Absolute Maximum Ratings**
- **1.4 DC Electrical Characteristics**
- 1.5 Optical Characteristics
- **1.6 Backlight Characteristics**
- **2. MODULE STRUCTURE** 
  - 2.1 Counter Drawing
  - 2.2 Interface Pin Description
  - **2.3** Timing Characteristics
  - 2.4 Display Command
  - 2.5 Character Pattern

# **3. QUALITY ASSURANCE SYSTEM**

- 3.1 Quality Assurance Flow Chart
- **3.2 Inspection Specification**

### 4. RELIABILITY TEST

4.1 Reliability Test Condition

# **5. PRECAUTION RELATING PRODUCT HANDLING**

- 5.1 Safety
- 5.2 Handling
- 5.3 Storage
- 5.4 Terms of Warranty

Note : For detailed information please refer to IC data sheet : ST7066U-0A-B, ST-7065C



### **1. SPECIFICATIONS**

### 1.1 Features

Item	Standard Value			
Display Type	16*2 Characters			
LCD Type	STN, Gray, Positive, Transflective, Normal Temp			
Driver Condition	LCD Module :1/16Duty, 1/5Bias			
Viewing Direction	12 O'clock			
Backlight	White LED B/L			
Weight	13g			
Interface	-			

# 1.2 Mechanical Specifications

Item	Standard Value	
Outline Dimension	53.0 (L)* 20.0 (W)*8.6 max.(H)	mm
Viewing Area	36.0(L)*10.0(W)	mm
Active Area	34.1(L)*7.4(W)	mm
Dot Size	0.33(L)* 0.35(W)	mm
Dot Pitch	0.38(L)* 0.40(W)	mm

Note : For detailed information please refer to LCM drawing

### **1.3 Absolute Maximum Ratings**

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	$V_{dd}$	_	-0.3	7.0	V
LCD Driver Supply Voltage	V <sub>LCD</sub>	_	VDD-10.0	VDD+0.3	V
Input Voltage	V <sub>IN</sub>	_	-0.3	V <sub>DD</sub> +0.3	V
Operating Temperature	T <sub>OP</sub>	_	0	50	°C
Storage Temperature	T <sub>ST</sub>	_	-20	70	°C
Storage Humidity	H <sub>D</sub>	Ta<40 °C	-	90	%RH

MULTI-INNO TECHNOLOGY CO.,LTD.



### **1.4 DC Electrical Characteristics**

		$V_{dd} = 4$	5.0±0.5V	, V <sub>SS</sub> =(	)V , Ta =	= 25°C
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	V <sub>dd</sub>	-	4.5	5.0	5.5	V
"H" Input Voltage	V <sub>IH</sub>	-	$0.7V_{DD}$	-	V <sub>DD</sub>	V
"L" Input Voltage	V <sub>IL</sub>	-	-0.3	-	0.6	V
"H" Output Voltage	V <sub>OH</sub>	IOH=-0.1mA	3.9	-	V <sub>DD</sub>	V
"L" Output Voltage	V <sub>OL</sub>	IOL=0.1mA	-	-	0.4	V
Surgly Current	т	VDD=5.0V;VOP=4.6V; Pattern= Full display	-	1.3	-	
Supply Current	I <sub>dd</sub>	V <sub>DD</sub> =5.0V;V <sub>OP</sub> =4.6V; Pattern= Horizontal line*1		1.5	3.0	- mA
		0°C	4.6	4.8	5.0	
LCM Driver Voltage	V <sub>OP</sub>	25°C*2	4.4	4.6	4.8	V
		50°C	4.2	4.4	4.6	

NOTE: \*1 The Maximum current display;

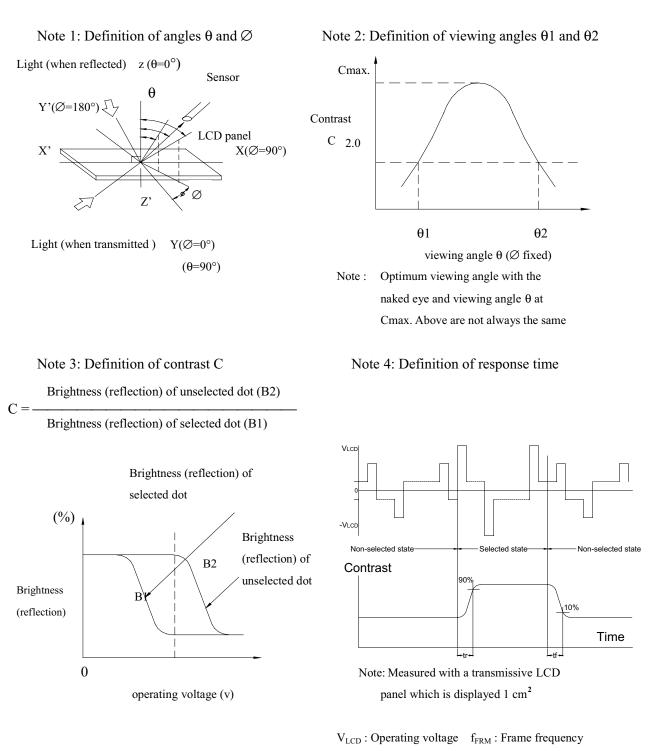
\*2 The VOP test point is VDD-VO.

# **1.5 Optical Characteristics**

LCD Panel: 1/16Duty , 1/5Bias ,  $V_{LCD}$  =4.67V , Ta =25 $^\circ\!\mathrm{C}$ 

Item	Symbol	Conditions	Min.	Тур.	Max.	Reference
View Angle	θ	$C \ge 2.0, \emptyset = 180^{\circ}$	0	-	$40^{\circ}$	Notes 1 & 2
Contrast Ratio	С	$\theta = -5, \emptyset = 180^{\circ}$	5	7	-	Note 3
Response Time(rise)	tr	$\theta = -5, \emptyset = 180^{\circ}$	-	150mS	-	Note 4
Response Time(fall)	tf	$\theta = -\mathfrak{H}, \emptyset = 180^{\circ}$	-	300mS	-	Note 4







# 1.6 Backlight Characteristics

### LCD Module with LED Backlight

### Maximum Ratings

Item	Symbol	Conditions	ТҮР	Unit
Forward Current	IF	Ta =25℃	40	mA
Reverse Voltage	VR	Ta =25℃	8	V
Power Dissipation	Pd	Ta =25℃	0.26	W

Electrical / Optical Characteristics

					Ta =	25℃
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF	IF= 40 mA	5.8	-	7.4	V
Reverse Current	IR	VR= 5 V	-	-	-	μA
Average Brightness (with LCD) *1	IV	IF=40 mA	35	55	-	cd/m <sup>2</sup>
Color of CIE Coordinate	Х	IF=40mA	0.26	0.30	0.34	
	Y	IF=40mA	0.26	0.30	0.34	
Uniformity *2	∆В	IF= 40mA	70	-	-	%
Color	White LED B/L					

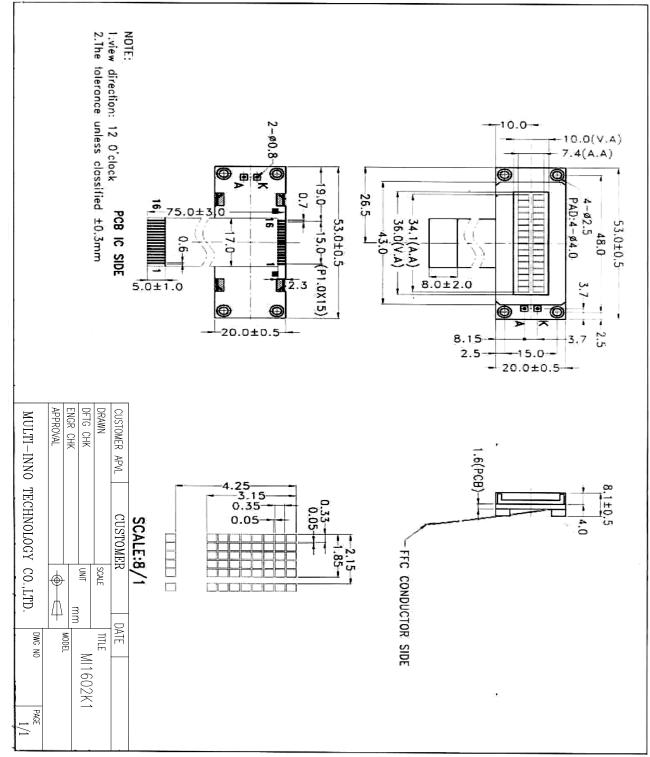
\*1 This vaule will be changed while mass production. \*2 :  $\triangle$ B=B(min) / B(max)



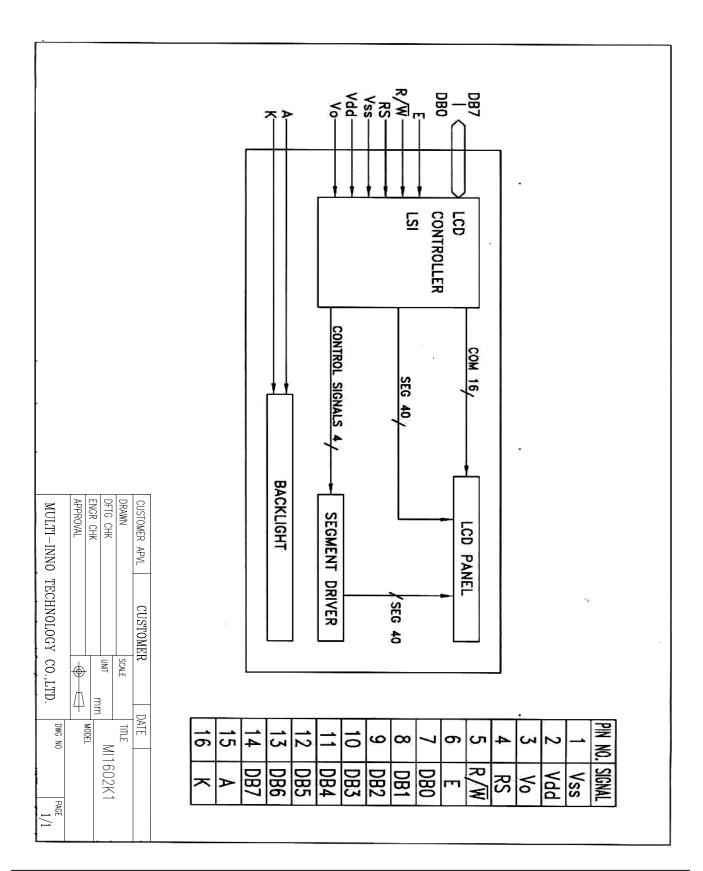
# Ver 1.3

# **2. MODULE STRUCTURE**

### 2.1 Counter Drawing





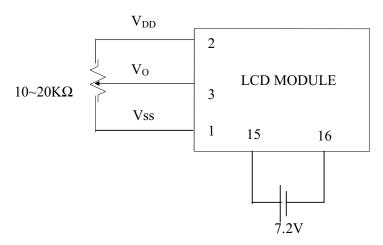




Pin No.	Symbol	Function
1	Vss	Signal ground (GND)
2	VDD	Power Supply for logic (VDD> Vss)
3	Vo	Operating Voltage for LCD (variable)
		Register selection input
4	RS	High = Data register
4	KS	Low = Instruction register (for write)
		Busy flag address counter (for read)
5		R/W signal input is used to select the read/write mode
3	R/W	High = Read mode, Low = Write mode
6	Е	Start enable signal to read or write the data
		Four low order bi-directional three-state data bus lines.
7-10	DB0 ~ DB3	Used
/-10	$DD0 \sim DD3$	for data transfer between the MPU and the LCD module.
		These four are not used during 4-bit operation.
		Four high order bi-directional three-state data bus lines.
11~14	DB4~DB7	Used for data transfer between the MPU and the LCD
11/~14	DD4/~DD7	module.
		DB7 can be used as a busy flag.
15	А	Power supply LED backlight (+)
16	K	Power supply LED backlight (-)

# 2.2 Interface Pin Description

Contrast Adjust

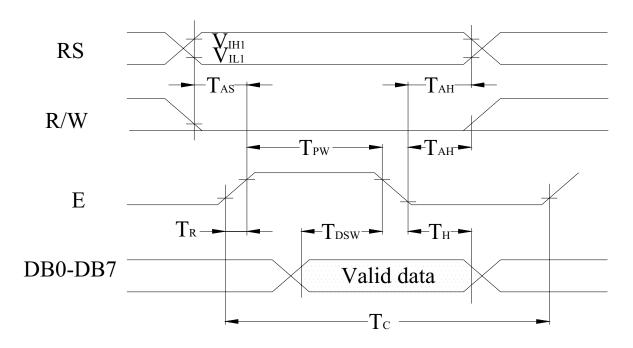




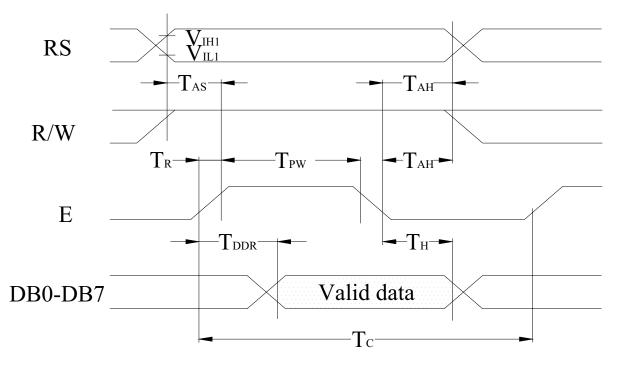
# Ver 1.3

# 2.3 Timing Characteristics

• Writing data from MPU to ST7066U



• Reading data from ST7066U to MPU



			,			
					(Vcc = -	-5V,Ta=25°C
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
T <sub>C</sub>	Enable Cycle Time	Pin E	1200	-	-	ns
$T_{PW}$	Enable Pulse Width	Pin E	140	-	-	ns
$T_R, T_F$	Enable Rise / Fall Time	Pin E	-	-	25	ns
$T_{AS}$	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
$T_{\rm AH}$	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
$T_{\text{DSW}}$	Data Setup Time	Pins:DB0~DB7	40	-	-	ns
$T_{\rm H}$	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

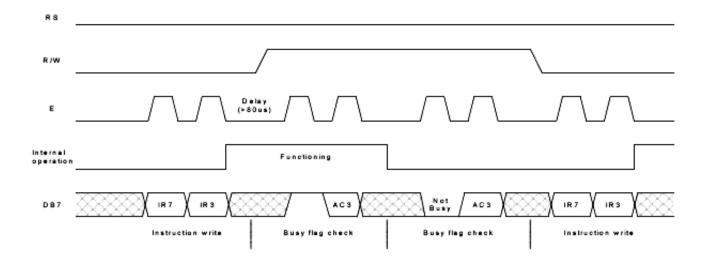
# • Write Mode (Writing data from MPU to ST7066U)

### • Read Mode (Reading data from ST7066U to MPU)

 $(Vcc = +5V, Ta = 25^{\circ}C)$ 

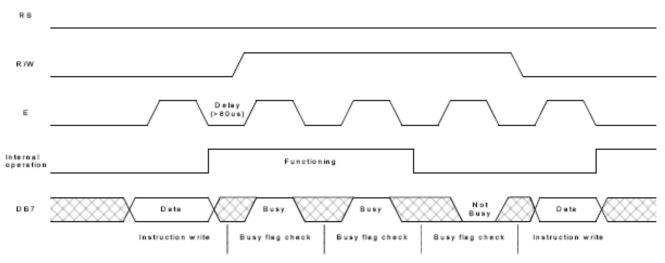
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
T <sub>C</sub>	Enable Cycle Time	Pin E	1200	-	-	ns
T <sub>PW</sub>	Enable Pulse Width	Pin E	140	-	-	ns
$T_R, T_F$	Enable Rise / Fall Time	Pin E	-	-	25	ns
T <sub>AS</sub>	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T <sub>AH</sub>	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T <sub>DDR</sub>	Data Setup Time	Pins:DB0~DB7	-	-	100	ns
T <sub>H</sub>	Data Hold Time	Pins:DB0~DB7	10	-	-	ns





Example of busy flag check timing sequence

For 8-bit interface date, all eight bus lines (DB0 to DB7) are used .



Example of busy flag check timing sequence



# 2.4 Display Command

					Instru	iction	Code					Description											
Instructions	ÞS	R/W	DB	DB	DB	DB	DB	DB	DB	DB	Description												
	КЗ	IV) W	7	6	5	4	3	2	1	0		(270KHz)											
Clear											Write "20H" to DDRAM. and set												
Display	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	DDRAM address to "00H" from	1.52ms
Display											AC.												
											Set DDRAM address to "00H"												
Return											from AC and return cursor to it's												
Home	0	0	0	0	0	0	0	0	1	×	original position if shifted.	1.52ms											
Tionic											The contents of DDRAM are not												
											changed.												
											Sets cursor move direction and												
Entry Mode	0	0	0	0 0	0	0	0	1	I/D	S	specifies display shift. These	37µs											
Set	0	U	0		0	0		1		6	operations are performed during												
											data write and read .												
Display											D=1 : entire display on												
ON/OFF	0	0	0	0	0	0	1	D	C	В	C=1 : cursor on	37µs											
											B=1 : cursor position on												
Cursor or											Set cursor moving and display												
	0	0	0	0	0	1	S/C	R/L			shift control bit, and the direction,	37µs											
Display Shift	0	0	0	0	0	1	5/C	K/L	×	×	without changing of DDRAM	57µ8											
SIIII											data.												
Function											DL: interface data is 8/4 bits												
	0	0	0	0	1	DL	Ν	F	×	×	NL: number of line is 2/1	37µs											
Set											F: font size is $5 \times 11/5 \times 8$												
Set					AC	AC	AC				Set CGRAM address in address												
CGRAM	0	0	0	1	AC 5	AC 4	AC 3					37µs											
Address					5	4	3	2	1	0	counter.												
Set	Set		Set DDRAM address in address																				
DDRAM	0	0	1	AC	AC 5	AC 4	AC 3	AC	AC			37µs											
Address				6	5	4	3	2	1	0	counter.												



Read Busy Flag and Address	0	1	BF	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0µs
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	37µs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	37µs

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Before checking BF, be sure to wait at least 80us.. Do not keep "E" always "High" for checking BF. Refer to Instruction Table for the list of each instruction execution time.



### 2.5 Character Pattern

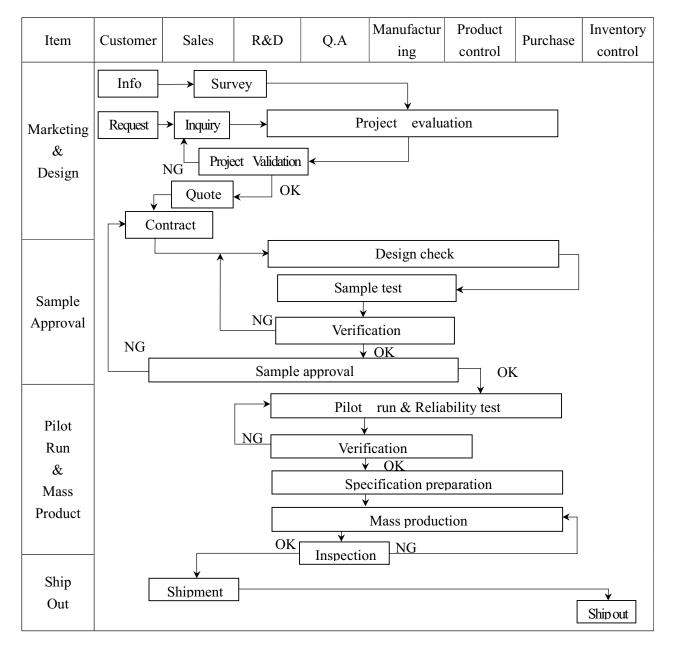
# CHARACTER PATTERN(SO/HO/EA,WA)

Lower 4 Bits	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
xxxx0000	CG RAM (1)			Ø	Ð	p		p::					-34	₩.	$\odot$	þ
xxxx0001	(2)		I	1	<b>  </b>	$\square$	-==1	-::				]P	Ţ.	£.,		
xxxx0010	(3)		•••			Pe	Ŀp	ŀ			I	·[	111	'	<b>p</b>	<b></b>
xxxx0011	(4)		#		<u> </u>	<u> </u>	: <u></u> .	: <b></b> .			I	7	7	1	: <b>::</b> -	•:-: <b>•</b>
xxxx0100	(5)		3	4		Τ	<u>.</u>	<u> </u>					ŀ.	12	<b> </b>	52
xxxx0101	(6)				<u> </u>	II	::::•	II				7	<u>-</u>		œ	<u>.</u>
xxxx0110	(7)		8	<b>6</b> .	[	IJ	- <b>F</b> <sup></sup> -	ıi				17	 		p	2
xxxx0111	(8)		7	7	G	Į, J	• <b>]</b> ]	II				=			<u> </u>	T
xxxx1000	(1)		Ć	8		3	ŀ"ı	:-:			1-		:	Ļ	-,I <sup></sup>	3
xxxx1001	(2)		]	9	Ι	<b>ا</b> ب <sup>1</sup>	i	'!				·'T	.!	I L.	1	ا <u></u> ا
xxxx1010	(3)		:-[-:	::	Т			<u>.</u>					•••	L	j	-
xxxx1011	(4)		]	3	ŀ€	Е	k	-			<b> 1</b> -	<u>.</u>	<u> </u>			]=1
xxxx1100	(5)		:=		l	4	1				- <b> </b> -:•	<u>.</u>	<u> </u>		<b>:</b>	<b>F</b>
xxxx1101	(6)				···	]]	[""]						••••		ŧ	- <u>-</u>
xxxx1110	(7)				ŀ]		ŀ~1	}				12		•••	F"ı	
xxxx1111	(8)		<sup></sup>					-42			• :_•	<b>!</b> !	·:"	<b>E</b> 1	<u> </u>	

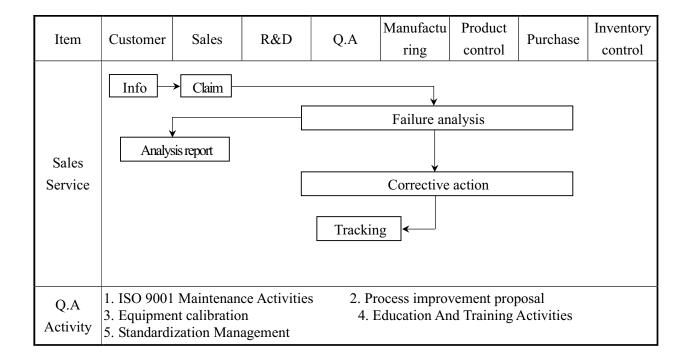


# 3. QUALITY ASSURANCE SYSTEM

### 3.1 Quality Assurance Flow Chart









### Ver 1.3

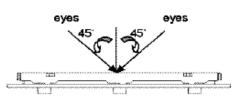
# 3.2 Inspection Specification

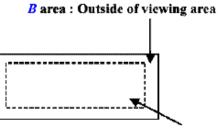
◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

- ◆Equipment : Gauge、MIL-STD、Tester、Sample
- ◆Defect Level : Major Defect AQL 0.4; Minor Defect AQL 1.5.
- ♦ OUT Going Defect Level : Sampling .

◆Manner of appearance test :

- (1). The test be under  $40W \times 2$  fluorescent light ' and distance of view must be at 30 cm.
- (2). The test direction is base on about around  $45^{\circ}$  of vertical line. (Fig. 1)
- (3). Definition of area . (Fig. 2)





A area : viewing area

#### ◆ Specification:

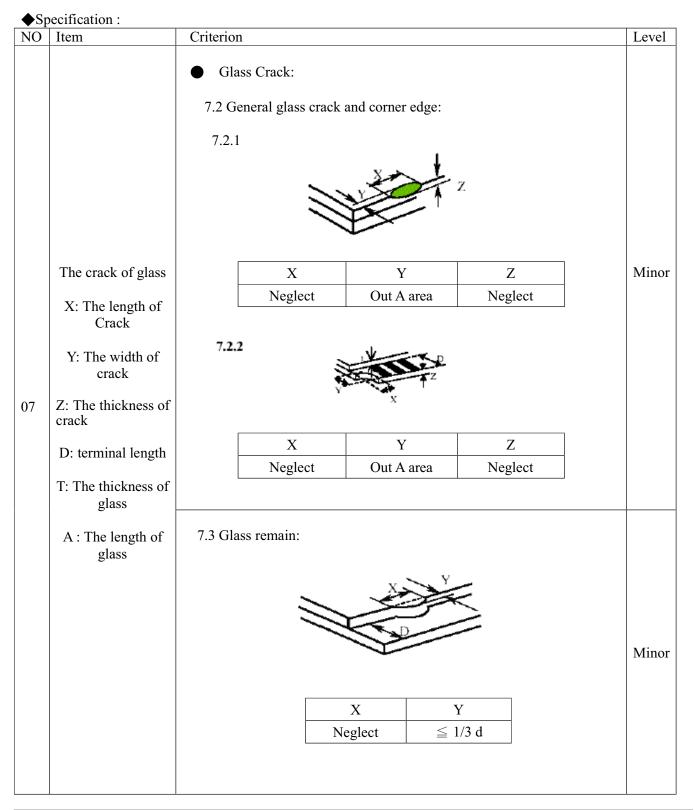
NO	Item	Criterion	level			
		1.1 The part number is inconsistent with work order of Production.	Major			
01	Product condition	1.2 Mixed production types.	Major			
		1.3 Assembled in inverse direction.	Major			
02	Quantity	2.1 The quantity is inconsistent with work order of production.	Major			
03	Outline dimension	3.1 Product dimension and structure must conform to Structure diagram.	Major			
		4.1 Missing line character   dot and icon.	Major			
		4.2 No function or no display.	Major			
04	Electrical Testing	4.3 Output data is error.	Major			
		4.4 LCD viewing angle defect.				
		4.5 Current consumption exceeds product specifications.	Major			
05	Black or white dot scratch contamination Round type	<ul> <li>5.1 Round type:</li> <li>5.1.1 display only :</li> <li>• White and black spots on display ≤ 0.25mm, no more than Four white or black spots present.</li> <li>• Densely spaced : NO more than two spots or lines within</li> </ul>	Minor			
		3mm				



NO	Item	Criterion						level
05	Black or white dot 、scratch 、	5.1.2 Nom-	display :					
	contamination	Dii	nension (diameter	:Ф)	Acceptance(	Q'ty)	]	
	Round type		$\Phi \leq$ 0.10mm		Accept no dense	e	]	
	→ <sub>x</sub> ←	0.	$10$ mm $< \Phi \leq 0.20$	mm	3			
	Y Y	0.	$20\mathrm{mm} < \Phi \leq 0.25$	mm	2			
	-		Total		4			
	$\Phi = (x+y)/2$	5.1.3 Line t	ype:					Minor
	$\Psi^{-}(\mathbf{x} \cdot \mathbf{y})/2$		ion (diameter : $\Phi$ )	)	Accep	tance (Q'ty)		
		Length	width		A area	B are		
	1		w≦ <b>0.03mm</b>		Accept no dens			
		L≦ <b>3.0mm</b>	$0.03$ mm $< \Phi \le 0$			Don't c		
		L≦ <b>2.5mm</b>	$0.05$ mm $< \Phi \leq 0$		4	Don't c	ount	
	L		w>0.075m	m	As ro	ound type		
				1				
				A	Acceptance(			
		Dimension	(diameter : $\Phi$ )	A	area	B are	a	
		$\Phi \leq$	≦0.20mm	Acc	ept no dense	Don't cou	nt	N
06	Polarizer	0.20mm	$<\Phi \leq 0.50$ mm		3	Don't cou	nt	Minor
00	Bubble	0.50mm	$<$ $\Phi$ $\leq$ 1.00mm		2	Don't cour		
			>1.00mm		0	Don't cour		
		Tota	l quantity		4	Don't cour	nt	
07	The crack of glass	-	Crack: ck on the circuit of	f electroc	le terminal :	Z	]	Minor
		Fro		a	Y≦ 1/2 D	$Z \leq t$	1	
		Ba			Neglect		-	
		Du			1.051000			

\_\_\_\_\_







	ecification :	0.14			T 1			
NO	Item The crack of glass X: The length of Crack Y: The width of crack	Criterion 7.4 Corner cr	ack and medial crack:	SP	Level			
07	<ul><li>Z: The thickness of crack</li><li>D: terminal length</li><li>T: The thickness of glass</li></ul>		G]	Minor				
		X	Y	Z				
	A : The length of glass	$ \leq 1/5a $ $ \leq 1/5a $	Crack can't enter viewing area Crack can't exceed the half of width of SP width of SP					
		8.1 Backlight	can't work normally.		Major			
08	Backlight elements	8.2 Backlight of	loesn't light or color is wrong.		Major			
00	cientents	8.3 Illumination source flickers when lit.						
		9.1 pin type must match type in specification sheet						
		9.2 No short circuits in components on PCB or FPC						
09	General appearance	9.3Product packaging must the same as specified on packaging specification sheet.						
		9.4 The folding and peeled off in polarizer are not acceptable						
		9.5 The PCB or FPC between B/L assembled distance (PCB or FPC) is $\leq 1.5$ mm						



NO.	TEST ITEM	TEST CONDITION							
1	High Temperature Storage Test	Keep in 70 $\pm 2^{\circ}$ C 96 hrs							
		Surrounding temperature, then storage at normal condition 4hrs							
2	Low Temperature Storage Test	Keep in -20 $\pm 2^{\circ}$ C 96 hrs							
		Surrounding temperature, then storage at normal condition 4hrs							
3	High Humidity Storage	Keep in $+40^{\circ}$ C/90%							
				ge at normal condition	4hrs				
4	Vibration Test	1. Sine wave $10 \sim 5$	5HZ frequency	v (1 min)					
4	vibration rest	2. The amplitude of	f vibration :1.5	mm					
		3. Each direction (2	KYZ) duration	for 2 Hrs					
		Air Discharge:		Contact Discharge:					
		Apply 6 KV with 5 ti	mes	Apply 250V with 5	times				
		Discharge foreach po	larity +/-	discharge foreach p	olarity +/-				
		1. Temperature amb	binace: $15^{\circ}C \sim 3$	5℃					
5	ESD Test	2. Humidity relative	:30%~60%						
		3. Energy Storage Capacitance(Cs+Cd):150pF±10%							
		4. Discharge Resistance(Rd):330 $\Omega \pm 10\%$							
		5. Discharge, mode of operation:							
		Single Discharge (time between successive discharges at least 1 s)							
		(Tolerance If the output voltage indication: ±5%)							
		$0^{\circ}C \rightarrow 25^{\circ}C \rightarrow 50^{\circ}C \rightarrow 25^{\circ}C$							
6	Temperature Cycling Test	(301	mins) (5mins) (	30mins) (5mins)					
0	Temperature Cycling Test	•	10 Cyc	cle					
		Surrounding temperature, then storage at normal condition 4hrs							
		1. Sine wave $10 \sim 55$ HZ frequency (1 min)							
7	Vibration Test (Packaged)	2. The amplitude of	vibration :1.5 r	nm					
		3. Each direction (XYZ) duration for 2 Hrs							
		``````````````````````````````````````	Weight (Kg)	Drop Height (cm)					
			~ 45.4	122					
			.4~90.8	76					
8	Drop Test (Packaged)								
	Brop Test (Tackaged)		.8 ~ 454	61					
		0	ver 454	46					
		Drop direct	ion : <b>※</b> 3 comer	/1 edges /6 sides etch	1 times				

# 4. RELIABILITY TEST 4.1 Reliability Test Condition



# **5. PRECAUTION RELATING PRODUCT HANDLING**

### 5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

### **5.2 HANDLING**

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is  $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

### **5.3 STORAGE**

- 5.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}C \pm 5^{\circ}C$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

### **5.4 TERMS OF WARRANTY**

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.