



MULTI-INNO TECHNOLOGY CO., LTD.

LCD MODULE SPECIFICATION

Model : MI1602I-G

Revision	
Engineering	
Date	
Our Reference	

**MODE OF DISPLAY**

Display mode	Display condition	Viewing direction
TN positive	Reflective type	6 O' clock
TN negative	Transflective type	12 O' clock
STN : Yellow green	Transmissive type	3 O' clock
Grey	Others	9 O' clock
Blue (negative)		
FSTN positive		
FSTN negative		

**GENERAL DESCRIPTION**

Display mode : 16 characters x 2 lines, COG LCD module.
Interface : 4 bit parallel
Driving method : 1/16 duty, 1/5 bias
Controller IC : Sunplus SPLC782A or equivalent
For the detailed information, please refer to the IC specifications.

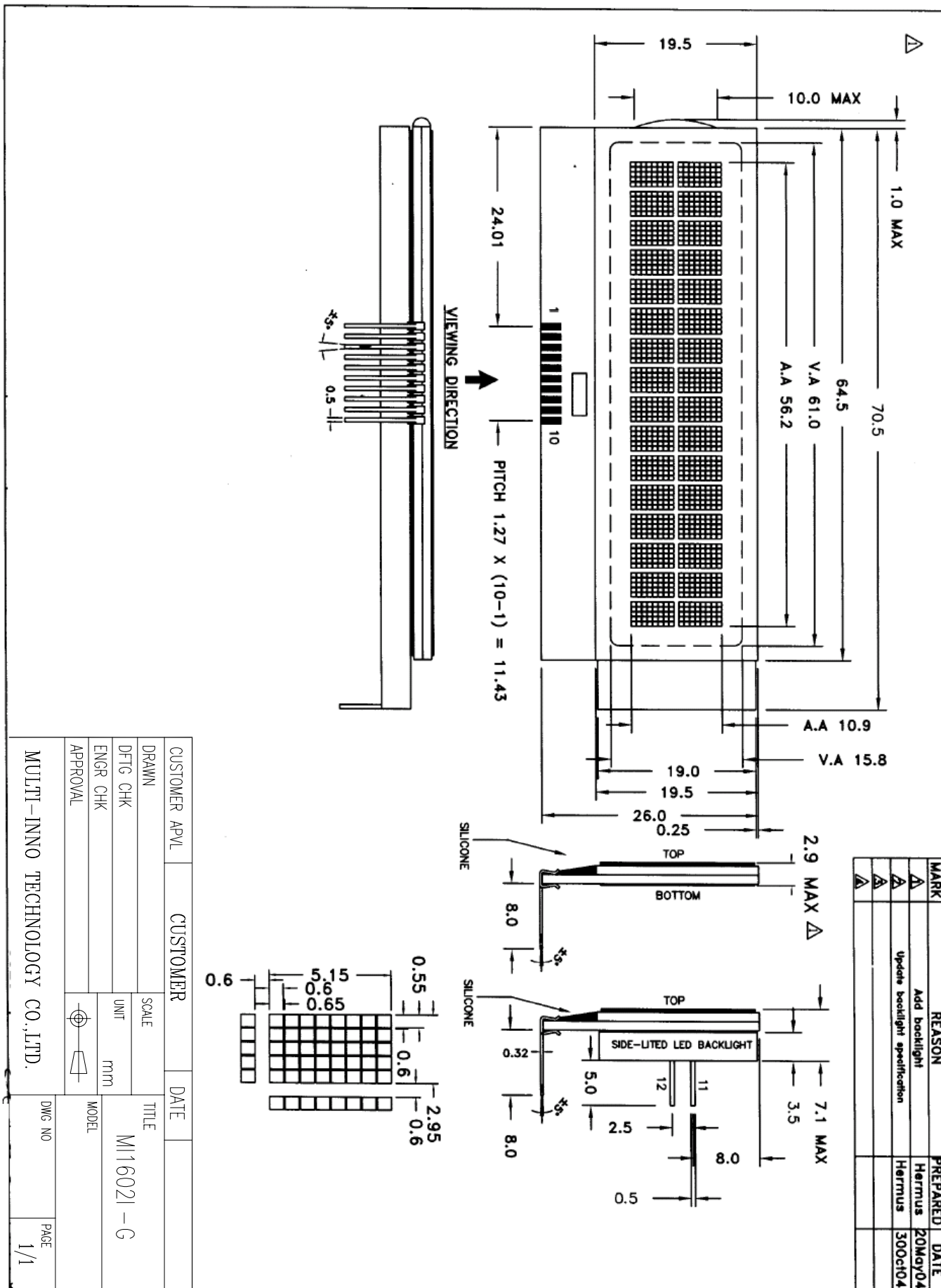
MECHANICAL DIMENSIONS

Item	Dimension	Unit	Item	Dimension	Unit
Outline Dimension	64.5(L)x26.0(W)x2.8(H)	mm	Character Pitch	3.55 (L)x 5.75(W)	mm
Viewing Area	61.0(L)x15.8(W)	mm	Dot Size	0.55(L)x0.6(W)	mm
Character Size	2.95 (L)x5.15 (W)	mm	-	-	-

CONNECTOR PIN ASSIGNMENT

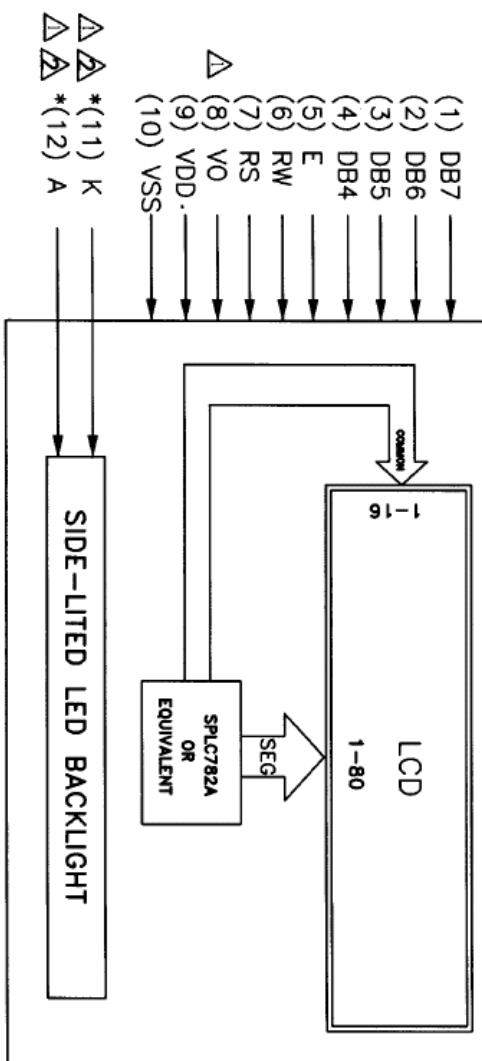
Pin No.	Signal	Function	Pin No.	Signal	Function
1	DB7	DATA BUS	6	RW	READ/WRITE SELECT
2	DB6	DATA BUS	7	RS	REGISTER SELECT
3	DB5	DATA BUS	8	VPP	POWER SUPPLY FOR LCD
4	DB4	DATA BUS	9	VDD	POWER SUPPLY FOR LOGIC
5	E	ENABLE SIGNAL	10	VSS	GROUND (0V)

COUNTER DRAWING OF MODULE DIMENSION



COUNTER DRAWING OF PIN OUT & BLOCK DIAGRAM

BLOCK DIAGRAM



PIN NUMBER	SYMBOL	FUNCTION
1.	DB7	DATA BUS
2.	DB6	DATA BUS
3.	DB5	DATA BUS
4.	DB4	DATA BUS
5.	E	ENABLE SIGNAL
6.	RW	READ/WRITE SELECT
7.	RS	REGISTER SELECT
8.	VO	INPUT VOLTAGE FOR LCD
9.	VDD	SUPPLY VOLTAGE FOR LOGIC
10.	VSS	GROUND
*11.	K	Supply Voltage for Backlight (-VE)
*12.	A	Supply Voltage for Backlight (+VE)

Note (*): Pin11,12 are used for backlight version

CUSTOMER APVL	CUSTOMER	DATE	
DRAWN	SCALE	TITLE	
DFTG. CHK	UNIT	MM	MI1602I-G
ENGR. CHK	MODEL		
APPROVAL			
MULTI-INNO TECHNOLOGY CO.,LTD.	DWG NO	PAGE	1/1

**ELECTRICAL CHARACTERISTICS**

Conditions: VSS=0V, Ta=25

Item	Symbol	MIN.	TYP.	MAX.	Unit	Item	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage for Logic	VDD	4.75	5.0	5.25	V	“H”Level Input Voltage	VIH	0.7VDD	-	VDD	V
Supply Current for Logic	IDD	-	0.8	-	mA	“L”Level Input Voltage	VIL	-0.3	-	0.55	V
Input Voltage for LCD	VO	-	4.5	-	V	-	-	-	-	-	-
EL Backlight Voltage (VEL)											
EL (@ Frequency 400Hz)	VBL	-	-	-	Vrms	-	-	-	-	-	-
Side-lited LED Backlight Forward Voltage (VF)						Side-lited LED Backlight Forward Current (IF)					
White (Current @40mA)	VBL	-	3.5	4.3	V	White	IBL	-	40	-	mA
Blue (Current @40mA)	VBL	-	3.3	4.0	V	Blue	IBL	-	40	-	mA
Yellow Green (Current @40mA)	VBL	-	5.0	-	V	Yellow Green	IBL	-	40	-	mA
Array LED Backlight Forward Voltage (VF)						Array LED Backlight Forward Current (IF)					
Yellow Green	VBL	-	-	-	V	Yellow Green	IBL	-	-	-	mA
Amber	VBL	-	-	-	V	Amber	IBL	-	-	-	mA
Orange	VBL	-	-	-	V	Orange	IBL	-	-	-	mA
Soft Orange	VBL	-	-	-	V	Soft Orange	IBL	-	-	-	mA

ABSOLUTE MAXIMUM RATINGS

Please make sure not to exceed the following maximum rating values under the worst application conditions

Item	Symbol	Rating (for normal temperature)	Rating (for wide temperature)	Unit
Supply Voltage	VDD	-0.3 to 7.0	-0.3 to 7.0	V
Input Voltage	VIN	-0.3 to VDD+0.3	-0.3 to VDD+0.3	V
Operating Temperature	Topr	0 to 50	-20 to 70	
Storage Temperature	Tstg	-10 to 60	-30 to 80	



INSTRUCTION TABLE

Instruction	Instruction Code										Description	Execution time (fosc = 270KHz)
	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0		
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM and set DDRAM address to "00H" from AC	1.52ms
Return Home	0	0	0	0	0	0	0	0	1	-	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.52ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Assign cursor moving direction and enable the shift of entire display	38μs
Display ON/OFF Control	0	0	0	0	0	0	1	D	C	B	Set display(D), cursor(C), and blinking of cursor(B) on/off control bit.	38μs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	-	-	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	38μs
Function Set	0	0	0	0	1	DL	N	F	-	-	Set interface data length (DL: 8-bit/4-bit), numbers of display line (N: 2-line/1-line) and, display font type (F:5x10 dots/5x8 dots)	38μs
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	38μs
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter	38μs
Read Busy Flag and Address Counter	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	38μs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	38μs

Note: "--": don't care

DISPLAY DATA RAM (DD RAM) AND CHARACTER POSITION

16x2, 1/16 Duty Cycle

	1	2		16	DISPLAY POSITION
line 1	00	01		0F	DD RAM ADDRESS
line 2	40	41		4F	

AC CHARACTERISTICS

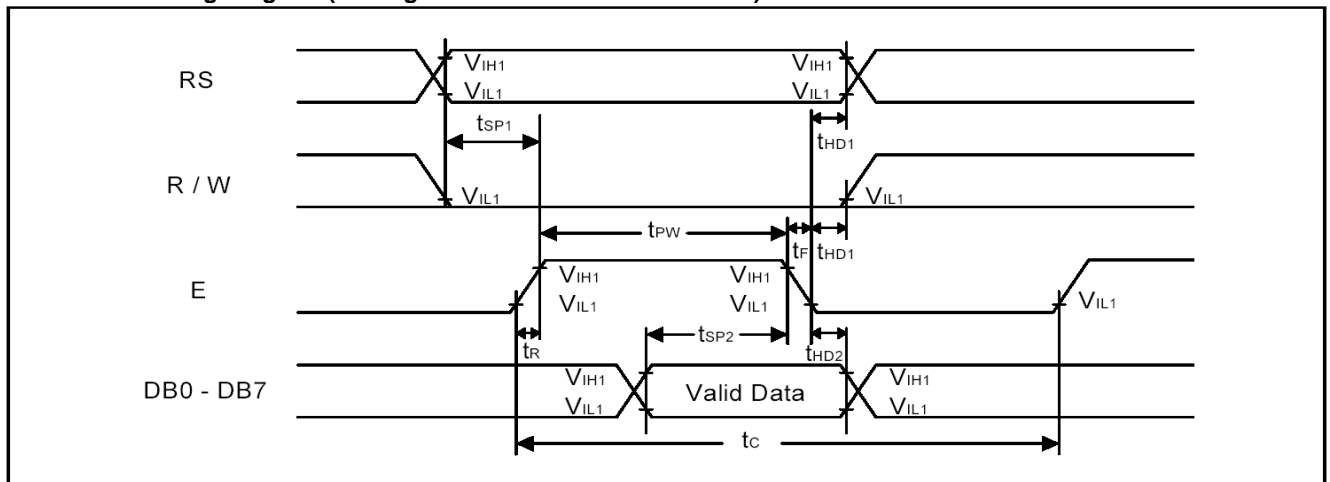
Write mode (Writing data from MPU to SPLC782A)

Characteristics	Symbol	Limit			Unit	Test Condition
		Min.	Typ.	Max.		
E Cycle Time	t_C	500	-	-	ns	Pin E
E Pulse Width	t_{PW}	230	-	-	ns	Pin E
E Rise/Fall Time	t_R, t_F	-	-	20	ns	Pin E
Address Setup Time	t_{SP1}	40	-	-	ns	Pins: RS, R/W, E
Address Hold Time	t_{HD1}	10	-	-	ns	Pins: RS, R/W, E
Data Setup Time	t_{SP2}	80	-	-	ns	Pins: DB0 - DB7
Data Hold Time	t_{HD2}	10	-	-	ns	Pins: DB0 - DB7

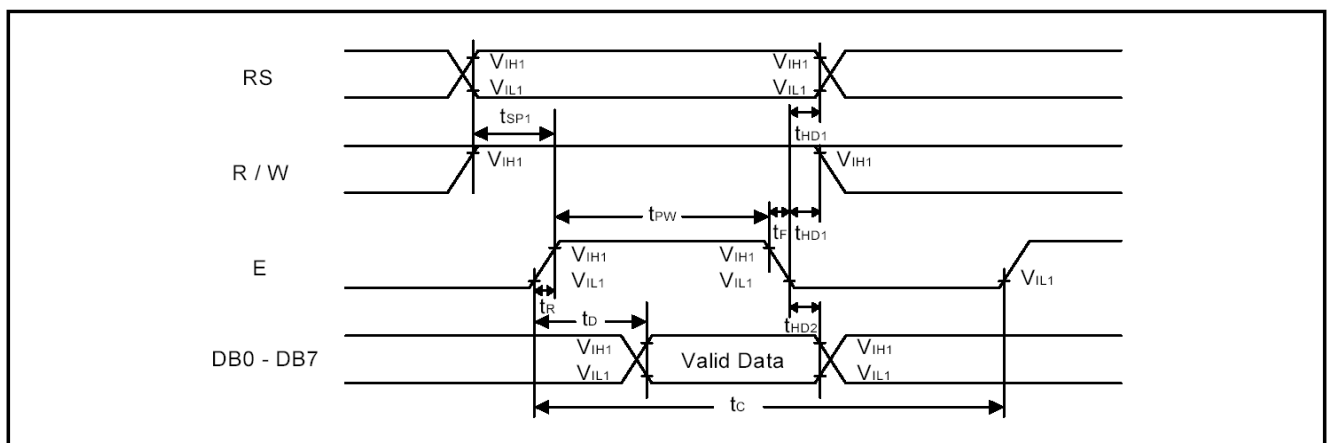
Read mode (Reading Data from SPLC782A to MPU)

Characteristics	Symbol	Limit			Unit	Test Condition
		Min.	Typ.	Max.		
E Cycle Time	t_C	500	-	-	ns	Pin E
E Pulse Width	t_W	230	-	-	ns	Pin E
E Rise/Fall Time	t_R, t_F	-	-	20	ns	Pin E
Address Setup Time	t_{SP1}	40	-	-	ns	Pins: RS, R/W, E
Address Hold Time	t_{HD1}	10	-	-	ns	Pins: RS, R/W, E
Data Output Delay Time	t_D	-	-	160	ns	Pins: DB0 - DB7
Data hold time	t_{HD2}	5.0	-	-	ns	Pins: DB0 - DB7

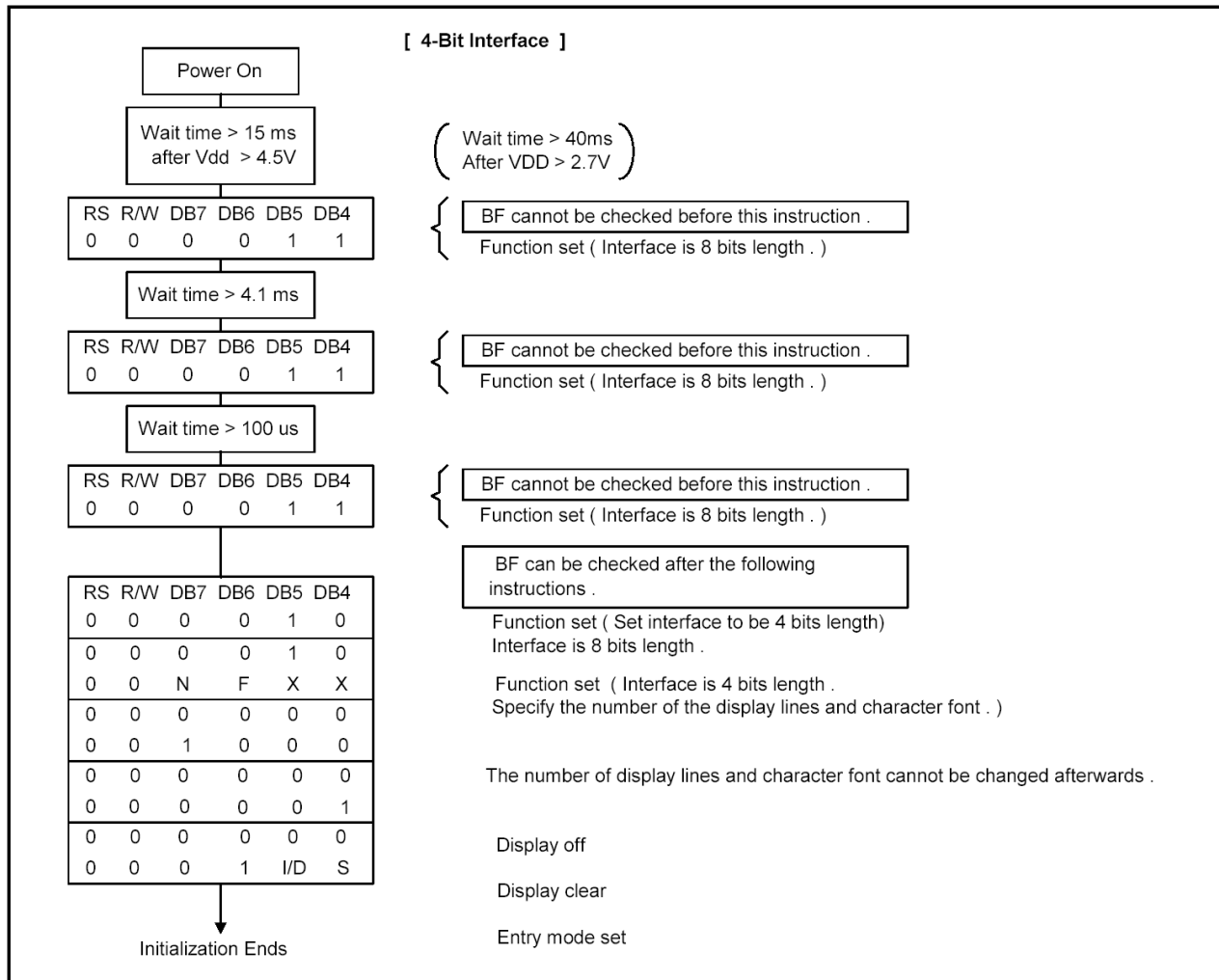
Write Mode Timing Diagram (Writing Data from MPU to SPLC782A)



Read Mode Timing Diagram (Reading Data from SPLC782A to MPU)



INITIALIZATION FLOWCHART



ELECTRO-OPTICAL CHARACTERISTICS

MEASURING CONDITION: POWER SUPPLY = $V_{OP} / 64 \text{ Hz}$
 TEMPERATURE = $22 \pm 5 \text{ }^{\circ}\text{C}$
 RELATIVE HUMIDITY = $60 \pm 15 \%$

ITEM	SYMBOL	UNIT	TYP. TN	TYP. STN
RESPONSE TIME	Ton	ms	130	150
	Toff	ms	170	190
CONTRAST RATIO	Cr	-	8	15
VIEWING ANGLE (6 O'clock) $Cr \geq 2$	V3:00	°	70	45
	V6:00	°	45	70
	V9:00	°	70	45
	V12:00	°	5	60

THE ELECTRO-OPTICAL CHARACTERISTICS ARE MEASURED VALUE BUT NOT GUARANTEED ONES.

**RELIABILITY OF LCD MODULE**

ITEM	TEST CONDITION FOR NORMAL TEMPERATURE	TEST CONDITION FOR WIDE TEMPERATURE	TIME
High temperature operating	50°C	70°C	240 hours
Low temperature operating	0°C	-20°C	240 hours
High temperature storage	60°C	80°C	240 hours
Low temperature storage	-10°C	-30°C	240 hours
Temperature-humidity storage	40°C 90% R.H.	60°C 90% R.H.	96 hours
Temperature cycling	-10°C to 60°C 30 Min Dwell	-30°C to 80°C 30 Min Dwell	5 cycle
Vibration Test at LCM Level	Freq 10-55 Hz Sweep rate: 10-55-10 at 1 min Sweep mode Linear Displacement: 2 mm p-p 1 Hour each for X, Y, Z	Freq 10-55 Hz Sweep rate: 10-55-10 at 1 min Sweep mode Linear Displacement: 2 mm p-p 1 Hour each for X, Y, Z	—

SAMPLING METHOD

SAMPLING PLAN: MIL-STD 105E

CLASS OF AQL: LEVEL II/ SINGLE SAMPLING
MAJOR-0.65% MINOR – 1.5%

QUALITY STANDARD

DEFECT	CRITERIA	TYPE	FIGURE
SHORT CIRCUIT	-	MAJOR	-
MISSING SEGMENT	-	MAJOR	-
UNEVEN / POOR CONTRAST	-	MAJOR	-
CROSS TALK	-	MAJOR	-
PIN HOLE	$\text{MAX}(a,b) \leq 1/4 W$	MINOR	1
EXCESS SEGMENT	$\text{MAX}(c,d) \leq 1/4 T$	MINOR	1
BUBBLES	$d^* \geq 0.2$ QTY=0	MINOR	2
BLACKS SPOTS	$d \leq 0.3$ N.A.** $0.3 < d \leq 0.4$ QTY ≤ 1 $0.4 < d$ QTY=0	MINOR	2
LINE SCRATCHES	$x \geq 0.7$ $y \geq 0.05$ QTY=0	MINOR	3
BLACK LINE	$x \geq 0.7$ $y \geq 0.05$ QTY=0	MINOR	3

*d = MAX (d₁,d₂)

** N. A . = NOT APPLICABLE

DEFECT TABLE : B

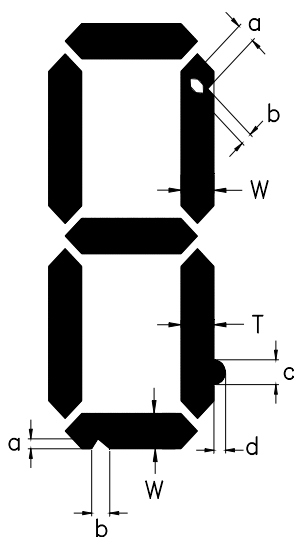
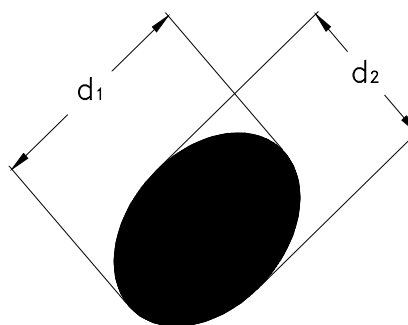
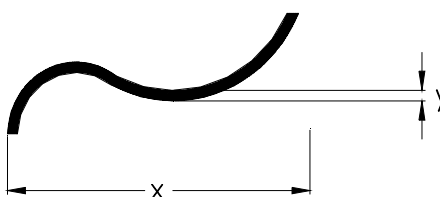


fig . 1



POLARIZER BUBBLES / SPOTS

fig . 2



LINE SCRATCHES / BLACK LINE

fig . 3

QUALITY STANDARD (CONT .)

DEFECT		CRITERIA	TYPE	FIGURE
CHIPS	CONTACT EDGE	$e \leq 1/2T$ $f \leq 1/3W$ $g \leq 3.5$	MINOR	4
	BOTTOM GLASS	$p \leq 1.0$ $q \leq 3.5$ $r \leq 1/2T$		4
	CORNER	$a \leq 1.5$ $b \leq W$		4
	TOP GLASS	$a \leq 3.0$ $b \leq 1/3T$ $c \leq 1/2W$		5
GLASS PROTRUSION		$a \leq 1/4 W$	MINOR	6
RAINBOW		-	MINOR	-

UNLESS STATE OTHERWISE , ALL UNIT ARE IN MILLIMETER .

DEFECT TABLE : B

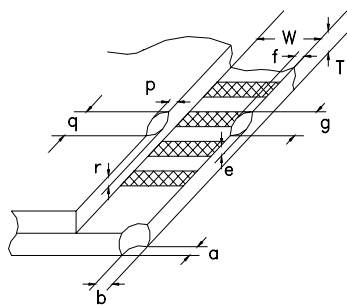


fig . 4

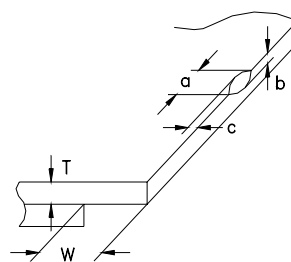


fig . 5

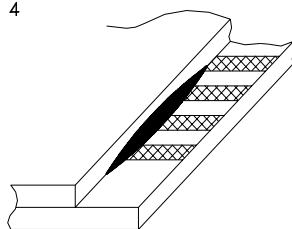


fig . 6



HANDLING PRECAUTIONS

(1) CAUTION OF LCD HANDLING & CLEANING

Use soft cloth with solvent (recommended below) to clean the display surface and wipe lightly.

- Isopropyl alcohol, ethyl alcohol, trichlorotrifluoroethane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use the following solvent;

-water, ketone, aromatics

(2) CAUTION AGAINST STATIC CHARGE

The LCD modules use CMOS LSI drivers, so customers are recommended that any unused input terminal would be connected to V_{DD} or V_{SS} , do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

Remove the protective film slowly and, if possible, under ESD control device like ion blower and humidity of working room should be kept over 50%RH to reduce risk of static charge.

(3) PACKAGING

Avoid intense shock and falls from a height and do not operate or store them exposed direct to sunshine or high temperature/humidity.

(4) CAUTION FOR OPERATION

It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life. The use of direct current drive should be avoided because an electrochemical reaction due to direct current causes LCD's undesirable deterioration.

Response time will be extremely delayed at low temperature, and LCD's show dark color at high temperature. However those phenomena do not mean malfunction or out of order with LCD's.

Some font will be abnormally displayed when the display area is pushed hard during operation. But it resumes normal condition after turning off once.

(5) SOLDERING (for Pin type)

It is recommended to complete dip soldering at 270 °C or hand soldering at 280 °C within 3 seconds. The soldering position is at least 3mm apart from the pin head. Wave or reflow soldering are not recommended. Metal pins should not be soldered for more than 3 times and each soldering should be done after cool down of metal pins

(6) SAFETY

For crash damaged or unnecessary LCD's, it is recommended to wash off liquid crystal by either of solvents such as acetone and ethanol and should be burned up later.

When any liquid leaked out of a damaged glass cell comes in contact with your hands, wash it off with soap and water.

WARRANTY

Multi-Inno will replace or repair any of her LCD module in accordance with her LCD specification for a period of one year from date of shipment. The warranty liability of Multi-Inno is limited to repair and/or replacement. Multi-Inno will not be responsible for any subsequent or consequential event.