

# MULTI-INNO TECHNOLOGY CO., LTD.

# www.multi-inno.com

# **LCD MODULE SPECIFICATION**

# Model : MI0700S4T-3

This module uses ROHS material

# For Customer's Acceptance:

Customer		
Approved		
Comment		

This specification may change without prior notice in	Revision	1.2
order to improve performance or quality. Please contact	Engineering	
Multi-Inno for updated specification and product status	Date	2013-08-28
before design for this product or release of this order.	Our Reference	



# **REVISION RECORD**

REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2013-02-27	First Release	
1.1	2013-07-25	Change LCD thinkness Update VGH,VGL,AVDD parameters Update LED forward current parameters Update current consumption	
1.2	2013-08-28	Update Timing	



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# ■ GENERAL INFORMATION

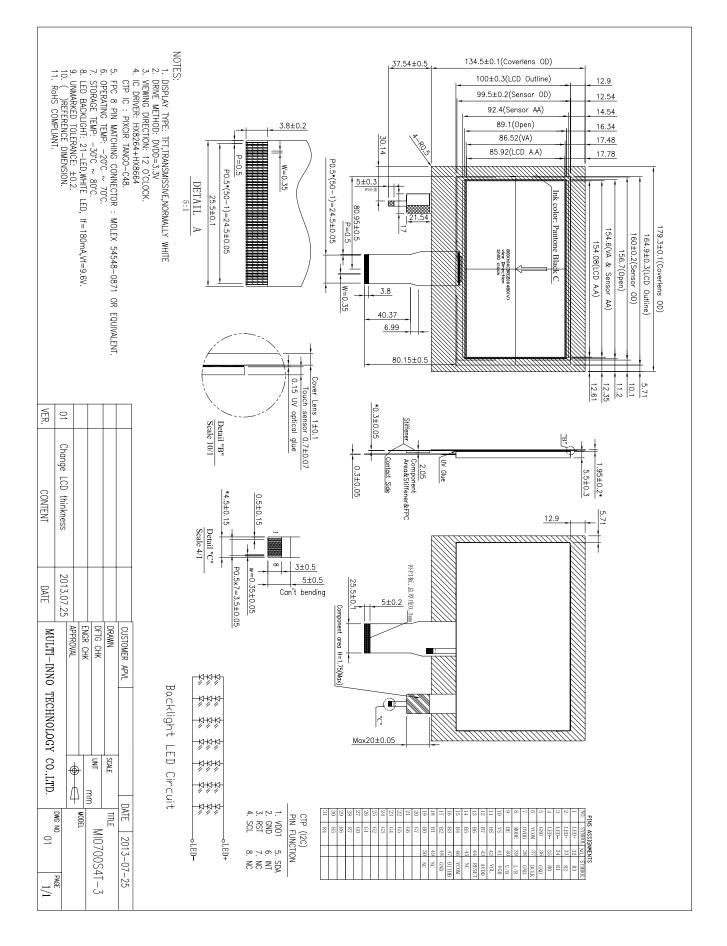
Item	Contents	Unit
LCD type	TFT/Transmissve/Normally white	/
Size	7.0	Inch
Viewing direction	12:00	O' Clock
Gray scale inversion direction	6:00	O' Clock
LCM $(W \times H \times D)$	179.30×134.50×7.45	mm <sup>3</sup>
Active area (W×H)	154.08×85.92	mm <sup>2</sup>
Pixel size (W×H)	0.0642×0.1790	mm <sup>2</sup>
Number of dots	800 (RGB) × 480	/
Driver IC	HX8264+HX8664	/
Backlight type	21 LEDs	/
Interface type	24bit RGB	/
Color depth	16.7M	/
Surface treatment	Anti-glare	/
Color arrangement	RGB-stripe	/
Backlight power consumption	1.728	W
Panel power consumption	327	mW
Input voltage	3.3	V
With/Without TSP	With CTP	/
Weight	TBD	g

Note 1:Viewing direction for best image quality is different from TFT definition, there is a 180 degree shift. Note 2 : RoHS compliant;

Note 3: LCM weight tolerance:  $\pm$  5%.

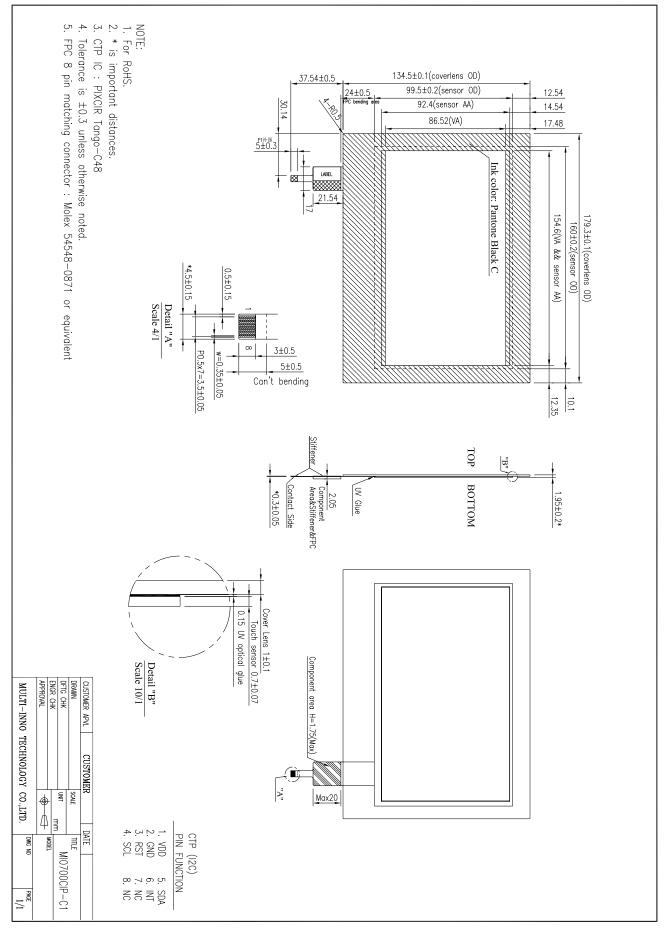


# ■ EXTERNAL DIMENSIONS





# **CTP OUTLINE DRAWING**





# ■ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit
	DVDD	-0.3	5.0	V
Derver velte co	AVDD	6.5	13.5	V
Power voltage	VGH	-0.3	43.0	V
	VGL	-20.0	0.3	V
	VGH-VGL	12.0	40.0	V
LED reverse voltage	V <sub>R</sub>	-	1.2	V
LED forward current	I <sub>F</sub>	-	30	mA
Operating temperature	Тор	-20	70	°C
Storage temperature	Тѕт	-30	80	°C
Humidity	RH	-	90%(Max60°C)	RH

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 2: VR Conditions: Zener Diode 20mA

# **ELECTRICAL CHARACTERISTICS**

DC CHARACTERISTICS

Parameter	Symbol	Min	Тур	Max	Unit
	DVDD	3.0	3.3	3.6	V
Power voltage	AVDD	9.4	9.6	9.8	V
	VGH	17.0	18.0	19.0	V
	VGL	-6.6	-6.0	-5.4	V
Input signal voltage	Vcom	3.8	4.0	4.2	V
Input logic high voltage	VIH	0.7DVDD	-	DVDD	V
Input logic low voltage	VIL	0	-	0.3DVDD	V

Note 1: Be sure to apply  $DV_{DD}$  and  $V_{GL}$  to the LCD first, and then apply  $V_{GH}$ 

Note 2: DV<sub>DD</sub> setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note 3: DCLK,HS,VS,RESET,U/D, L/R,DE,R0~R7,G0~G7,B0~B7,MODE,DITHB.



#### CURRENT CONSUMPTION

	Seconda e 1	Values			The:4	Dement	
Item	Symbol	Min.	Тур.	Max.	Unit	Remark	
	I <sub>GH</sub>	-	0.5	1	mA	VGH =18.0V	
Commont for Driver	I <sub>GL</sub>	-	0.5	1	mA	$V_{GL} = -6.0V$	
Current for Driver	IDV <sub>DD</sub>	-	8	15	mA	DVDD =3.3V	
	IAV <sub>DD</sub>	-	30	40	mA	AVDD =9.6V	

# ■ BACKLIGHT CHARACTERISTICS

Item	Symbol	Min.	Тур.	Max.	Unit	Condition
Voltage for LED backlight	VL	9.3	9.9	10.5	V	Note 1
Current for LED backlight	IL	170	180	200	mA	
LED life time	-	20,000	-	-	Hr	Note 2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25 $^\circ\!C$  and  $_L$  =180mA.

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and I<sub>L</sub> =180mA. The LED lifetime could be decreased if operating I<sub>L</sub> is lager than 180mA.



Item	Symbol	Condition	Min	Тур	Max	Unit	Remark	Note					
Response time	Tr +Tf	2	-	25	50	ms	Fig.1	4					
Contrastratio	Cr	θ=0°	400	500	-		FIG 2.	1					
Luminance uniformity	δ WHITE	Ø=0° Ta=25℃	70	75	-	%	FIG 2.	3					
Surface Luminance	Lv	1a-23 C	270	340	-	cd/m <sup>2</sup>	FIG 2.	2					
		Ø = 90°	40	50	-	deg	<b>FIG 3.</b>						
Viewing angle range	θ	Ø = 270°	60	70	-	deg	<b>FIG 3.</b>	6					
viewing angle l'ange			0	$\emptyset = 0^{\circ}$	60	70	-	deg	<b>FIG 3.</b>	U			
										Ø = 180°	60	70	-
	Red x		TBD	TBD	TBD								
	Red y		TBD	TBD	TBD								
	Green x	θ=0°	TBD	TBD	TBD								
CIE (x, y) chromaticity	Green y		TBD	TBD	TBD		FIG 2.	5					
	Blue x	Ø=0° T₂=25℃	TBD	TBD	TBD		110 2.	5					
	Blue y	Ta=25℃	TBD	TBD	TBD								
	White x	]	0.260	0.310	0.360								
	White y		0.280	0.330	0.380								

### **ELECTRO-OPTICAL CHARACTERISTICS**

Note 1. Contrast Ratio(CR) is defined mathematically as For more information see FIG 2.

Contrast Ratio = <u>Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)</u> Average Surface Luminance with all black pixels (P1, P2, P 3, P4, P5)

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)

Note 3. The uniformity in surface luminance ,  $\delta$  WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance. For more information see FIG 2.

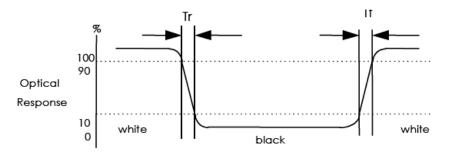
 $\delta \text{ WHITE} = \underline{\text{Minimum Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)}}_{\text{Maximum Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)}}$ 

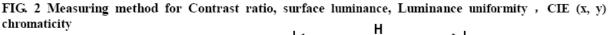
- Note 4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1. The test equipment is Autronic-Melchers's ConoScope. Series.
- Note 5. CIE (x, y) chromaticity, The x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.
- Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the conrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.
- Note 7. For viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments For contrast ratio, Surface Luminance, Luminance uniformity, CIE The test data is base on TOPCON's BM-5 photo detector.

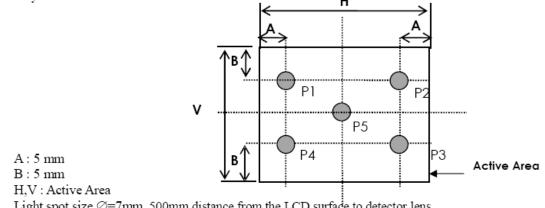


#### FIG. 1 The definition of Response Time

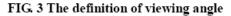
The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

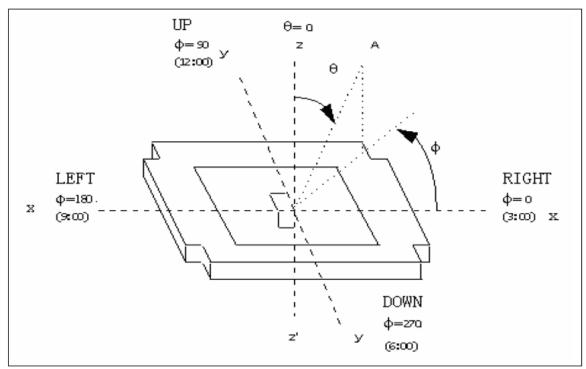






Light spot size Ø=7mm, 500mm distance from the LCD surface to detector lens measurement instrument is TOPCON's luminance meter BM-5







# ■ INTERFACE DESCRIPTION

FPC Connector is used for the module electronics interface. The recommended model is FH12A-50S-0.5SH manufactured by Hirose.

Pin No.	Symbol	I/O	Functi	Remark
1	V <sub>LED+</sub>	Р	Power for LED backlight (Anode)	
2	V <sub>LED+</sub>	Р	Power for LED backlight (Anode)	
3	VLED-	Р	Power for LED backlight (Cathode)	
4	VLED-	Р	Power for LED backlight (Cathode)	
5	GND	Р	Power ground	
6	VCOM	Ι	Common voltage	
7	DV <sub>DD</sub>	Р	Power for Digital Circuit	
8	MODE	Ι	DE/SYNC mode select	Note 1
9	DE	Ι	Data Input Enable	
10	VS	Ι	Vertical Sync Input	
11	HS	Ι	Horizontal Sync Input	
12	B7	Ι	Blue data(MSB)	
13	B6	Ι	Blue data	
14	B5	Ι	Blue data	
15	B4	Ι	Blue data	
16	B3	Ι	Blue data	
17	B2	Ι	Blue data	
18	B1	Ι	Blue data	Note 2
19	В0	Ι	Blue data(LSB)	Note 2
20	G7	Ι	Green data(MSB)	
21	G6	Ι	Green data	
22	G5	Ι	Green data	
23	G4	Ι	Green data	
24	G3	Ι	Green data	
25	G2	Ι	Green data	
26	G1	Ι	Green data	Note 2
27	G0	Ι	Green data(LSB)	Note 2



28	R7	Ι	Red data(MSB)	
29	R6	I	Red data	
30	R5	I	Red data	
31	R3 R4	I	Red data	
32	R4 R3			
		I		
33	R2	Ι	Red data	
34	R1	Ι	Red data	Note 2
35	R0	Ι	Red data(LSB)	Note 2
36	GND	Р	Power Ground	
37	DCLK	Ι	Sample clock	Note 3
38	GND	Р	Power Ground	
39	L/R	Ι	Left / right selection	Note 4,5
40	U/D	Ι	Up/down selection	Note 4,5
41	VGH	Р	Gate ON Voltage	
42	VGL	Р	Gate OFF Voltage	
43	AV <sub>DD</sub>	Р	Power for Analog Circuit	
44	RESET	Ι	Global reset pin.	Note 6
45	NC	-	No connection	
46	V <sub>COM</sub>	Ι	Common Voltage	
47	DITHB	Ι	Dithering function	Note 7
48	GND	Р	Power Ground	
49	NC	-	No connection	
50	NC	-	No connection	

I: input, O: output, P: Power

Note 1: DE/SYNC mode select. Normally pull high.

When select DE mode, MODE="1", VS and HS must pull high.

When select SYNC mode, MODE= "0", DE must be grounded.

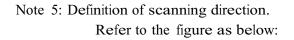
Note 2: When input 18 bits RGB data, the two low bits of R,G and B data must be grounded.

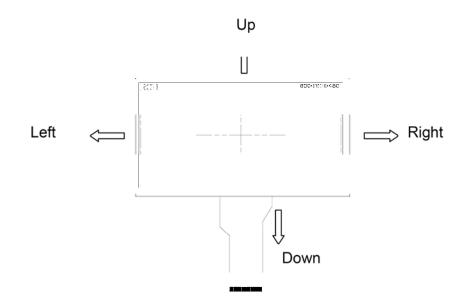
Note 3: Data shall be latched at the falling edge of DCLK.

Note 4: Selection of scanning mode.

Set of scan control input		Scanning direction
U/D	L/R	
GND	DVDD	Up to down, left to right
DVDD	GND	Down to up, right to left
GND	GND	Up to down, right to left
DVDD	DVDD	Down to up, left to right







- Note 6: Global reset pin. Active low to enter reset state. Suggest to connect with an RC reset circuit for stability. Normally pull high.
- Note 7: Dithering function enable control, normally pull high. When DITHB="1",Disable internal dithering function, When DITHB="0",Enable internal dithering function.



# ■ APPLICATION NOTES

# 1. Timing Characteristics

# 1.1 AC Electrical Characteristics

Iteres	Symphol		Values			Remark
Item	Symbol	Min.	Тур.	Max.	Unit	Kemark
HS setup time	Thst	8	-	-	ns	
HS hold time	Thh	8	-	-	ns	
VS setup time	Tvst	8	-	-	ns	
VS hold time	Tvh	8	-	-	ns	
Data setup time	Tds	8	-	-	ns	
Data hole time	Tdh	8	-	-	ns	
DE setup time	Tes	8	-	-	ns	
DE hole time	Teh	8	-	-	ns	
DVDD Power On Slew rate	TPOR	-	-	20	ms	From 0 to 90% DVDD
RESET pulse width	TRst	1	-	-	ms	
DCLK cycle time	Тсо	20	-	-	ns	
DCLK pulse duty	Tcwh	40	50	60	%	



## 1.2. Data Input Format





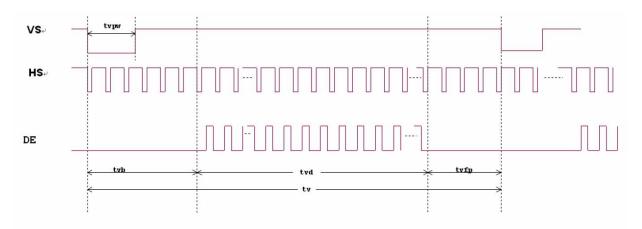


Figure 2 Vertical input timing diagram



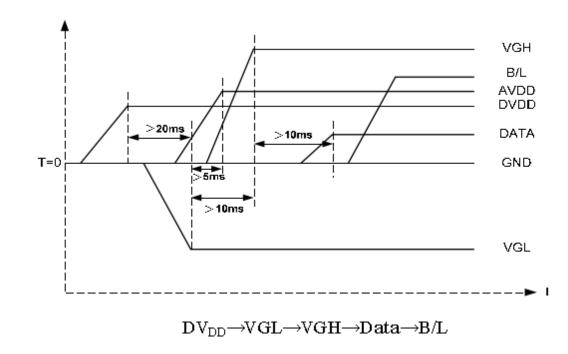
# 1.3. Timing

	Saurala a l		Values		TT	
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Horizontal Display Area	thd	-	800	-	DCLK	
DCLK Frequency	fclk	29.0	33.0	38.0	MHz	
One Horizontal Line	th	1026	1056	1086	DCLK	
HS pulse width	thpw	-	30	-	DCLK	
HS Blanking	thb	-	46	-	DCLK	
HS Front Porch	thfp	180	210	240	DCLK	

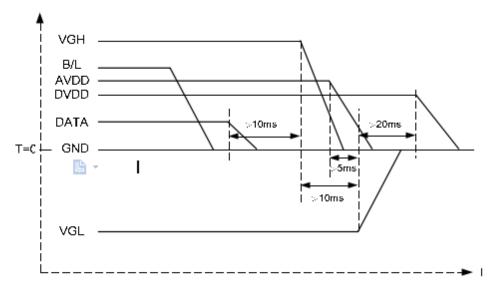
	G 1 1		Values	<b>T</b> T •		
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Vertical Display Area	tvd	-	480	-	TH	
VS period time	tv	515	525	535	TH	
VS pulse width	tvpw	-	13	-	TH	
VS Blanking	tvb	-	23	-	TH	
VS Front Porch	tvfp	12	22	32	TH	

# 2. Power Sequence

a. Power on :



b. Power off:



 $B/L \rightarrow Data \rightarrow VGH \rightarrow VGL \rightarrow DV_{DD}$ 

Note: Data include R0~R7, B0~B7, GO~G7, U/D, L/R, DCLK, HS, VS, DE.





# **CTP GENERAL SPECIFICATIONS**

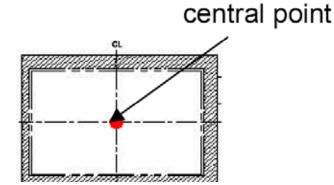
## **1. GENERAL SPECIFICATIONS**

Composition: 7inch Capacitive Touch Panel (CTP). Interface:  $I^2C$  for the CTP.

ltem	Specification	Unit
Туре	Transparent type projected capacitive touch panel	
Input mode	Human's finger	
Finger	5	
Resolution	1024 x 600	dots
Outline Dimension	179.3(W) x 134.5(H) x 1.95(D)	mm
Sensor Active Area	154.6(W)(typ.) x92.4(H)(typ.)	mm
Transparency	≥85%	%
Haze	≦5.0%	%
Hardness	7H (typ.)[by JIS K5400]	Pencil hardness
Weight	TBD	g
Report rate	Max : 122	Points/sec
Response time	15	ms
Point hitting life time	1,000,000 times min.	Note 1

Note 1: Use 8 mm diameter silicon rubber/force 3N to knock on the same point twice per second

(no-operating), after test function check pass.



#### 2. ABSOLUTE MAXIMUM RATINGS

Symbol	Description	Min	Тур	Max	Unit	Notes
VDD	Supply voltage	-0.3	-	6.5	V	
Vio	DC input voltage	-0.3	-	VDD+0.3	V	



#### 3. ELECTRICAL CHARACTERISTICS

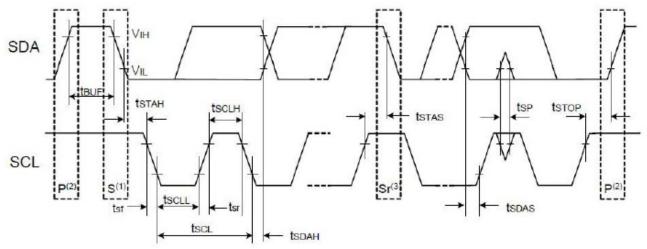
Symbol	Description	Fingers	F <sub>scan</sub> (Hz)	Min	Тур	Max	Unit
VDD	Supply voltage			2.5	3.3	3.6	V
GND	Supply voltage			-	0	-	V
Vih	Input H voltage			0.8VDD	-	VDD	V
VIL	Input L voltage			0	-	0.2VDD	V
	System clock frequency					20	MHz
	CPU clock frequency					20	MHz
		1	280		-	4	mA
		2	160		-	5	mA
1	Active mode	3	90		-	5.2	mA
		4	80		-	5.4	mA
		5	75		-	5.6	mA
lalaan	Sleep mode	0	10		-	0.11	mA
Isleep	Deep sleep mode	-			-	50	uA
Ifreeze	Freeze mode	-			-	2	uA
	bootload	-			-	6.2	mA
	Calibration	-			-	6.2	mA

#### 4. TIMING SPECIFICATIONS

# 4.1 CTP Interface and Data Format [Slave address is 0x5C ( 7 bit addressing )]

Communication protocol: I<sup>2</sup>C

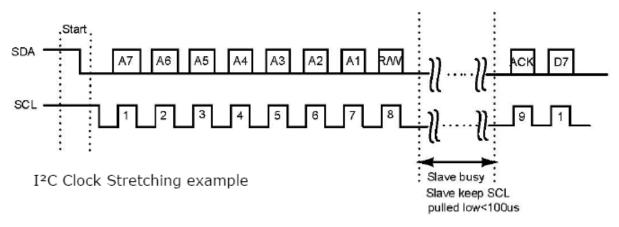
Clock frequency: 100 KHz (400 KHz Fast mode)



Note : (1) Start Condition ;(2)Stop Condition;(3)Retransmit start condition



Symbol	Description	Min	Мах	Unit
tscl	SCL input cycle time	12tcyc+600	-	
tSCLH	SCL input H width	3tcyc+300	-	
tSCLL	SCL input L width	5tcyc+500	-	
tSF	SCL, SDA input fall time		300	
tSP	SCL, SDA input spike pulse rejection time		1 tcyc	
tsuf	SDA input bus-free time	5tcyc		ns
<b>t</b> STAH	Start condition input hold time	3tcyc		
<b>t</b> STAS	Retransmit start condition input setup time	3tcyc		
<b>t</b> STOP	Stop condition input setup time	3tcyc		
tsdas	Data input setup time	1tcyc+40		1
<b>t</b> SDAH	Data Input hold time	10		1



The protocol for data exchange has been designed with the following considerations 1 Most of the data traffic is read operation to get the finger or fingers position

2 Read operations do need an initial write operation.

3 Write operations are most of the time power management and interrupt setting instructions

4 Interrupt pulse width setting adjustments need a write operation.

S	START
Р	STOP
A	Acknowledge
N	No acknowledge
W	WRITE
R	READ
DATA	8-bit

From slave to Master From Master to Slave



## 4.2 Timing Characteristic

#### **Read Operation**

Read packets have variable content length, decided by the host. It is available to do a single read operation or a sequential read operation. Therefore, the beginning register address is needed to set before a read operation. And the data sent exactly follow the register table 9, table 11, table 12, and table 15. And, the firmware in the slave will use a memory copy of the register for I<sub>2</sub>C slave read operation, so that firmware can continue updates, and I<sub>2</sub>C slave is still using a consistent (but old) coordinates for read operation as below,

S 1 <sup>2</sup> C Address 0x5C Mem. Address	AS I <sup>2</sup> C Address RA 0x5C	Data[0] A • • •	Data[n-1] A	Data[n] N P
--	--	-----------------	-------------	-------------

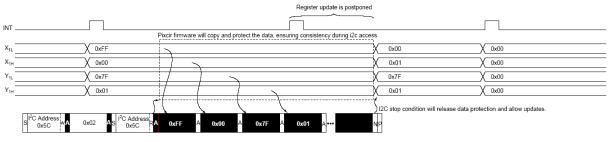
#### Read operation

In a sequential read operation, the first data sent by the MSI device is therefore the touching register, and then the X and Y coordinates of the first finger, then 2nd finger, 3rd finger, 4th finger and then coordinates of the 5th finger, and so on. Refer in below,

S 0x5C Address AA 0x00 AS 0x5C RA to		Data: Data: Strength4 <sup>A</sup> Strength5 <sup>N</sup> P
--------------------------------------	--	--

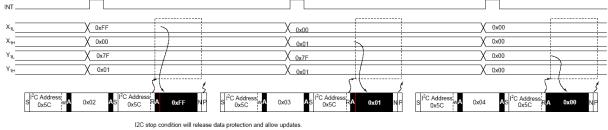
#### Coordinates read operation

If the host does not finish the read operation when the INT line is set again, the slave firmware will delay to update coordinates registers for I<sup>2</sup>C read operation until the host finish the read operation referred to below



Received data is : X=0x00FF, Y=0x017F (correct)

I<sup>2</sup>C stop condition will release data protection and allow the slave firmware update the coordinates registers for I<sup>2</sup>C read operation. So, the host has the change to give incorrect data when it gets the coordinates data with single read operation. Because the host sends many times for I<sup>2</sup>C stop condition in each multi-fingers coordinate's position reading, it will give the slave firmware chance to update the coordinates registers for I<sup>2</sup>C read operation, the host will give a combine unrelated data combines new and old coordinates together, referred to below



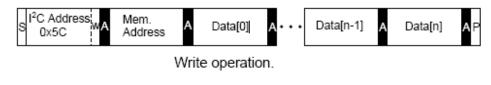
Received data is : X=0x01FF, Y=0x0000 incorrect because it combines unrelated data

# Coordinates read operation explanation



#### Write Operation

Write packets have variable content length, decided by the host. Write operation stops when host issues an I<sub>2</sub>C STOP symbol. The write packet is illustrated in below. Following the I<sub>2</sub>C device address, the first byte of the write packet is always the destination register address, referred in table 9, table 11, table 12, and table 15. Subsequent data values are written at the register pointed by the address, immediately upon reception of the byte. The address counter is automatically incremented. Subsequent data bytes are treated in continuation of the writing operation.



S 0x5C WA	0x33	A power mode A	INT mode	A INT width	AP
S 0x5C	0,00	power_mode	INT_INODE	_wigan	$\sim$

Write mode setting operation .

Address	Туре	Name	Description	Category
0	char	touching	Bitfield, see table 10	
1	char	buttons	Buttons bitfield	1
2 (lsb)	int	posx1	Finger #1 X position	1
3 (msb)				
4 (Isb)	int	posy1	Finger #1 Y position	1
5 (msb)				
6	char	id1	Finger #1 identificator	
7 (Isb)	int	posx2	Finger #2 X position	7
8 (msb)				
9 (lsb)	int	posy2	Finger #2 Y position	
10 (msb)				touch
11	char	id2	Finger #2 identificator	louen
12 (Isb)	int	posx3	Finger #3 X position	
13 (msb)				
14 (Isb)	int	posy3	Finger #3 Y position	
15 (msb)				
16	char	id3	Finger #3 identificator	
17 (Isb)	int	posx4	Finger #4 X position	
18 (msb)				
19 (lsb)	int	posy4	Finger #4 Y position	
20 (msb)				
21	char	id4	Finger #4 identificator	
22 (Isb)	int	posx5	Finger #5 X position	
23 (msb)				
24 (Isb)	int	posy5	Finger #5 Y position	
25 (msb)				
26	char	id5	Finger #5 identificator	
27	char	strength1	Finger #1 strength	1
28	char	strength2	Finger #2 strength	1
29	char	strength3	Finger #3 strength	4
30	char	strength4	Finger #4 strength	
31	char	strength5	Finger #5 strength	

#### Note1: MSI Registers



Bit 0,1,2	Nb of fingers touching (NBF)	
Bit 3	Noise flag (indicates the report is unreliable) (NOI)	
Bit 4	message flag (indicates a message string is sent by slave) (MSG)	
Bit 5	buffer indicates the master has missed more than 2 reports, which are	
	stored in buffer array (BUF)	
Bit 6	palm flag (indicates the algorithm has a palm or similar blocking issue) (PAL)	
Bit 7	water flag, indicates the algorithm has a rejected inputs due to water (WAT)	

Address	Туре	Name	Description	Category
32 (lsb) 33 (msb)	int	initial_distance	Distance separating fingers on the first time multitouch is detected	gesture
34 (lsb) 35 (msb)	int	distance	Distance separating fingers	
36 (lsb) 37 (msb)	int	ratio	100.distance / initial_distance	
38	char	water_level		
39	char	noise_level		1
40	char	palm_level		monitor
41	char	signal_x		]
42	char	signal_y		]
43	char	button1	Signal level of the buttons	buttons
50		button8		
51	char	power_mode	Power management register. See §2.2.3 and table 16	nower
52	char	INT_mode	Control of the ATTb pin, see §2.2.4 and table 17	power management
53	char	INT_width	ATTb pulse width	
54-57	char		reserved for future use	1
58	char	SPECOP	Special operation . See table 13	special
59 (lsb) 60 (msb)	int	EEPROM_read_ad	Address used during special operation	operations
61	char	Engineering_cmd	Allows, with I <sup>2</sup> c, to send "hyperterminal like commands" for engineering modes	
62 (lsb) 63 (msb)	int	CRC	FLASH CRC value (must be requested by SPECOP), excluding "EEPROM" zone	version
64-95	char	version[031]	Customer version control (32bytes) (imap to "eeprom")	



96-135	char	message[039]	Null terminated ASCII message string for engineering and debug purpose	
136 (lsb) 137 (msb)	int	RAW_CTRL	Controls RAW data mode (internal, raw, etc) see table 14	
138	char	cross_x	X coordinate for method 1 crossing node measurement request	method 1
139	char	cross_y	Y coordinate for method 1 crossing node measurement request	metriod i
140 (lsb) 142 (msb)	int	cross_node	Measurement result for method 1	
142 (lsb) 143 (msb)	int	RAW[069]	Raw data, content controlled by RAW_CTRL register, or alternatively, history buffer (see	RAW data
144 (lsb) 145 (msb)	int	shared with	below)	
etc.	int	history_buffer		

0	Normal operation
1	"EEPROM" read operation, start address must be written in EEPROM_read_addr
2	"EEPROM" write operation NOT IMPLEMENTED
3	Calibration
4	CRC checksum of the application in Flash

Bit 0	Chapped function (0: history buffer 1: RAW/ data 2: system info) See table 1	
Bit 1	Choose function (0: history buffer, 1: RAW data, 2: system info) See table 15	
Bit 2	Method (0 0r 1)	
Bit 3	Show offset correction (and low-pass fiter for M0)	
Bit 4	Show m0 sensitivity adjustment (bit3 must also be set)	
Bit 5	M1 pattern small (0) or pattern large (1)	
Bit 6	M1 sense direction (0:Y,1:X)	
Bit 7	M1 band scan. if 0, only report a single cross node. If 1, report a full X axis	
	scan at RAW position	
Bit 8	Disable Algorithm	
Bit 9	Enable single shot RAW refresh, must be set to 1 and bit9 to 0. Auto back to	
	0 and bit9 to 1 after single shot is done	
Bit 10	Refresh frozen after single shot is done when 1. Set to 0 to release the	
	freeze and go back to normal refreshing	
Bit 11		
Bit 12		
Bit 13		
Bit 14		
Bit 15		



Address	Туре	Name	Description	Category
142	char	interval	Subsampling rate when filling the history buffer. Disable: 0. Keep all points: 1. Keep one out of two: 2. Etc.	history buffer
143	char	buffer_level	Number of fingers report in the buffer	
144 (lsb) 145 (msb)	int	posx	Coordinate X of the reported point, at time=0	
146 (lsb) 147 (msb)	int	posy	Coordinate Y of the reported point, at time=0	
148 (lsb) 149 (msb)	int	posx	Coordinate X of the reported point at time=1	
150 (lsb) 151 (msb)	int	posy	Coordinate Y of the reported point at time=1	
298 (lsb) 299 (msb)	int	posx	Coordinate X of the reported point, at time=19	
300 (lsb) 301 (msb)	int	posy	Coordinate Y of the reported point, at time=19	

## 4.3 Operating Mode Register

## 4.3.1 POWER\_MODE Register

Address	Name Description of POWER_MODE Register	
7-4		Refer to ALLOW_SLEEP function description
		Idle_period_time = k * 16 * Active_scan_period_time [s], with
	IDLE_PERIOD[3-0]	k = value of IDLE_PERIOD[3-0]
		Active_scan_period_time = duration [s] of a scan period in active
		mode.
3	-	Not used
		Allow self demotion from active to sleep mode, provide that this
		flag is set. If the MSI device is in active mode and no fingers is
2	ALLOW_SLEEP	detected for more than IDLE_PERIOD time, then it allow AUTO
		JUMP to sleep mode. If this flag is not set, the host must
		explicitly switch the device from active to sleep mode.
		Power mode setting of the MSI device:
		00:Active Mode
1-0	POWER_MODE[1-0]	01:Sleep Mode
		10:Deep Sleep Mode
		11:Freeze Mode



Address	Name Description		
7-4	-	Not used	
3	EN_INT	0:disable interrupt mode 1:enable interrupt mode	
2	INT_POL 0:the interrupt is low active(default) 1:the interrupt is high active		
1-0	INT_MODE[1-0]	00:INT assert periodically 01:INT assert only when finger moving 10:INT assert only when finger touch(default)	

#### 4.3.2 INT\_MODE Register

#### 4.3.3 Power management

#### Active mode

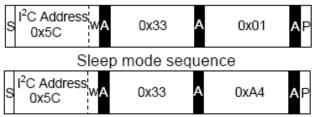
In this mode, the slave resumes with a new scan directly after each I<sup>2</sup>C transfer (after INT rising edge). This is used to reach the highest refresh rate, but also has the highest current consumption. Below shows how to force the slave into Active mode.



Active mode sequence

#### Sleep mode

This mode is selected to decrease the current consumption during low activity phases on the sensor, which need a lower refresh rate(10Hz). The MSI can automatically switch to Active mode(when finger is detected, provided that ALLOW\_SLEEP bit is set in the POWER\_MODE register) or by set POWER\_MODE register. Also, the MSI can automatically switch from Active to Sleep mode when no finger is detected for more than IDLE\_PERIOD time, provided that ALLOW\_SLEEP bit is set in the POWER\_MODE register. Figure 44 shows how to force the slave into Sleep mode. Below shows how to force the slave into Sleep mode can automatically switch, provided IDLE\_PERIOD=10.



Sleep mode automatically switch sequence

#### Deep Sleep mode

This mode is selected to achieve the minimum consumption during very low activity phases on the sensor, which need a lowest refresh rate (1Hz). The MSI only can switch to Deep Sleep mode by set POWER\_MODE register. Below shows how to force the slave into Deep Sleep mode.





Deep Sleep mode sequence

#### Freeze mode

In this mode, the slave MCU internal clock source is stopped, and consumption is only MOS leakage. Below shows how to force the slave into Freeze mode. There are two ways to wake up from freeze mode.

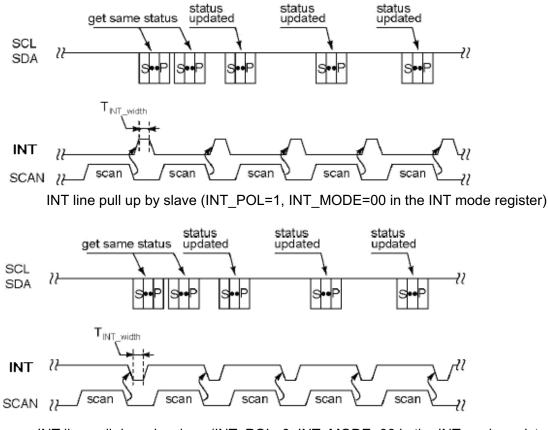
- RST pin pull down (connect to the Ground) (default)
- INT pin change ("1 to 0" or "0 to 1")



Freeze mode sequence

#### 4.3.4 Transition of INT line

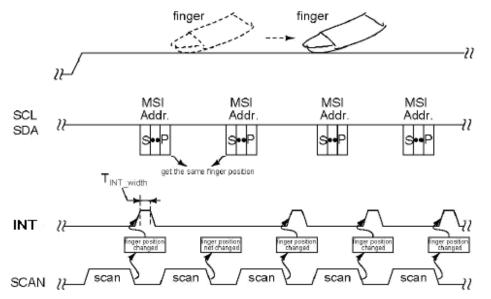
When INT\_MODE=00 in the INT MODE register, the slave will set the INT line with INT\_width pulse width after each scan in order to request the attention from the host, as shown in below



INT line pull down by slave (INT\_POL=0, INT\_MODE=00 in the INT mode register)

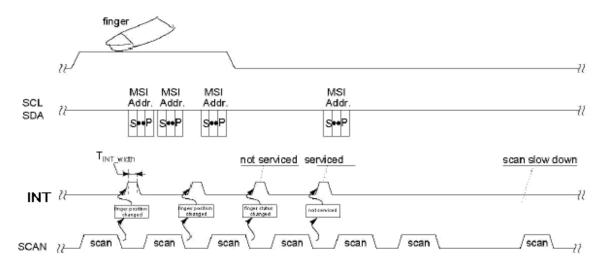


When INT\_Mode=01 in the INT mode register and finger moving on the panel, the slave will set The INT line after each scan, as shown in below.



INT line pull up when finger moving (INT\_POL=1, INT\_MODE=01 in the INT mode register)

When fingers leaves the panel, the slave will continue to pulse INT line for each scan; but once the master has serviced this request and become now aware that there is no more finger touching, the slave will stop pulse the INT line, and will also gradually reduce the scan speed, as shown in below

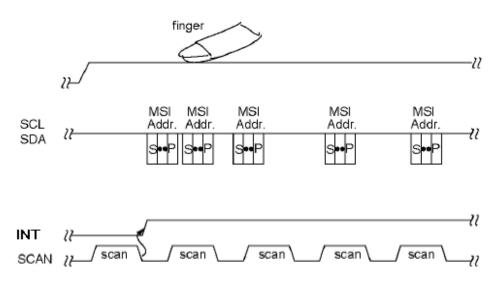


INT line will stop pulse when finger leaves and master has acknowledge the situation (INT\_POL=1 in the INT mode register)

When INT\_Mode=10 in the INT mode register and finger touch the panel, the slave will set The INT line after each scan as shown in below.

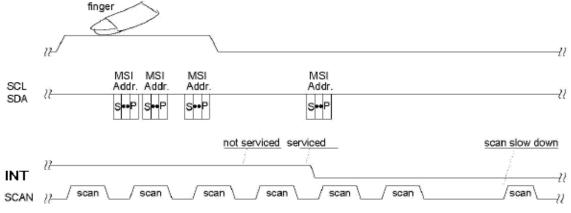






INT line pull up when finger touch (INT\_POL=1, INT\_MODE=10 in the INT mode register)

When fingers leaves the panel, the slave will continue keep INT line status for each scan; but once the master has serviced this request and become now aware that there is no more finger touching, the slave will release the INT line, and will also gradually reduce the scan speed, as shown in below



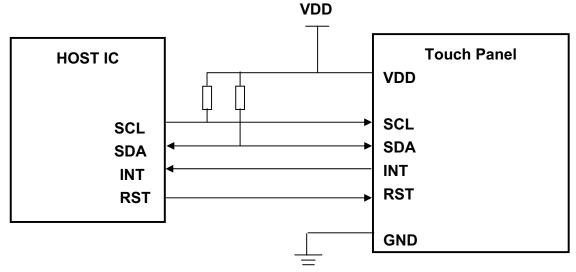
INT line will stop pulse when finger leaves and master has acknowledge the situation (INT\_POL=1 in the INT mode register)



#### **5. PIN CONNECTIONS**

No.	Name	I/O	Description		
1	VDD	Р	Power; VDD=3.3V(typ.)		
2	GND	Р	Ground		
3	RST	I	Reset		
4	SCL	I	Clock; 100KHz		
5	SDA	I/O	Serial data access		
6	INT	0	Active low when data output from touch panel		
7	NC	-	No Connect		
8	NC	-	No Connect		

#### 6. BLOCK DIAGRAM



Note : 1. USE APPROPRIATE RESISTOR VALUE DURING HIGH SPEED SCL CLOCK.

 $\label{eq:suggestion:resistor recommendation: 1K ohm. \\$ 

2. To reduce the noise from the power, we suggest you use the independent power for the touch panel (VDD)



# RELIABILITY TEST

No.	Test Item	Test Condition	Inspection after test
1	High Temperature Storage	$80\pm2^{\circ}C/240$ hours	Note1,Note4
2	Low Temperature Storage	$-30\pm2^{\circ}C/240$ hours	Note1,Note4
3	High Temperature Operating	$70\pm2^{\circ}C/240$ hours	Note2,Note4
4	Low Temperature Operating	$-20\pm2^{\circ}C/240$ hours	Note2,Note4
5	Temperature Cycle storage	-30±2°C~25~80±2°C×100cycles (30min.) (5min.) (30min.)	Note4
6	Damp proof Test operating	$60^{\circ}\text{C} \pm 5^{\circ}\text{C} \times 90\%$ RH/240 hours	Note4
7	Vibration Test	Frequency: 10~55Hz Stroke:1.5mm Sweep: 10Hz~55Hz~10Hz 2hours for each direction of X.Y.Z (6 hours for total)	
8	Package drop test	Height:60 cm 1 corner,3 edges,6 surfaces	
9	ESD test	±2KV, Human body mode,100pF	
10	Mechanical shock	100G 6ms, $\pm X$ , $\pm Y$ , $\pm Z$ 3 times for each direction	
11	Package vibration test	Random vibration :0.15G*G/HZ from 5-200 HZ,-6dB/Octave from 200-500HZ of each direction of X.Y. Z (6 hours for total)	

Note 1: Ta is the ambient temperature of samples.

Note 2: Ts is the temperature of panel's surface.

Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.



## ■ INSPECTION CRITERION

	OUTGOING QUALITY STANDARD	PAGE 1 OF 8
<b>FITLE</b> ·FUNCTION	ONAL TEST & INSPECTION CRITERIA	

This specification is made to be used as the standard acceptance/rejection criteria for Wider Screen TFT-LCD module product.

# 1. Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993, normal level 2 and based on:

Major defect: AQL 0.65

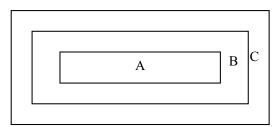
Minor defect: AQL 1.5

## 2. Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of  $20\sim40W$  light intensity, all directions for inspecting the sample should be within  $45^{\circ}$  against perpendicular line.

## **3.** Definition of Inspection Item.

3.1 Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

ZoneB+ZoneC= Around opaque edge area on TP.

Fig.1 Inspection zones in an LCD.

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

#### 3.2 Definition of some visual defect

Bright dot.	Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern.
Dark dot.	Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue picture, or pure whiter picture.
Dark / Bright Lines.	Lines on display which appear dark/bright and usually result from the contamination.



#### OUTGOING QUALITY STANDARD

#### PAGE 2 OF 8

#### TITLE: FUNCTIONAL TEST & INSPECTION CRITERIA

#### 4. Major Defect

Item No	Items to be inspected	Inspection Standard	Classification of defects
4.1	All functional defects	<ol> <li>No display</li> <li>Display abnormally</li> <li>Open or missing segment</li> <li>Short circuit</li> <li>Excess power consumption</li> <li>Back-light no lighting, flickering and abnormal lighting.</li> </ol>	
4.2	Missing	Missing component	Major
4.3	Outline dimension	Overall outline dimension beyond the drawing is not allowed.	
4.4	Crack	Creaks tend to break are not allowed.	

#### 5. Minor Defect

Item No	Items to be inspected	Inspection Standard					Classification of defects
	Bright dot. defect.	Zone			Qty		
		Size(mm)		A	В	C	
5.1	O↓y	Φ≤0.15		ceptable (c f spot not a	•	, Acceptable	
	$\Phi = (x+y)/2$	$0.15 \! < \! \Phi \! \leqslant \! 0.25$		N≤€	<b>.</b>		
		$0.25 < \Phi \le 0.50$		N≤2	2		
							Minor
		Zone		Acceptable Q'ty			
		Size(mm)		А	В	С	
5.2	Dark dot defect.	Φ ≤0.15		Accepta	able		
		0.15<Φ≤0.3	0	N≤	6	Acceptable	
		0.30<Φ≤0.	50	) N≤4			
5.3	Bright / Dark line.	$0.01 < W \leq 0.10,$ $N \leq 1$		.30 < L ≤	≤ 1.50,	Acceptable	
2 3	. Minimum d	ve dots shall not exce istance between def dark sub pixel defec L: Length, N: Count	fective t or l	ve dots is 1			han 1pair.



# OUTGOING QUALITY STANDARD

### PAGE 3 OF 8

## TITLE: FUNCTIONAL TEST & INSPECTION CRITERIA

Item No	Items to be inspected		Classification of defects				
	Linear defect	Size( m) Acceptable Qty				Minor	
	Foreign material under polarizer,	L(Length)	W(Width)	A	Zone B	C	
		Ignore	W≤0.05	Acce	ptable	Ac	
		L≪5.0	0.05 <w≤0.1< td=""><td>5 1</td><td>N≤5</td><td>Acceptable</td><td></td></w≤0.1<>	5 1	N≤5	Acceptable	
5.4		5.0≤L	0.15≤W		0	le	_
5.4	Circular Defect,						Minor
	Foreign material under polarizer,	Zor	ne A	cceptab	le Q'ty		
	y y	Size(mm)	A	В		С	
		Φ≤0.25	Accer	otable			
<b>«</b> X-	<b>≪→</b>	$0.25 < \Phi \le 0.5$	50 N≤	N≤4		eptable	
	$\Phi = (x+y)/2$	0.50≤Φ	(	0			
		dimension (ii) Incomplete is not alle 5.4.2 Dirt on po	n position should e covering of the v owed. larizer can be wiped ea	iewing an	rea due to s	hifting	Minor
5.5	Polarizer	Sizes(mm)	1	Acceptable Qty			
	defect.			Zon	e		
			A	В	C		
		Φ<0.2	5 Accep	table			
		0.25≤Φ≤	0.5 N≤	≦4	Accepta	able	
		Φ>0.5	0				
			· · · · ·				



OUTGOING QUALITY STANDARD

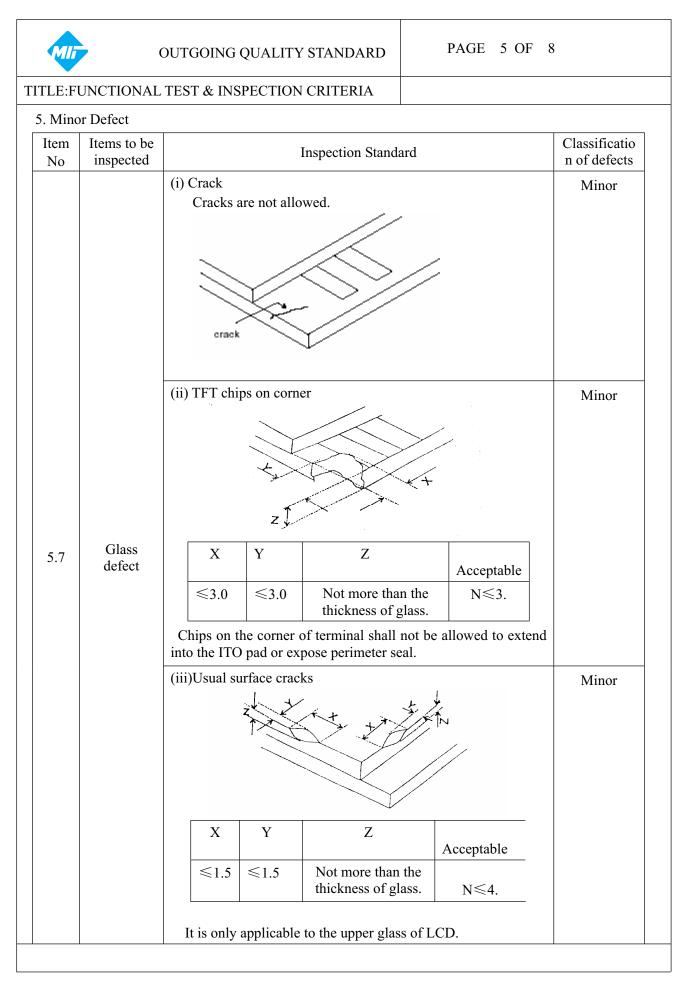
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### TITLE: FUNCTIONAL TEST & INSPECTION CRITERIA

5. Minor Defect
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Item No	Items to be inspected	Inspection Standard					Classificatio of defects
		5.4.4Air bubbles between glass & polarizer:					Minor
				Acce	ptable	Qty	
		Size	(mm)		Zone	me	
			ŀ	A B	3	С	
		Φ <	≤0.3 A	Acceptable	e		
		0.3<	$\Phi \leq 1.0$	3		<b>A</b> a contable	
		1.0<	Φ≤1.5	1		Acceptable	
		Φ.2	>1.5	0			
5.6		1000-00			0.100.0	amonial am	~1~
5.6		judge	ze(mm)			special an ble Qty	igle,
5.6		judge	by the following ze(mm)			ble Qty	igle,
5.6		judge	by the following		ccepta	ble Qty	ıgle,
5.6		judge	by the following ze(mm)	Ac	ccepta Zoi B	ble Qty ne	igle,
5.6		judge Si L(Length)	by the following ze(mm) W(Width) W  le 0.02	Ac A Igno	ccepta Zoi B ore	ble Qty ne	igle,
5.6		judge Si L(Length) Ignore	by the following ze(mm) W(Width) W  le 0.02	Ac A Igno	Zor B ore	ble Qty ne C	ıgle,







M		OUTGOING QU.	ALITY STANDAR	D	PAGE 6 (	DF 8	
TITLE:F	UNCTIONAL	TEST & INSPEC	CTION CRITERIA				
	Cosmetic Defe	et.					
Item No	Items to be inspected		Inspection Star	ndard		Classification of defects	
		For dark/white spot, size $\Phi$ is defined as $\Phi = \frac{(x+y)}{2}$					
	Black and		Zone Ac	ceptable Qty			
	white Spot defect	Size(mm)	A	B+C			
6.1	Foreign	Φ≤0.15	Ign	ore		Minor	
	Particle,	0.15<Φ≤0	0.25 6	5	distance 5mm		
		0.25<Φ≤0	0.50 4	Ļ	over		
				Φ>0.5	0	)	
		Total defectiv TP.	e dots shall not exc	ceed 6 pcs on	the same		
Item No	Items to be inspected	Inspection Standard			Classification of defects		
		Siz	e(mm)	Accepta	ble Qty		
				Zo	~ •		
	Black line, White	L(Length)	W(Width)	A B+C			
	line, Scratch,	Ignore	W≤0.03	Ignore			
6.2	Foreign	L≤5.0	0.03 <w≤0.05< td=""><td>5</td><td>distance</td><td>Minor</td></w≤0.05<>	5	distance	Minor	
	material under	L≤5.0	0.05 <w≤0.1< td=""><td>2</td><td>- 5mm over</td><td></td></w≤0.1<>	2	- 5mm over		
	film,		0.1 <w< td=""><td>0</td><td></td><td></td></w<>	0			
	1					1	



Mli		OUTGOING QUALITY STANDARD	PAGE 7 OF	7 8
		TEST & INSPECTION CRITERIA		
5. TP C	Cosmetic DefeItems to be			Classification of
No	inspected	Inspection Standard		defects
6.3	TP defect	(i) Chips on corner X $Y$ $TX(mm)$ $Y(mm)\leq 3.0 \leq 3.0(ii)Usual surface cracksX(mm)$ $Y(mm)\leq 6.0 < 2.0$	Z(mm) Z <t< td=""><td>Minor</td></t<>	Minor
		(iii) Crack Cracks tending to break are not allowed.		Major
6.4	Total number of dots	The total number of luminous dots, dark do particles, bubbles, scratch defects, pinholes 10 /piece on the same TP.		



OUTGOING QUALITY STANDARD

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## TITLE: FUNCTIONAL TEST & INSPECTION CRITERIA

Item No	Items to be inspected	Inspection Standard	Classification of defects
1	Difference in Spec.	None allowed	Major
2	Pattern peeling	No substrate pattern peeling and floating	Major
3	Soldering defects	No soldering missing No soldering bridge No cold soldering	Major Major Minor
4	Resist flaw on Printed Circuit Boards	visible copper foil (Ø0.5mm or more) on substrate pattern.	Minor
5	Accretion of metallic Foreign matter	No accretion of metallic foreign matters (Not exceed $\emptyset$ 0.2mm).	Minor Minor
6	Stain	No stain to spoil cosmetic badly.	Minor
7	Plate discoloring	No plate fading, rusting and discoloring.	Minor
8	Solder amount 1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly. (too much) b. Components side (In case of 'Through Hole PCB')	Minor
	2. Flat packages	Solder to reach the Components side of PCB. Either 'Toe' (A) or 'Seal' (B) of the lead to be covered by 'Filet'. Lead form to be assume over solder.	Minor
	3. Chips	$(3/2) H \ge h \ge (1/2) H$	Minor
9	Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad $h \ge 0.13$ mm. The diameter of solder ball d $\le 0.15$ mm. d	Minor
		b. The quantity of solder balls or solder. Splashes isn't beyond 5 in 600 mm <sup>2</sup> . $\bigcirc$ h	Minor
		c.Solder balls/Solder splashes do not violate minimum electrical clearance.	Major
		d.Solder balls/Solder splashes must be entrapped / encapsulated or attached to the metal surface .	Minor
		Note: Entrapped/encapsulated/attached is intended to mean that normal service environment of the product will not cause a solder ball to become dislodged.	



# ■ PRECAUTIONS FOR USING LCD MODULES

#### **1 Handing Precautions**

- 1.1 The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- 1.2 If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- 1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- 1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in to contact with room temperature air.
- 1.5 If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
  - Isopropyl alcohol
  - Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

- 1.6 Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
  - Water
  - Ketone
  - Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contact with oil and fats.

- 1.7 Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets moisture condensation or a current flow in a high-humidity environment
- 1.8 Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- 1.9 Do not attempt to disassemble or process the LCD module.
- 1.10 NC terminal should be open. Do not connect anything.
- 1.11 If the logic circuit power is off, do not apply the input signals.
- 1.12 Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- Before removing LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.



- Tools required for assembling, such as soldering irons, must be properly grounded. Make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.

- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dry. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.

The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

1.13 Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- Do not alter modify or change the shape of the tab on the metal frame.

- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.

- Do not damage or modify the pattern writing on the printed circuit board.

- Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.

- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.

- Do not drop, bend or twist the LCM.



- 2 Handling precaution for LCM
  - 2.1 LCM is easy to be damaged. Please note below and be careful for handling.
  - 2.2 Correct handling:





As above picture, please handle with anti-static gloves around LCM edges.

#### 2.3 Incorrect handling:



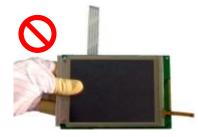
Please don't stack LCM.

Please don't stretch interface of output, such as FPC cable.



Please don't operate with sharp stick such as pens.

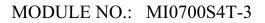




Please don't hold the surface of panel.



Please don't hold the surface of IC.



### **3** Storage Precautions

- 3.1 When storing the LCD modules, the following precaution are necessary.
  - 3.1.1 Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.
  - 3.1.2 Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
  - 3.1.3 The polarizer surface should not come in contact with any other objects (We advise you to store them in the anti-static electricity container in which they were shipped).
- 3.2 Transportation Precautions
  - 3.2.1 During shipment, please handle with care. The packaging bag can not be broken, step on trap. Packaging Carton layer height can not be over two meters.
  - 3.2.2 The transportation process should pay attention to the waterproof and moisture-proof measures. Product can not be watering. Ethylene sealed bags can not be unsealed.

#### 3.3 Others

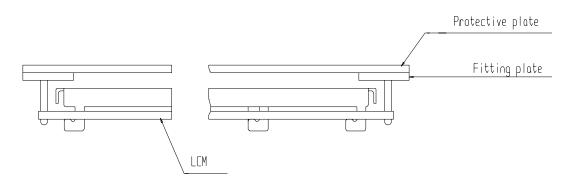
- 3.3.1 Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.
- 3.3.2 If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- 3.3.3 To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.
  - 3.3.3.1 Exposed area of the printed circuit board.
  - 3.3.3.2 -Terminal electrode sections.

#### 4 USING LCD MODULES

4.1 Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

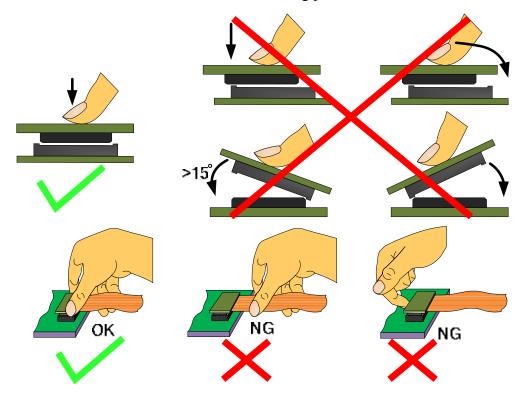
4.1.1 Cover the surface with a transparent protective plate to protect the polarizer and LC cell.





- 4.1.2 When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be  $\pm 0.1$  mm
- 4.2 Precaution for assemble the module with BTB connector:

Please note the position of the male and female connector position, don't assemble or assemble like the method which the following picture shows







**4.3** Precaution for soldering the LCM

	Manual soldering	Machine drag soldering	Machine press soldering
No RoHS Product	290°C ~350°C. Time : 3-5S.	330°C ~350°C. Speed : 4-8 mm/s.	300°C ~330°C. Time : 3-6S. Press: 0.8~1.2Mpa
RoHS Product	340°C ~370°C. Time : 3-5S.	350°C ~370°C. Time : 4-8 mm/s.	330°C ~360°C. Time : 3-6S. Press: 0.8~1.2Mpa

- 4.3.1 If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation (This does not apply in the case of a non-halogen type of flux). It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters
- 4.3.2 When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- 4.3.3 When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.
- 4.4 Precautions for Operation
  - 4.4.1 Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.
  - 4.4.2 It is an indispensable condition to drive LCD s within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided
  - 4.4.3 Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operating temperature.
  - 4.4.4 If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
  - 4.4.5 A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50%RH or less is required
  - 4.4.6 Input logic voltage before apply analog high voltage such as LCD driving voltage when power on. Remove analog high voltage before logic voltage when power off the module. Input each signal after the positive/negative voltage becomes stable.
  - 4.4.7 Please keep the temperature within the specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity
- 4.5 Safety
  - 4.5.1 It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol which should later be burned
  - 4.5.2 If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water



Unless agreed between Multi-Inno and the customer Multi-Inno will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with Multi-Inno LCD acceptance standards (copies available upon request) for a period of one year from date of production. Cosmetic/visual defects must be returned to Multi-Inno within 90 days of shipment. Confirmation of such date shall be based on data code on product. The warranty liability of Multi-Inno limited to repair and/or replace on the terms set forth above. Multi-Inno will not be responsible for any subsequent or consequential events.

- 4.7 Return LCM under warranty
  - 4.7.1 No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :
    - 4.7.1.1 Broken LCD glass.
    - 4.7.1.2 PCB eyelet is damaged or modified.
    - 4.7.1.3 -PCB conductors damaged.
    - 4.7.1.4 Circuit modified in any way, including addition of components.
    - 4.7.1.5 PCB tampered with by grinding, engraving or painting varnish.
    - 4.7.1.6 Soldering to or modifying the bezel in any manner.
  - 4.7.2 Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet, conductors and terminals.

# **PACKING SPECIFICATION**

Please consult our technical department for detail information.

# PRIOR CONSULT MATTER

- 1 For Multi-Inno standard products, we keep the right to change material, process ... for improving the product property without prior notice to our customer.
- 2 For OEM products, if any changes are needed which may affect the product property, we will consult with our customer in advance.
- 3 If you have special requirement about reliability condition, please let us know before you start the test on our samples.