

# MULTI-INNO TECHNOLOGY CO., LTD.

www.multi-inno.com

# **LCD MODULE SPECIFICATION**

Model : MI0570KT

# For Customer's Acceptance:

Customer	
Approved	
Comment	

Revision	1.4
Engineering	
Date	2008-05-21
Our Reference	



# **REVISION RECORD**

REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2007-02-07	First Release	
1.1	2007-07-08	Modify input signal timing specification	
1.2	2007-08-09	Add LED life time 25000 hours(typ.)	
1.3	2007-11-29	Modify LED life time	
1.4	2008-05-21	Modify chromaticity data	



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# ■ GENERAL INFORMATION

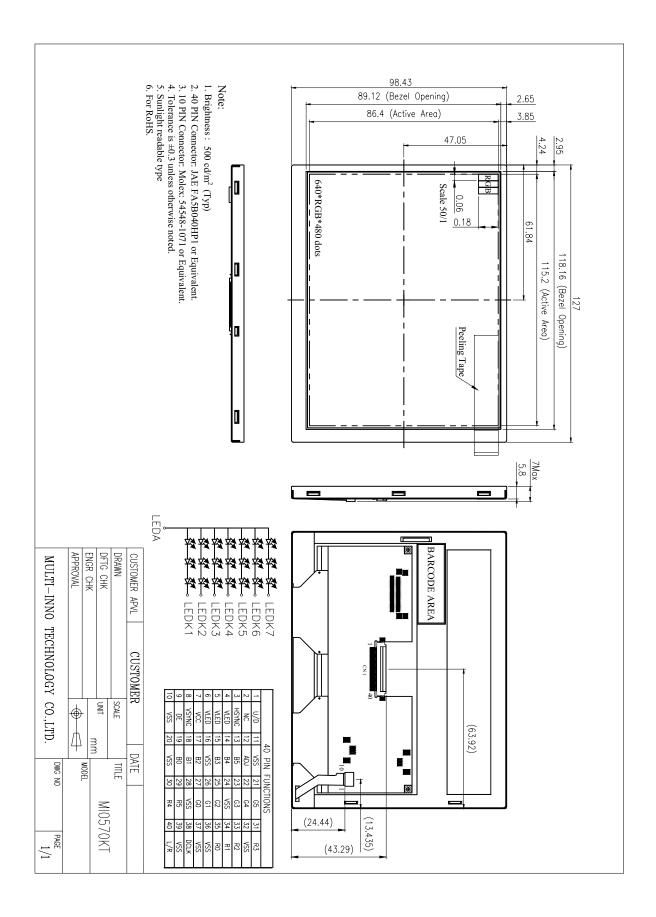
Item	Contents	Unit
LCD type	TFT	/
Size	5.7	Inch
Viewing direction	6:00	O' Clock
Gray scale inversion direction	12:00	O'Clock
$LCM(W \times H \times D)$	127.00×98.43×7.00	mm <sup>3</sup>
Active area (W×H)	115.20×86.40	mm <sup>2</sup>
Dot pitch (W×H)	0.06×0.18	mm <sup>2</sup>
Number of dots	640 (RGB) × 480	/
Backlight type	21 LEDs	/
Interface type	RGB 18 bits	/
Color depth	262K	/
Color configuration	R.G.B stripe	/
Surface treatment	Clear	/
Input voltage	3.3	V
With/Without TSP	Without TSP	/
Weight	110	g

Note 1:Viewing direction for best image quality is different from TFT definition, there is a 180 degree shift. Note 2 : RoHS compliant;

Note 3: LCM weight tolerance:  $\pm$  5%.



# EXTERNAL DIMENSIONS





# ■ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit
Power supply voltage	VCC	-0.3	5.0	V
Logic input voltage	VI	-0.3	VCC+0.3	V
Operating temperature	Тор	-20	70	°C
Storage temperature	Тѕт	-30	80	°C
Humidity	RH	-	90%(Max60°C)	RH

# **ELECTRICAL CHARACTERISTICS**

## DC CHARACTERISTICS

Parameter	Symbol	Min	Тур	Max	Unit
Power supply voltage for LCD	VCC	3.0	3.3	3.6	V
Power supply current for LCD	ICC	-	111	140	mA
Power supply voltage for LED	VLED	4.5	5	5.5	V
Power supply current for LCD	I led	-	333	400	mA
Ripple voltage	V <sub>RF</sub>	-	-	100	mVp-p
Input voltage ' H ' level	Vih	0.7VCC	-	VCC	V
Input voltage ' L ' level	VIL	0	-	0.3VCC	V
ADJ frequency	-	19K	20K	21K	Hz
ADJ input voltage	Vih	3.0	-	3.3	V
ADJ input vonage	Vil	0	-	0.3	V
LED life time	-	-	50,000	-	Hr

Note 1: The "LED dice life time" is defined as the brightness decrease to 50% original brightness that the ambient temperature is  $22^{\circ}$ C and LED dice current=20mA.



Item		Symbol	Condition	Min	Тур	Max	Unit	Remark	Note	
Response time		Tr+Tf		-	50	-	ms	FIG 1.	4	
Contrast r	atio	Cr	θ=0°	200	300	-		FIG 2.	1	
Luminar uniform		δ WHITE	Ø=0° Ta=25℃	70	80	-	%	FIG 2.	3	
Surface Lum	inance	Lv		450	500	-	cd/m <sup>2</sup>	FIG 2.	2	
Viewing angle range			$\emptyset = 90^{\circ}$	50	60	-	deg	FIG 3.		
		θ	$\emptyset = 270^{\circ}$	30	40	-	deg	FIG 3.	6	
		Ø	$\emptyset = 0^{\circ}$	60	70	-	deg	FIG 3.		
			$\emptyset = 180^{\circ}$	60	70	-	deg	FIG 3.		
	Red	Х		0.565	0.615	0.665				
	Reu	у		0.310	0.360	0.410				
	Green	X	θ=0°	0.295	0.345	0.395				
CIE (x, y)	Ulteri	У	Ø=0°	0.490	0.540	0.590		FIG 2.	5	
chromaticity	Blue	X	Ta=25℃	0.098	0.148	0.198		110 2.		
	Diuc	У	1 a-25 C	0.056	0.106	0.156				
	White	Х		0.259	0.309	0.359				
	wille	у		0.270	0.320	0.370				
Image sticking	-	tis	2 hours	-	-	2	Sec	-	8	

# **■**ELECTRO-OPTICAL CHARACTERISTICS

Note 1. Contrast Ratio(CR) is defined mathematically as For more information see FIG 2.

Contrast Ratio = <u>Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)</u> Average Surface Luminance with all black pixels (P1, P2, P 3, P4, P5)

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)

Note 3. The uniformity in surface luminance ,  $\delta$  WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see FIG 2.

δ WHITE =Minimum Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)Maximum Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)

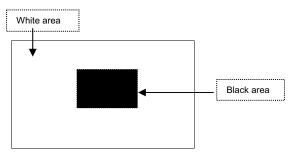
- Note 4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1. The test equipment is Autronic-Melchers's ConoScope. Series.
- Note 5. CIE (x, y) chromaticity, The x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.
- Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the conrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.
- Note 7. For viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments For contrast ratio, Surface Luminance, Luminance uniformity, CIE The test data is base on TOPCON's BM-5 photo detector.



Note 8: Definition of Image sticking (tis):

Continuously display the test pattern shown in the figure below for 2 hours. Then display a completely white screen. The previous image shall not persist more than 2 sec at 25 °C

#### Image sticking pattern



#### FIG. 1 The definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

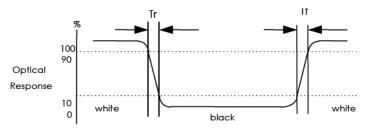
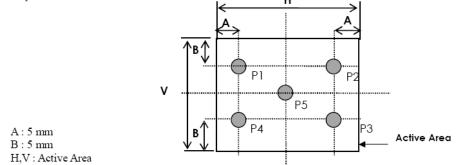
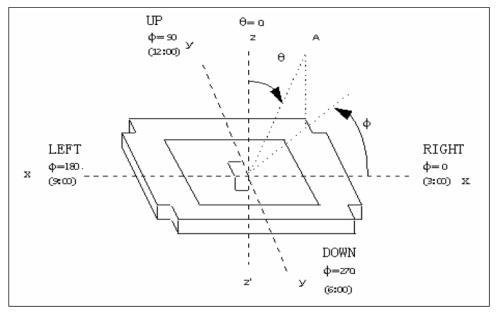


FIG. 2 Measuring method for Contrast ratio, surface luminance, Luminance uniformity , CIE (x, y) chromaticity



Light spot size  $\emptyset$ =7mm, 500mm distance from the LCD surfade to detector lens measurement instrument is TOPCON's luminance meter BM-5

#### FIG. 3 The definition of viewing angle





## ■ INTERFACE DESCRIPTION

Pin NO.	SYMBOL	DESCRIPTION
1	U/D	Up or Down Display Control
2	NC	No Connection
3	Hsync	Horizontal SYNC.
4	VLED	Power Supply for LED Driver circuit
5	VLED	Power Supply for LED Driver circuit
6	VLED	Power Supply for LED Driver circuit
7	Vcc	Power Supply for LCD
8	Vsync	Vertical SYNC.
9	DE	Data Enable
10	VSS	Power Ground
11	VSS	Power Ground
12	ADJ	Brightness control for LED B/L
13	B5	Blue Data 5 (MSB)
14	B4	Blue Data 4
15	B3	Blue Data 3
16	Vss	Power Ground
17	B2	Blue Data 2
18	B1	Blue Data 1
19	B0	Blue Data 0 (LSB)
20	Vss	Power Ground
21	G5	Green Data 5 (MSB)
22	G4	Green Data 4
23	G3	Green Data 3
24	Vss	Power Ground
25	G2	Green Data 2
26	G1	Green Data 1
27	G0	Green Data 0 (LSB)
28	Vss	Power Ground
29	R5	Red Data 5 (MSB)
30	R4	Red Data 4
31	R3	Red Data 3
32	Vss	Power Ground
33	R2	Red Data 2
34	R1	Red Data 1
35	R0	Red Data 0
36	VSS	Power Ground
37	VSS	Power Ground
38	DCLK	Clock Signals ; Latch Data at the Falling Edge
39	Vss	Power Ground
40	L/R	Left or Right Display Control

Remarks :

ADJ is brightness control Pin. The larger of the pulse duty is, the higher of the brightness.
 ADJ signal is 0~3.3V.Operation frequency is 20KHz
 VSS PIN must be grounding, can not be floating.



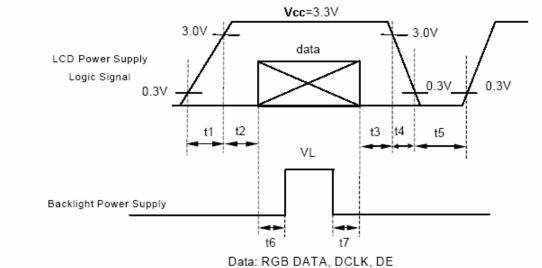
#### 4) U/D and L/R control Function

L/R	U/D	Function					
1	1 0 Normally display						
0	0	Left and Right opposite					
1	1	Up and Down opposite					
0	1	Left and Right opposite <sup>,</sup> Up and Down opposite					

5) If DE signal is fixed low, SYNC mode is used. Otherwise, DE mode is used.

#### 8.1 Power Signal Sequence

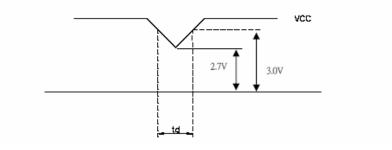
Remarks: \*1) Power Signal sequence:  $t1 \le 10ms$  : 1 sec $\le t5$  $50ms \le t2$  : 200ms  $\le t6$  $0 < t3 \le 50ms$ : 200ms $\le t7$  $0 < t4 \le 10ms$ 



VCC-dip condition:

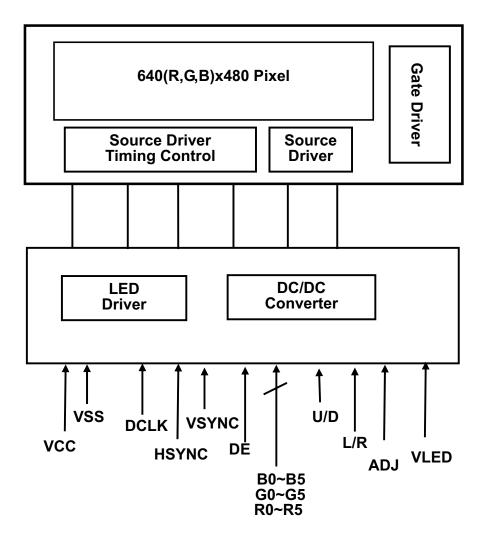
(1) 2.7 V  $\leq$  VCC <3.0V,td  $\leq$  10 ms

(2) VCC> 3.0V,VCC-dip condition should be the same with VCC-turn-on condition  $\circ$ 





## ■ BLOCK DIAGRAM





# ■ APPLICATION NOTES

# 1. INTERFACE SPECIFICATIONS

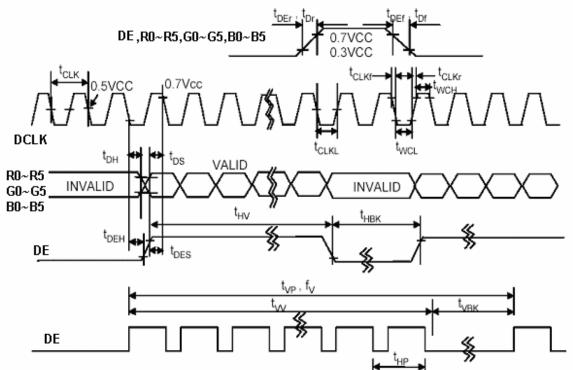
#### **1.1 DE mode Input signal characteristics**

Signal	Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Remarks
DCLK	Period	tськ	33	40	43	ns	
	Frequency	fclk	23	25	30	MHz	
	Low Level Width	t <sub>wcL</sub>	6	-	-	ns	
	High Level Width	t <sub>wch</sub>	6	-	-	ns	
	Rise, Fall Time	t_t_t	-	_	3	ns	
	Duty	-	0.45	0.50	0.55	-	
DE	Setup Time	t <sub>DES</sub>	5	-	-	ns	
(Data	Hold Time	t <sub>DEH</sub>	10	-	-	ns	
Enable)	Rise, Fall Time	t_t DEr, DEf	-	-	16	ns	
	Horizontal Period	t <sub>HP</sub>	750	800	900	t <sub>ськ</sub>	
	Horizontal Valid	t <sub>HV</sub>	640	640	640	t <sub>ськ</sub>	
	Horizontal Blank	t <sub>нвк</sub>	110	160	260	t <sub>ськ</sub>	
	Vertical Period	t <sub>vP</sub>	515	525	560	t <sub>HP</sub>	
	Vertical Valid	t <sub>w</sub>	480	480	480	t <sub>HP</sub>	
	Vertical Blank	t <sub>vвк</sub>	35	45	80	t <sub>HP</sub>	
	Vertical Frequency	f	55	60	65	Hz	
Data	Setup Time	t <sub>DS</sub>	5	-	-	ns	
R,G,B	Hold Time	t <sub>DH</sub>	10	-	-	ns	
	Rise, Fall Time	t_t	-	-	3	ns	

Note: (1) tCLKL / tCLK.



#### 1.1.1 DE mode timing waveform

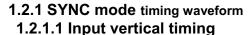


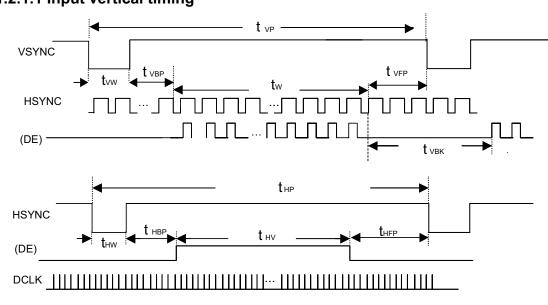
#### **1.2 SYNC mode Input signal characteristics**

Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Remarks
Clock Period	t <sub>clk</sub>	33	40	43	ns	
Clock Frequency	f <sub>ськ</sub>	23	25	30	MHz	
Clock Low Level Width	t <sub>wcL</sub>	6	-	-	ns	
Clock High Level Width	t <sub>wch</sub>	6	-	-	ns	
Clock Rise, Fall Time	tt	-	-	3	ns	
HSYNC Period	t <sub>HP</sub>	750	800	900	t <sub>CLK</sub>	
HSYNC Pulse Width	t <sub>HW</sub>	5	30	-	t <sub>ськ</sub>	
HSYNC Front Porch	t <sub>HFP</sub>	1	16	116	t <sub>ськ</sub>	
HSYNC Back Porch	t <sub>HBP</sub>	1	114	139	t <sub>CLK</sub>	
HSYNC Width + Back Porch	t <sub>HW</sub> +t <sub>HBP</sub>	144	144	144	t <sub>CLK</sub>	
Horizontal Blank	t <sub>нвк</sub>	1	160	260	t <sub>CLK</sub>	
Horizontal Valid	t <sub>HV</sub>	640	640	640	t <sub>CLK</sub>	
VSYNC Period	t <sub>vP</sub>	515	525	560	t <sub>HP</sub>	
VSYNC Pulse Width	t <sub>vw</sub>	1	3	5	t <sub>HP</sub>	
VSYNC Front Porch	t <sub>VFP</sub>	1	10	45	t <sub>HP</sub>	
VSYNC Back Porch	t <sub>VBP</sub>	30	32	34	t <sub>HP</sub>	
VSYNC Width + Back Porch	t <sub>vw</sub> +t <sub>vbp</sub>	35	35	35	t <sub>CLK</sub>	
Vertical Blank	t <sub>vBK</sub>	35	45	80	t <sub>HP</sub>	
Vaild data Width	t <sub>w</sub>	480	480	480	t <sub>HP</sub>	
Data Setup Time	t <sub>DS</sub>	5	-	-	ns	
Data Hold Time	t <sub>DH</sub>	10	-	-	ns	

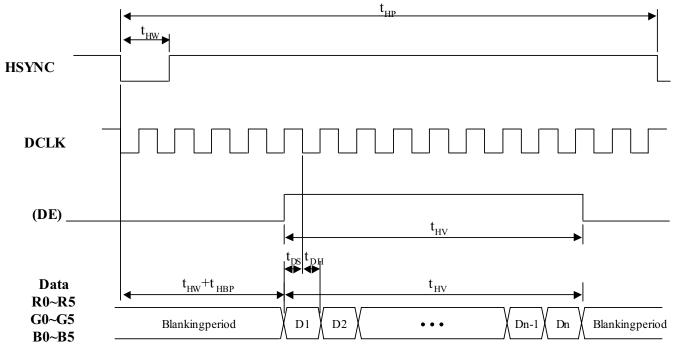
Note: (1) tHBK = tHFP + tHW + tHBP







Remark : If SYNC mode is used, please fix DE signal to low, DE timing waveform is for reference only. **1.2.1.2 Input horizontal timing** 



Remark : If SYNC mode is used, please fix DE signal to low, DE timing waveform is for reference only.



#### 1.3 Color Data Assignment

COLOR	INPUT		F	R DA	TA	•	•		G DATA							B D/	٩ΤΑ		
	DATA	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	В4	В3	B2	B1	B0
		MSB					LSB	MSB					LSB	MSB					LSB
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
BASIC	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
OOLOIN	CYAN	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
RED	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	RED(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
GREEN	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	GREEN(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
BLUE	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	BLUE(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

Remarks:(1) Definition of Gray Scale

color(n):n is series of Gray Scale The more n value is, the bright Gray Scale.

(2)Data:1-High,0-Low

#### Correspondence between Data and Display Position

	S0001	S0002	S0003	S0004	S0005	S0006	S0007	S0008		S1919	S1920
C001	R001	G001	B001	R002	G002	B002	R003	G003		G640	B640
i											
			1	1	1	1	1		I	1	
C480	R001	G001	B001	R002	G002	B002	R003	G003	1	G640	B640



# **RELIABILITY TEST**

No.	Test Item	Test Condition
1	High Temperature Storage	$80\pm2^{\circ}C/240$ hours
2	Low Temperature Storage	$-30\pm2$ °C/240 hours
3	High Temperature Operating	$70\pm2^{\circ}C/240$ hours
4	Low Temperature Operating	$-20\pm2^{\circ}C/240$ hours
5	Temperature Cycle storage	$-30\pm2^{\circ}C\sim25\sim80\pm2^{\circ}C\times200$ cycles (30min.) (5min.) (30min.)
6	Damp proof Test operating	$60^{\circ}\text{C} \pm 5^{\circ}\text{C} \times 90\%$ RH/240 hours
7	Vibration Test (no-operation)	Frequency: 0~55Hz Amplitude:1.5mm Sweep time: 11min 6 cycles for each direction of X.Y.Z
8	ESD test (No operation)	150pF,330Ω Air: ± 12KV;Contact: ± 6KV 10 time/point;4 point/panel face



# ■ INSPECTION CRITERION

	OUTGOING QUALITY STANDARD	PAGE 1 OF 6
TITLE:FUNCTIO	NAL TEST & INSPECTION CRITERIA	

This specification is made to be used as the standard acceptance/rejection criteria for Wider Screen TFT-LCD module product.

#### 1. Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993, normal level 2 and based on:

Major defect: AQL 0.65

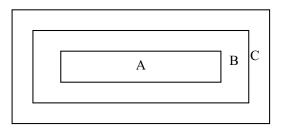
Minor defect: AQL 1.5

#### 2. Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45° against perpendicular line.

#### **3.** Definition of Inspection Item.

3.1 Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

ZoneB+ZoneC= Around opaque <u>edge</u> area on TP.

Fig.1 Inspection zones in an LCD.

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

#### **3.2 Definition of some visual defect**

Bright dot.	Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern.
Dark dot.	Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue picture, or pure whiter picture.
Dark / Bright Lines.	Lines on display which appear dark/bright and usually result from the contamination.



#### OUTGOING QUALITY STANDARD

PAGE 2 OF 6

## TITLE: FUNCTIONAL TEST & INSPECTION CRITERIA

#### 4. Major Defect

MF

Item No	Items to be inspected	Inspection Standard	Classification of defects		
4.1	All functional defects	<ol> <li>No display</li> <li>Display abnormally</li> <li>Open or missing segment</li> <li>Short circuit</li> <li>Excess power consumption</li> <li>Back-light no lighting, flickering and abnormal lighting.</li> </ol>			
4.2	Missing	Missing component	Major		
4.3	Outline dimension	Overall outline dimension beyond the drawing is not allowed.			
4.4	Crack	Creaks tend to break are not allowed.			

#### 5. Minor Defect

Item No	Items to be inspected	Inspection Standard					Classification of defects	
5.1	Bright dot. defect.	$\Delta = \frac{\text{Zone}}{\Phi \leq 0.15}$		Acceptable QtyABCAcceptable (clustering of spot not allowed)Acceptable				
$\Phi = (x+y)/2$		$0.15 < \Phi \le 0.25$ 0.25 < $\Phi \le 0.50$		N≤6. N≤2		Acceptable		
		Zone		Acceptable Q'ty			Minor	
		Size(mm)		А	В	С		
5.2	Dark dot defect.	Φ≤0.15		Acceptable				
		0.15<Ф≤0.30		N≤6		Acceptable		
		0.30<Φ≤0	50	N≪4				
5.3	Bright / Dark line.	$0.01 < W \leq 0.10$ , $0.30 < L \leq 1.50$ , Accepta			Acceptable			
2 3	. Minimum d	ve dots shall not exce istance between def dark sub pixel defec .: Length, N: Count	ecti t or	ive dots is			han 1pair.	

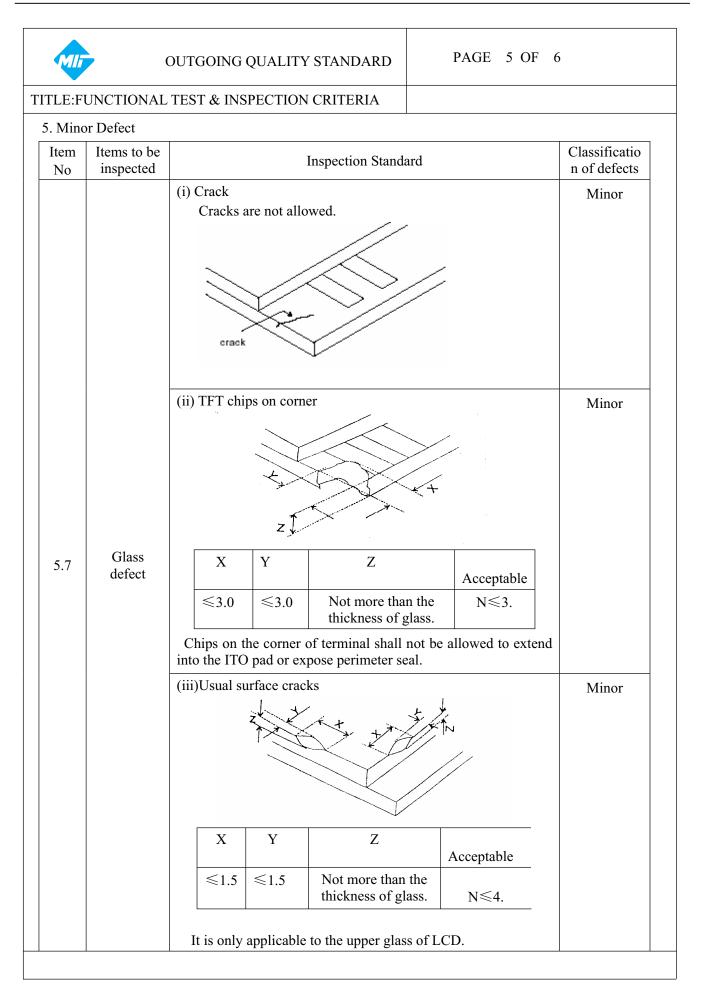


OUTGOING QUALITY STANDARD							PAGE 3 OF 6		
TLE:F	UNCTIONAL TES	T & INSPECTIO	ON CR	RITERIA					
Itom	Itoma to he							Classification	
Item No	Items to be inspected		]	Inspection St	andaro	1		of defects	
					1			Minor	
	Linear defect	Siz	n)	Acce	eptable Qty	1			
	Foreign material under polarizer,	L(Length)	W	V(Width)	A	ZoneABC		_	
		Ignore		W≤0.05	Acce	eptable	A		
		L≤5.0	0.05	<w≤0.15< td=""><td></td><td colspan="2">N≤5</td><td></td></w≤0.15<>		N≤5			
		5.0≤L (		0.15≤W		0	Acceptable		
5.4	Circular Defect,							Minor	
	Foreign material	Zone		Acceptabl		ıble Q'ty			
	under polarizer,	Size(mm) Φ≤0.25		А	A B		С		
	$\left  \begin{array}{c} \bigcirc \downarrow \\ \checkmark \end{array} \right ^{j}$			Acceptable		Acceptable			
		$0.25 < \Phi \le 0.50$		N≪4					
	$\Phi = (x+y)/2$	<b>0.50</b> ≤Φ	0						
		<ul> <li>5.4.1 Polarizer</li> <li>(i) Shifting in dimension</li> <li>(ii) Incomplet</li> <li>is not allowed to the second second</li></ul>	n posi n. e cove owed. olarize n can b	tion should r ring of the vie r pe wiped easi	wing a	rea due to sl	hifting	ine Minor	
5.5	Polarizer	Sizes(mm)		Acceptable		able Qty			
	defect.			-		Zone			
				Α	В	C			
		Φ<0.2	5	Accepta	ble				
		0.25≤Φ≤0.5		N≤4		Acceptable			
		0.25 < 1 <		1, ,		Ассериа			



#### PAGE 4 OF 6 Slh OUTGOING QUALITY STANDARD TITLE: FUNCTIONAL TEST & INSPECTION CRITERIA 5. Minor Defect Item Items to be Classification **Inspection Standard** of defects inspected No Minor 5.4.4Air bubbles between glass & polarizer: Acceptable Qty Size(mm) Zone В С А $\Phi \leq 0.3$ Acceptable $0.3 < \Phi \le 1.0$ 3 Acceptable $1.0 < \Phi \le 1.5$ 1 $\Phi > 1.5$ 0 5.4.5 Polarizer scratch Minor (i) If the Polarizer scratch can be seen after cover assembling or in the operating condition, judge by Polarizer the line defect of 5.4. defect (ii) If the Polarizer scratch can be seen only in non-operating condition or some special angle, 5.6 judge by the following. Size(mm) Acceptable Qty Zone L(Length) W(Width) В С А Ignore W≤0.02 Ignore 1.0<L≤5.0 0.02<W\le 0.2 N≤4. Ignore 0 5.0<L 0.2 < W





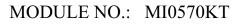


## OUTGOING QUALITY STANDARD

PAGE 6 OF 6

# TITLE: FUNCTIONAL TEST & INSPECTION CRITERIA

Item	Items to be inspected	Inspection Standard	Classification	
<u>No</u>	Difference in Spec.	None allowed	of defects Major	
	-			
2	Pattern peeling	No substrate pattern peeling and floating	Major	
3	Soldering defects	No soldering missing No soldering bridge No cold soldering	Major Major Minor	
4	Resist flaw on Printed Circuit Boards	Minor		
5	Accretion of metallic Foreign matter	No accretion of metallic foreign matters (Not exceed $\emptyset$ 0.2mm).	Minor Minor	
6	Stain	No stain to spoil cosmetic badly.	Minor	
7	Plate discoloring	No plate fading, rusting and discoloring.	Minor	
8	Solder amount 1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly. (too much) b. Components side ( In case of 'Through Hole PCB')	Minor	
	2. Flat packages	Solder to reach the Components side of PCB. Either 'Toe' (A) or 'Seal' (B) of the lead to be covered by 'Filet'. Lead form to be assume over	Minor	
	3. Chips	solder. (3/2) $H \ge h \ge (1/2) H$	Minor	
9	Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad $h \ge 0.13$ mm. The diameter of solder ball d $\le 0.15$ mm. d	Minor	
		b. The quantity of solder balls or solder. Splashes isn't beyond 5 in 600 mm <sup>2</sup> . $\bigcirc$ h	Minor	
		c.Solder balls/Solder splashes do not violate minimum electrical clearance.	Major	
		d.Solder balls/Solder splashes must be entrapped / encapsulated or attached to the metal surface .	Minor	
		Note: Entrapped/encapsulated/attached is intended to mean that normal service environment of the product will not cause a solder ball to become dislodged.		





1

# Handing Precautions

- 1.1 The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- 1.2 If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- 1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- 1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in to contact with room temperature air.
- 1.5 If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
  - Isopropyl alcohol
  - Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

- 1.6 Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
  - Water
  - Ketone
  - Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contact with oil and fats.

- 1.7 Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- 1.8 Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- 1.9 Do not attempt to disassemble or process the LCD module.
- 1.10 NC terminal should be open. Do not connect anything.
- 1.11 If the logic circuit power is off, do not apply the input signals.
- 1.12 Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- Before removing LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.

- Tools required for assembling, such as soldering irons, must be properly grounded. Make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.

- To reduce the amount of static electricity generated, do not conduct assembling



and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dry. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.

- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

1.13 Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- Do not alter, modify or change the shape of the tab on the metal frame.

- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.

- Do not damage or modify the pattern writing on the printed circuit board.

- Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.

- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.

- Do not drop, bend or twist the LCM.

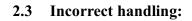


- 2 Handling precaution for LCM
  - 2.1 LCM is easy to be damaged. Please note below and be careful for handling.
  - 2.2 Correct handling:





As above picture, please handle with anti-static gloves around LCM edges.





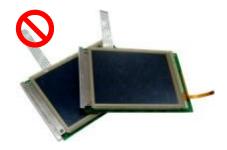
Please don't touch IC directly.



Please don't hold the surface of panel.



Please don't hold the surface of IC.



Please don't stack LCM.



Please don't stretch interface of output, such as FPC cable.



Please don't operate with sharp stick such as pens.

#### **3** Storage Precautions

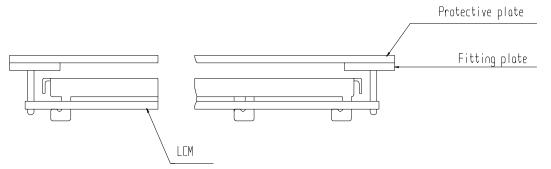
- 3.1 When storing the LCD modules, the following precaution are necessary.
  - 3.1.1 Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.
  - 3.1.2 Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
  - 3.1.3 The polarizer surface should not come in contact with any other objects (We advise you to store them in the anti-static electricity container in which they were shipped).
- 3.2 Others 其它
  - 3.2.1 Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.
  - 3.2.2 If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
  - 3.2.3 To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.
    - 3.2.3.1 Exposed area of the printed circuit board.
    - 3.2.3.2 -Terminal electrode sections.

## 4 USING LCD MODULES

4.1 Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

4.1.1 Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



4.1.2 When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be  $\pm 0.1$  mm.



#### **4.3** Precaution for soldering the LCM

	Manual soldering	Machine drag soldering	Machine press soldering
No RoHS	290°C ~350°C.	330°C ~350°C.	300°C ~330°C.
Product	Time : 3-5S.	Speed : 15-17 mm/s.	Time : 3-6S.
Floquet			Press: 0.8~1.2Mpa
RoHS	340°C ∼370°C.	350°C ~370°C.	330°C ~360°C.
Product	Time : 3-5S.	Speed : 15-17 mm/s.	Time : 3-6S.
TIOUUCI			Press: 0.8~1.2Mpa

- 4.3.1 If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation (This does not apply in the case of a non-halogen type of flux). It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- 4.3.2 When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- 4.3.3 When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.
- 4.4 Precautions for Operation
  - 4.4.1 Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.
  - 4.4.2 It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
  - 4.4.3 Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operating temperature.
  - 4.4.4 If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
  - 4.4.5 A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50%RH or less is required.
  - 4.4.6 Input logic voltage before apply analog high voltage such as LCD driving voltage when power on. Remove analog high voltage before logic voltage when power off the module. Input each signal after the positive/negative voltage becomes stable.
  - 4.4.7 Please keep the temperature within the specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

#### 4.5 Safety

- 4.5.1 It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- 4.5.2 If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.



4.6 Limited Warranty

Unless agreed between Multi-Inno and the customer, Multi-Inno will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with Multi-Inno LCD acceptance standards (copies available upon request) for a period of one year from date of production. Cosmetic/visual defects must be returned to Multi-Inno within 90 days of shipment. Confirmation of such date shall be based on data code on product. The warranty liability of Multi-Inno limited to repair and/or replace on the terms set forth above. Multi-Inno will not be responsible for any subsequent or consequential events.

- 4.7 Return LCM under warranty
  - 4.7.1 No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :
    - 4.7.1.1 Broken LCD glass.
    - 4.7.1.2 PCB eyelet is damaged or modified.
    - 4.7.1.3 -PCB conductors damaged.
    - 4.7.1.4 Circuit modified in any way, including addition of components.
    - 4.7.1.5 PCB tampered with by grinding, engraving or painting varnish.
    - 4.7.1.6 Soldering to or modifying the bezel in any manner.
  - 4.7.2 Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet, conductors and terminals.

# PACKING SPECIFICATION

Please consult our technical department for detail information.

# PRIOR CONSULT MATTER

- 1 For Multi-Inno standard products, we keep the right to change material, process ... for improving the product property without prior notice to our customer.
- 2 For OEM products, if any changes are needed which may affect the product property, we will consult with our customer in advance.
- 3 If you have special requirement about reliability condition, please let us know before you start the test on our samples.