



**MULTI-INNO TECHNOLOGY CO., LTD.**

[www.multi-inno.com](http://www.multi-inno.com)

## **LCD MODULE SPECIFICATION**

**Model : MI0220ET-1**

**For Customer's Acceptance:**

Customer	
Approved	
Comment	

Revision	1.0
Engineering	
Date	2012-05-18
Our Reference	



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**■ GENERAL INFORMATION**

Item	Contents	Unit
LCD type	TFT ECB Transflective	/
Size	2.2	Inch
Viewing direction	12:00	O' Clock
Gray scale inversion direction	6:00	O' Clock
LCM (W × H × D )	40.60×56.60×2.70	mm <sup>3</sup>
Active area (W×H)	33.84×45.12	mm <sup>2</sup>
Pixel pitch (W×H)	0.141×0.141	mm <sup>2</sup>
Number of dots	240 (RGB) × 320	/
Driver IC	ILI9341	/
Backlight type	4 LEDs	/
Interface type	RGB 18 bits+SPI	/
Color depth	262K	/
Pixel configuration	R.G.B vertical stripe	/
Surface treatment(Up polarizer)	Clear type(3H)	/
Surface treatment	Clear type(3H)	/
Input voltage	2.8	V
With/Without TSP	Without TSP	/
Weight	12.74	g

Note 1: Viewing direction for best image quality is different from TFT definition, there is a 180 degree shift.

Note 2 : RoHS compliant;

Note 3: LCM weight tolerance: ± 5% .



## ■ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit
Logic supply voltage	VDD	-0.3	4.6	V
Input signal voltage	VIN	-0.3	VCC+0.5	V
Back light forward current	I <sub>LED</sub>	-	25	mA
Operating temperature	T <sub>OP</sub>	-20	70	°C
Storage temperature	T <sub>ST</sub>	-30	80	°C
Humidity	RH	-	90%(Max60°C)	RH

Note :VIN: R0-R5,G0-G5,B0-B5,ENABLE,DCLK,HSYNC,VSYNV,/CS,SCL,SDI,SDO/RESET.

## ■ELECTRICAL CHARACTERISTICS

### DC CHARACTERISTICS

Parameter	Symbol	Min	Typ	Max	Unit
Digital supply voltage	VDD	2.5	2.8	3.3	V
Current of VDD power supply	ICC	-	10	-	mA
Input voltage ' H ' level	V <sub>IH</sub>	0.8VDD	-	VDD	V
Input voltage ' L ' level	V <sub>IL</sub>	VSS	-	0.2VDD	V
Output voltage ' H ' level	V <sub>OH</sub>	0.7VDD	-	VDD	V
Output voltage ' L ' level	V <sub>OL</sub>	VSS	-	0.3VDD	V

Note 1:VIH/VIL: R0-R5,G0-G5,B0-B5,ENABLE,DCLK,HSYNC,VSYNV,/CS,SCL,SDI,SDO/RESET.

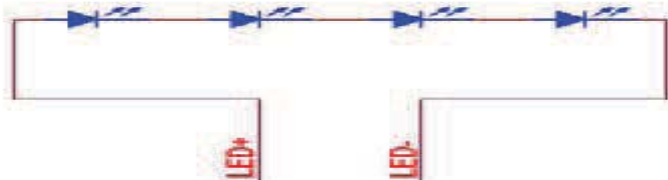
Note 2:VOH/VOL: Output signal voltage

Note 3:To test the current dissipation,use "all Balck Pattern".

## ■ BACKLIGHT CHARACTERISTICS

Item	Symbol	Min.	Typ.	Max.	Unit	Condition
Forward voltage	V <sub>f</sub>	-	12.8	-	V	4 LEDs serial
Forward current	I <sub>f</sub>	-	20	-	mA	
Power consumption	W <sub>BL</sub>	-	256	-	mW	
Operating life time	-	10000	20000	-	Hrs	

Note1: Figure below shows the connection of backlight LED.



Note 2: One LED: I<sub>F</sub> =20 mA, V<sub>F</sub> =12.8V

Note 3: : I<sub>F</sub> is defined for one channel LED.

Optical performance should be evaluated at Ta=25 °C only.

If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

## ■ELECTRO-OPTICAL CHARACTERISTICS

Item	Symbol	Condition	Min	Typ	Max	Unit	Remark	Note
Response time	Tr+Tf	$\theta=0^\circ$ $\varnothing=0^\circ$ Ta=25°C	-	35	-	ms	FIG 1.	4
Contrast ratio	Cr		100	120	-	---	FIG 2.	1
Luminance uniformity	$\delta$ WHITE		-	80	-	%	FIG 2.	3
Surface Luminance	Lv		80	90	-	cd/m <sup>2</sup>	FIG 2.	2
Viewing angle range	$\theta$	$\varnothing = 90^\circ$	-	48	53	deg	FIG 3.	6
		$\varnothing = 270^\circ$	-	45	50	deg	FIG 3.	
		$\varnothing = 0^\circ$	-	40	45	deg	FIG 3.	
		$\varnothing = 180^\circ$	-	45	50	deg	FIG 3.	
CIE (x, y) chromaticity	Red	x	-	-	-	FIG 2.	5	
		y	-	-	-			
	Green	x	-	-	-			
		y	-	-	-			
	Blue	x	-	-	-			
		y	-	-	-			
	White	x	-	0.310	-			
		y	-	0.300	-			
NTSC	-	-	-	30	-	%	-	-
Reflectance	-	-	-	6.8	-	%	-	8

Note 1. Contrast Ratio(CR) is defined mathematically as For more information see FIG 2.

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

$$L_v = \text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}$$

Note 3. The uniformity in surface luminance ,  $\delta$  WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see FIG 2.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

Note 4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1. The test equipment is Autronic-Melchers's ConoScope. Series.

Note 5. CIE (x, y) chromaticity, The x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

Note 7. For viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments For contrast ratio, Surface Luminance, Luminance uniformity, CIE The test data is base on TOPCON's BM-5 photo detector.

Note 8. Definition of reflectance measurement system

Note 5) Reflectance is defined as follows:

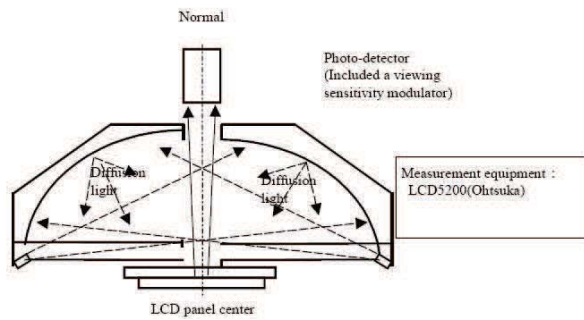


FIG. 1 The definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for “black” and “white”.

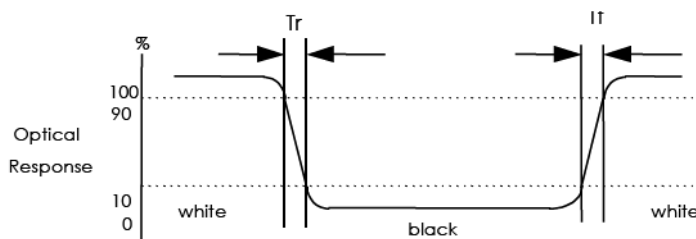


FIG. 2 Measuring method for Contrast ratio, surface luminance, Luminance uniformity , CIE (x, y) chromaticity

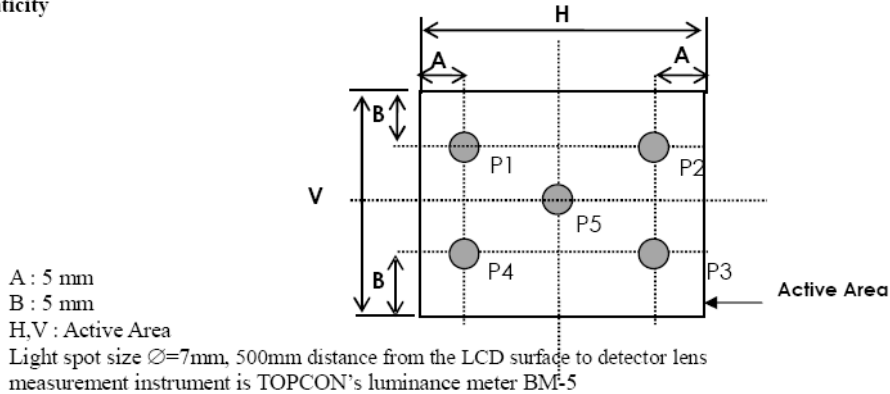
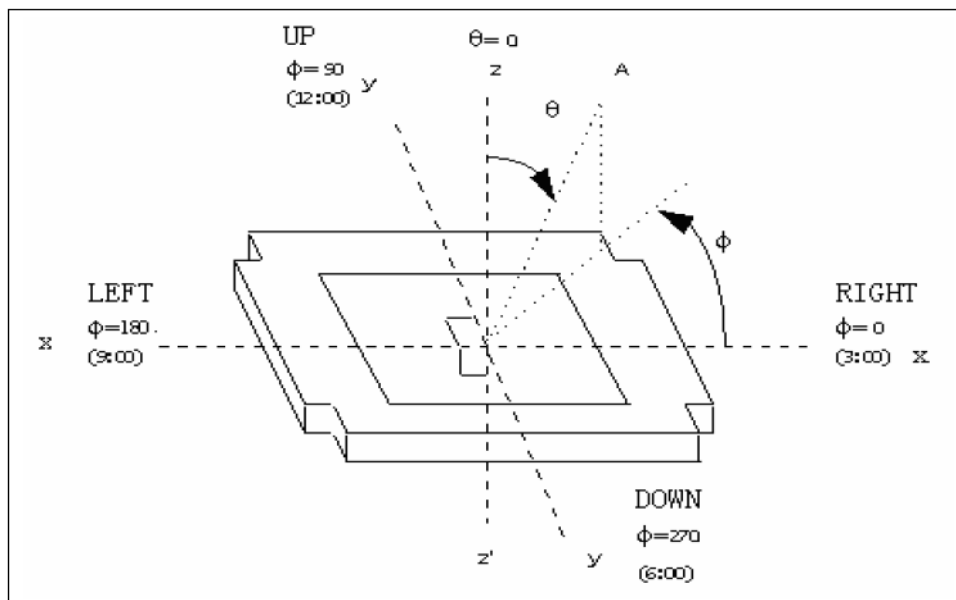


FIG. 3 The definition of viewing angle





## ■ INTERFACE DESCRIPTION

Recommended connector: FH23-39S-0.3SHW (HIROSE)

No	Symbol	I/O	Description	Remarks
1	VL1 (LED+)	I	Power supply for LED(High voltage)	
2	VL2 (LED-)	I	Power supply for LED(Low voltage)	
3	GND	P	Ground	
4	VDD	P	Power supply of gate driver(high level)	
5	GND	P	Ground	
6	VSYNC	I	Vertical sync. signal	
7	RESET	I	Reset Enable	
8	GND	P	Ground	
9	CS	I	SPI Chip select	
10	SDO	O	SPI serial Data output	
11	SDI	I	SPI serial Data input	
12	GND	P	Ground	
13	SCL	I	SPI serial interface clock	
14	GND	P	Ground	
15	B5	I	Blue data signal	
16	B4	I	Blue data signal	
17	B3	I	Blue data signal	
18	B2	I	Blue data signal	
19	B1	I	Blue data signal	
20	B0	I	Blue data signal	
21	ENABLE	I	Data enable signal	
22	HSYNC	I	Horizontal sync signal	
23	GND	P	Ground	
24	DCLK	I	Data sampling clock signal	
25	GND	P	Ground	
26	G5	I	Green data signal	
27	G4	I	Green data signal	
28	G3	I	Green data signal	
29	G2	I	Green data signal	
30	G1	I	Green data signal	
31	G0	I	Green data signal	
32	GND	P	Ground	
33	R5	I	Red data signal	
34	R4	I	Red data signal	
35	R3	I	Red data signal	
36	R2	I	Red data signal	
37	R1	I	Red data signal	
38	R0	I	Red data signal	
39	GND	P	Ground	

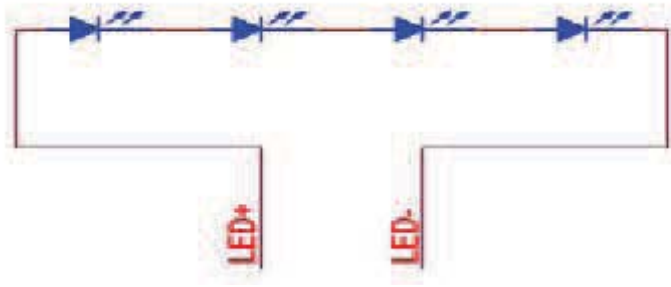
Note2-1: I/O definition:

I----Input

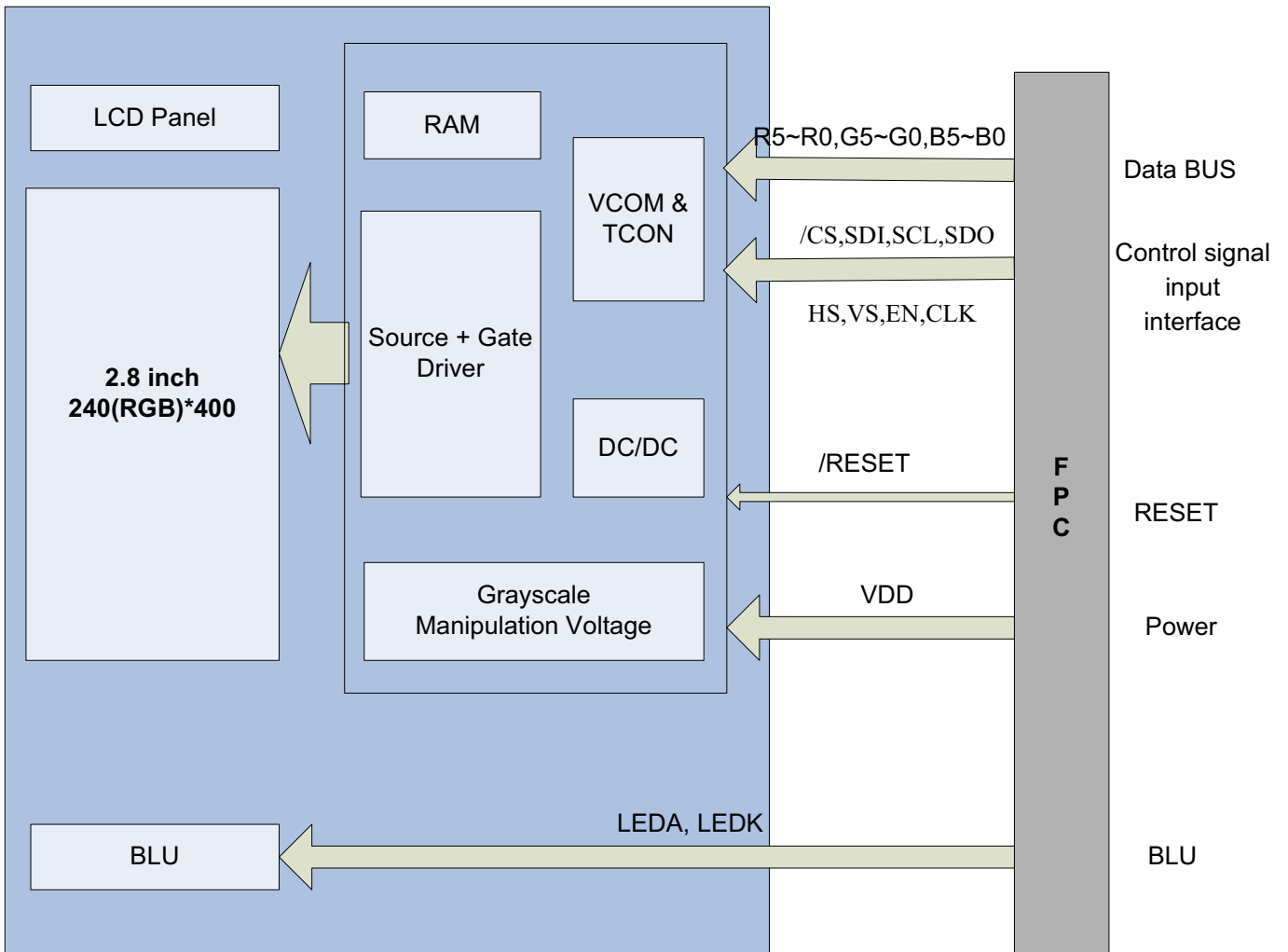
O---Output

P----Power

Note 2-2: The figure below shows the connection of backlight LED.



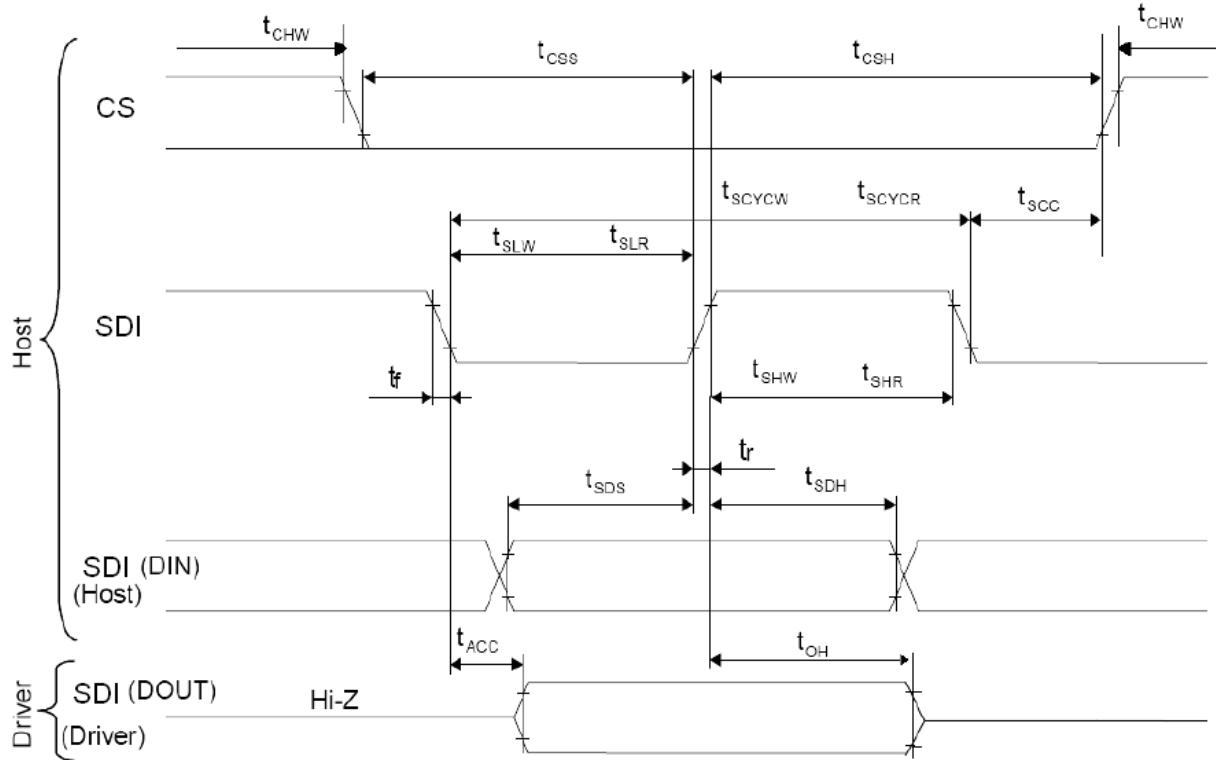
■ BLOCK DIAGRAM



## ■ APPLICATION NOTES

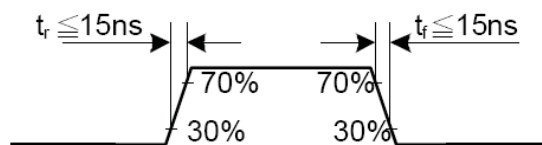
### 1 Timing chart

#### 1.1 3 wire INTERFACE CHARACTERISTICS

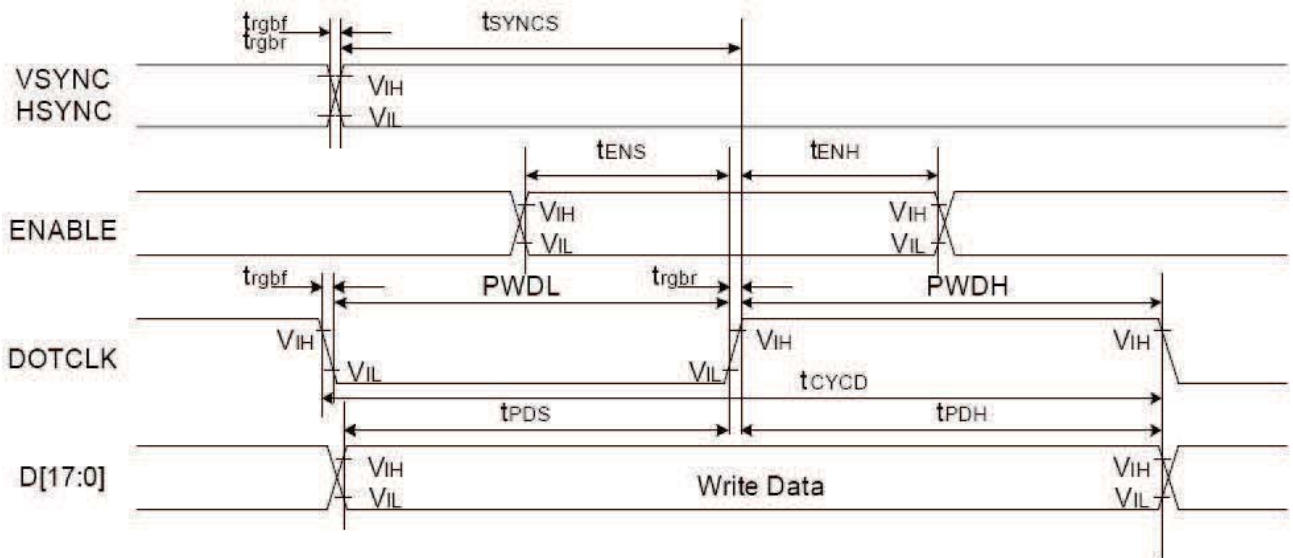


Signal	Symbol	Parameter	min	max	Unit	Description
SCL	tscycw	Serial Clock Cycle (Write)	100	-	ns	
	tshw	SCL "H" Pulse Width (Write)	40	-	ns	
	tslw	SCL "L" Pulse Width (Write)	40	-	ns	
	tscycr	Serial Clock Cycle (Read)	150	-	ns	
	tshr	SCL "H" Pulse Width (Read)	60	-	ns	
SDA / SDI (Input)	tsds	Data setup time (Write)	30	-	ns	
	tsdh	Data hold time (Write)	30	-	ns	
SDA / SDO (Output)	tacc	Access time (Read)	10	-	ns	
	toh	Output disable time (Read)	10	50	ns	
CS	tsc	SCL-CSX	20	-	ns	
	tch	CSX "H" Pulse Width	40	-	ns	
	tcs	CSX-SCL Time	60	-	ns	
	tcs		65	-	ns	

Note:  $T_a = 25^\circ\text{C}$ ,  $V_{DDI} = 1.65\text{V to } 3.3\text{V}$ ,  $V_{CI} = 2.5\text{V to } 3.3\text{V}$ ,  $AGND = VSS = 0\text{V}$

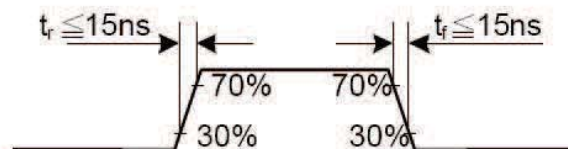


## 1.2 Parallel 18/16/6-bit RGB Interface Timing Characteristics

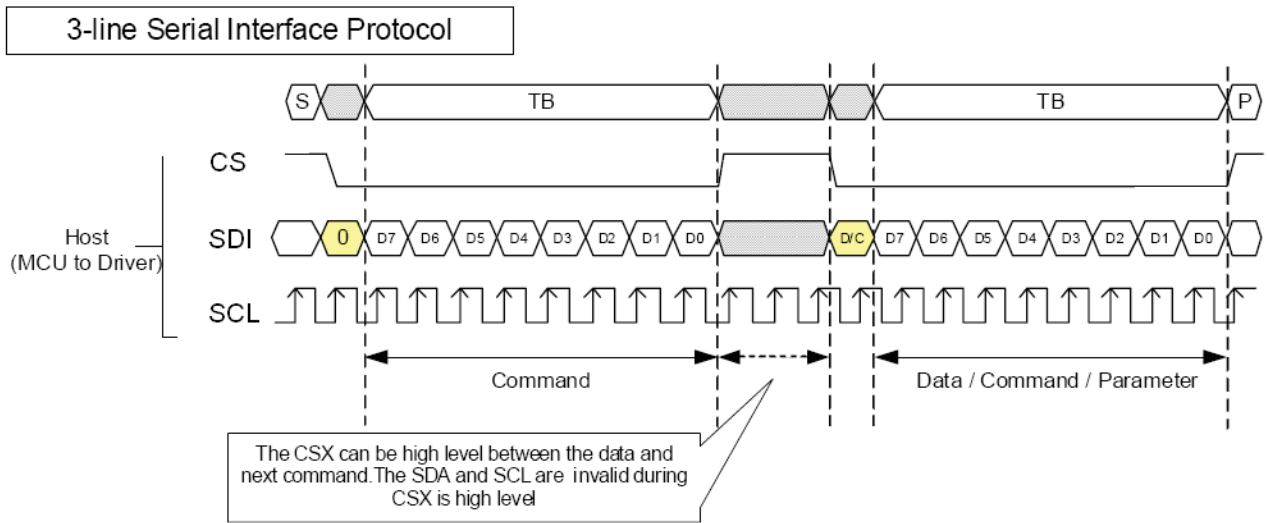


Signal	Symbol	Parameter	min	max	Unit	Description	
VSYNC / HSYNC	$t_{SYNCS}$	VSYNC/HSYNC setup time	15	-	ns	18/16-bit bus RGB interface mode	
	$t_{SYNCH}$	VSYNC/HSYNC hold time	15	-	ns		
DE	$t_{ENS}$	DE setup time	15	-	ns		
	$t_{ENH}$	DE hold time	15	-	ns		
D[17:0]	$t_{POS}$	Data setup time	15	-	ns		
	$t_{PDH}$	Data hold time	15	-	ns		
DOTCLK	PWDH	DOTCLK high-level period	15	-	ns		
	PWDL	DOTCLK low-level period	15	-	ns		
	$t_{CYCD}$	DOTCLK cycle time	100	-	ns		
	$t_{rgbr}, t_{rgbf}$	DOTCLK,HSYNC,VSYNC rise/fall time	-	15	ns		
VSYNC / HSYNC	$t_{SYNCS}$	VSYNC/HSYNC setup time	15	-	ns		6-bit bus RGB interface mode
	$t_{SYNCH}$	VSYNC/HSYNC hold time	15	-	ns		
DE	$t_{ENS}$	DE setup time	15	-	ns		
	$t_{ENH}$	DE hold time	15	-	ns		
D[17:0]	$t_{POS}$	Data setup time	15	-	ns		
	$t_{PDH}$	Data hold time	15	-	ns		
DOTCLK	PWDH	DOTCLK high-level pulse period	15	-	ns		
	PWDL	DOTCLK low-level pulse period	15	-	ns		
	$t_{CYCD}$	DOTCLK cycle time	100	-	ns		
	$t_{rgbr}, t_{rgbf}$	DOTCLK,HSYNC,VSYNC rise/fall time	-	15	ns		

Note:  $T_a = -30$  to  $70$  °C,  $V_{DDI} = 1.65V$  to  $3.3V$ ,  $V_{CI} = 2.5V$  to  $3.3V$ ,  $AGND = VSS = 0V$

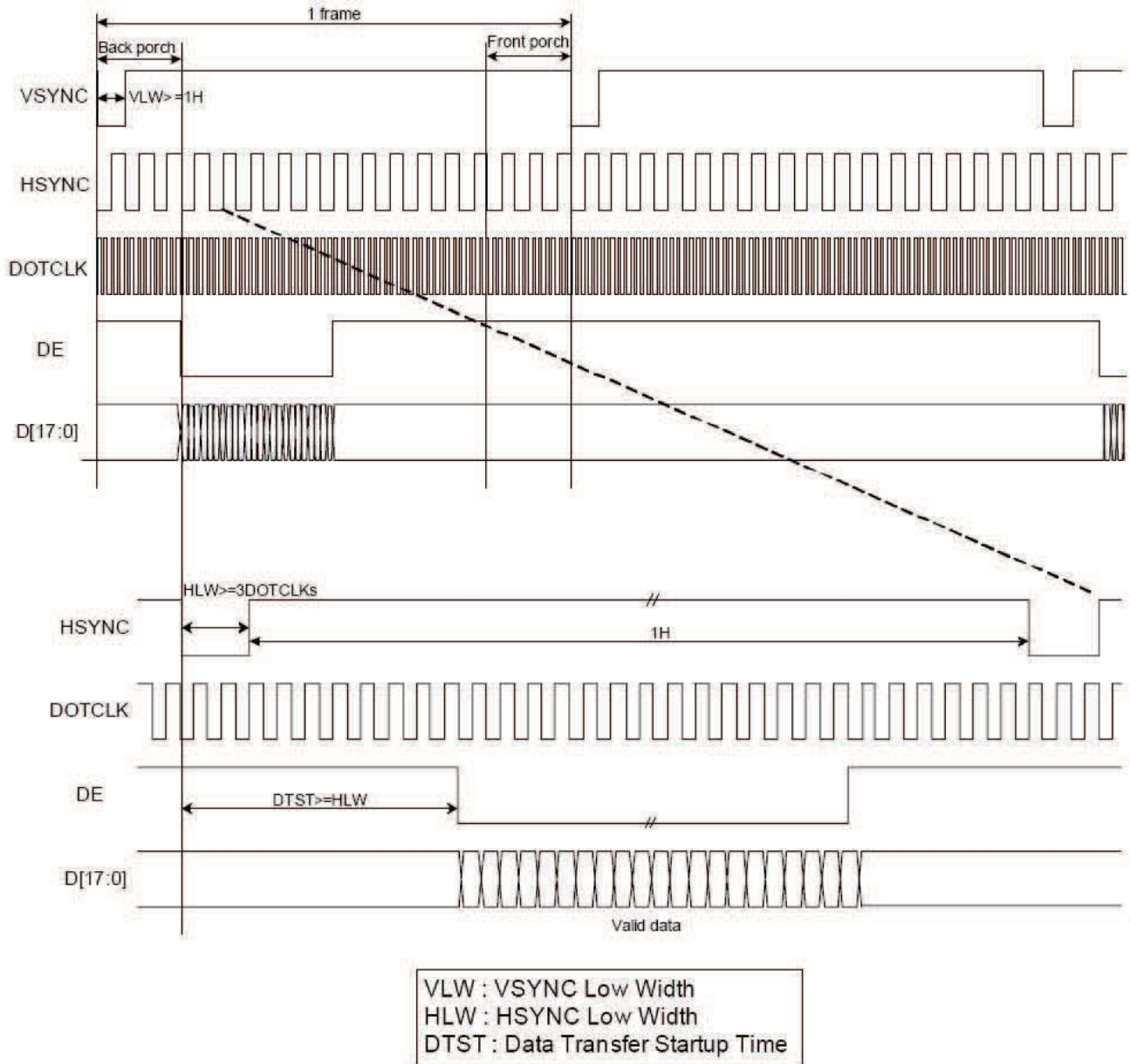


## 1.3 3-line Serial Interface Protocol



### 1.4 RGB Interface Timing

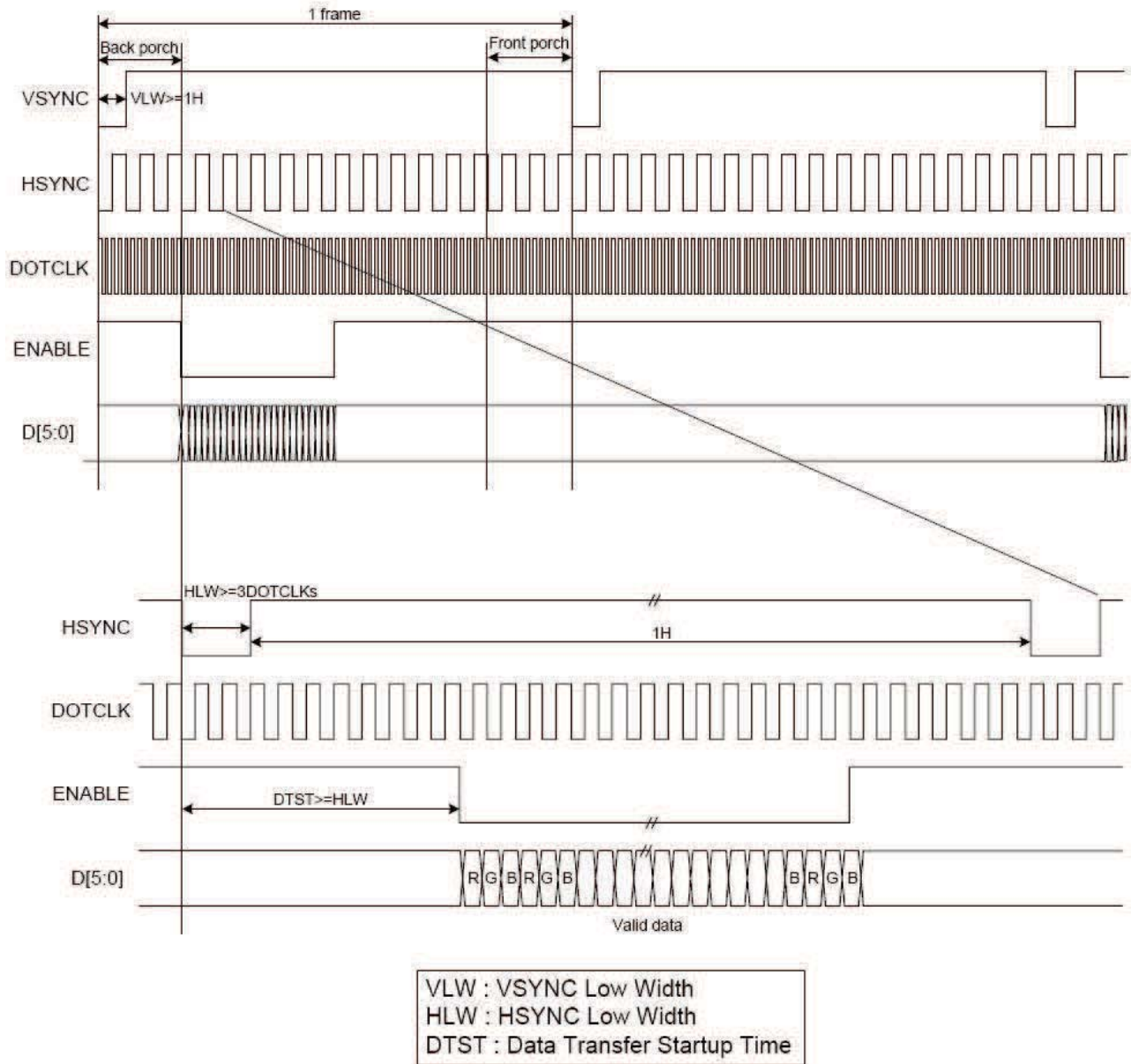
The timing chart of 18-/16-bit RGB interface mode is shown as



Note 1: The DE signal is not needed when RGB interface SYNC mode is selected.

Note 2: VSPL='0', HSPL='0', DPL='0' and EPL='1' of "Interface Mode Control (B0h)" command.

The timing chart of 6-bit RGB interface mode is shown as below



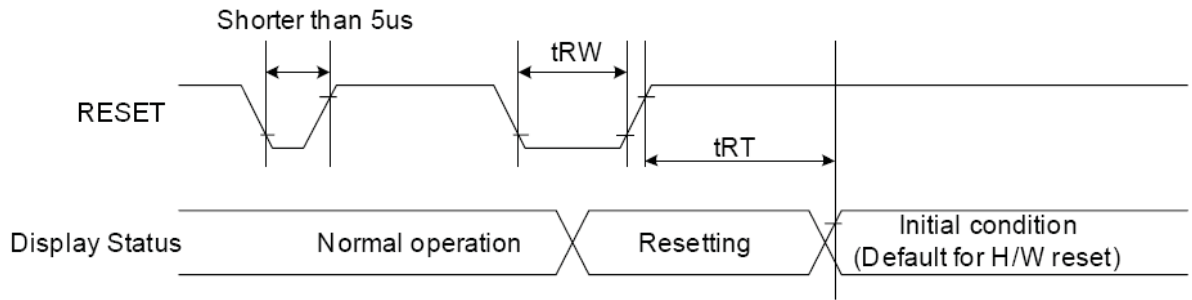
Note 1: The DE signal is not needed when RGB interface SYNC mode is selected.

Note 2: VSPL='0', HSPL='0', DPL='0' and EPL='1' of "Interface Mode Control (B0h)" command.

Note 3: In 6-bit RGB interface mode, each dot of one pixel (R, G and B) is transferred in synchronization with DOTCLK.

Note 4: In 6-bit RGB interface mode, set the cycles of VSYNC, HSYNC and DE to 3 multiples of DOTCLK.

## 1.5 Reset Timing

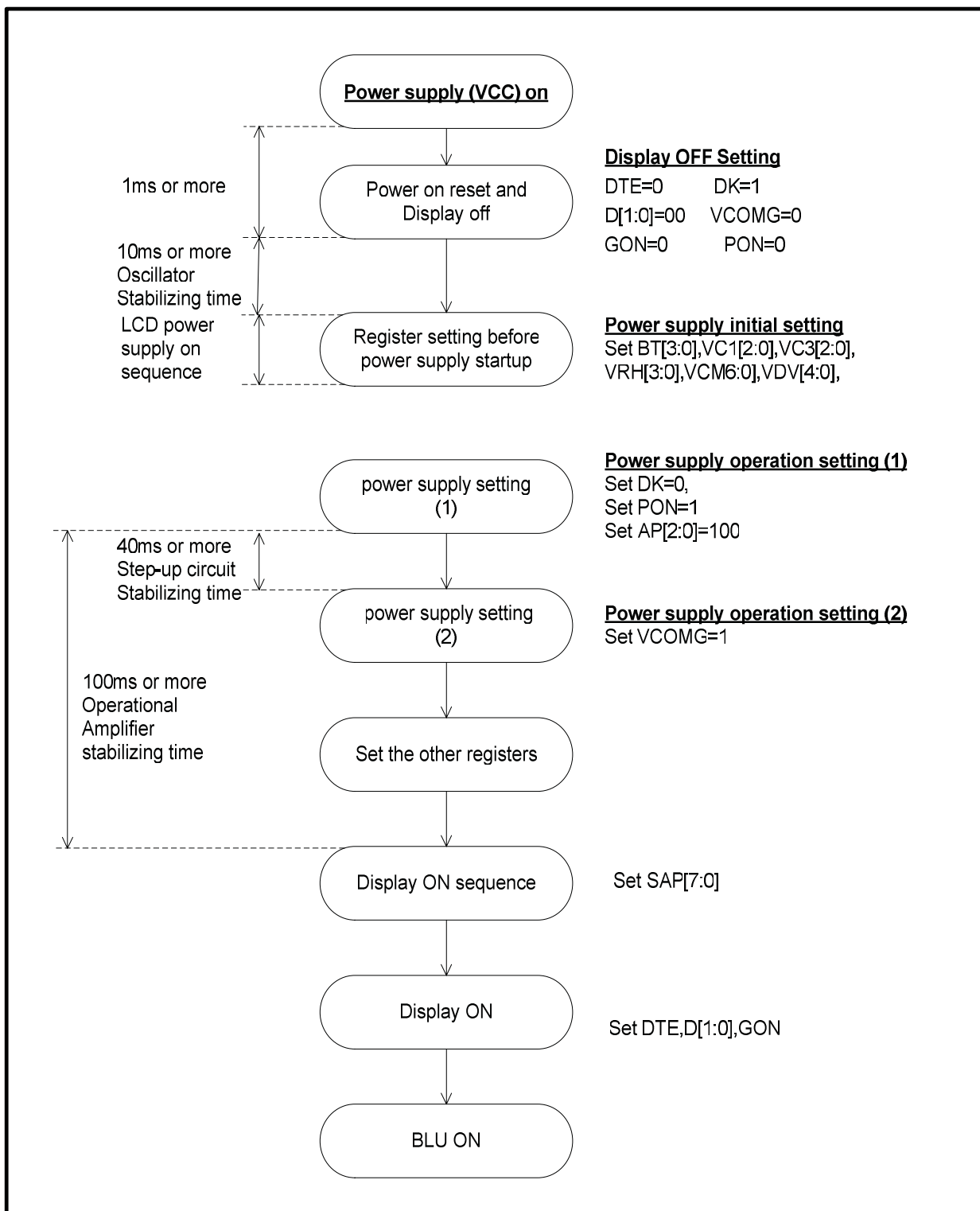


Signal	Symbol	Parameter	Min	Max	Unit
RESET	tRW	Reset pulse duration	10		uS
	tRT	Reset cancel		5	mS
				120	mS



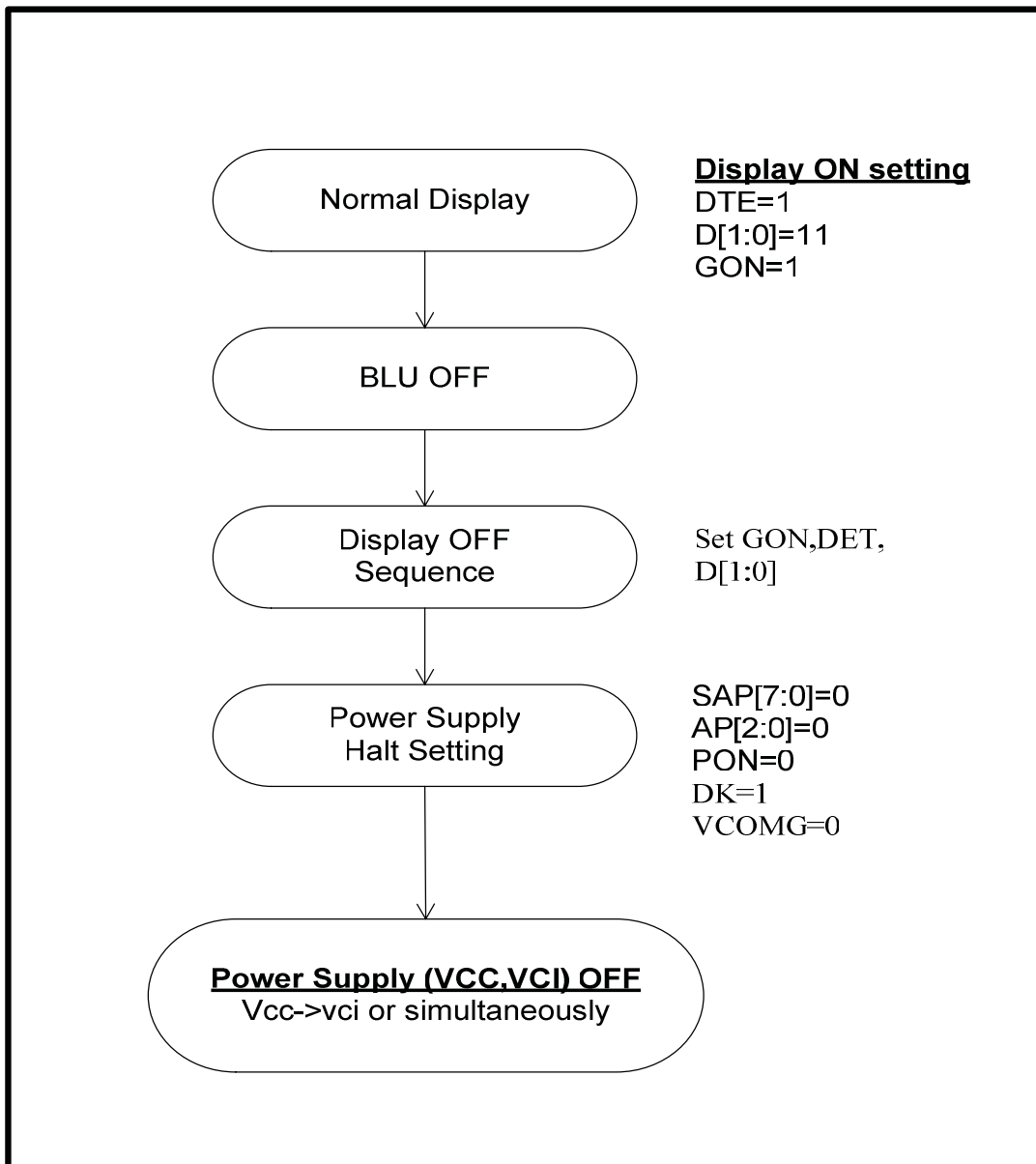
## 2 Power on/off Sequence

### 2.1 Power on sequence



Power On Sequence

2.2 Power off sequence



Power OFF Sequence

**■ RELIABILITY TEST**

No.	Test Item	Test Condition	Remark
1	High Temperature Storage	$80 \pm 2^{\circ}\text{C}/240$ hours	IEC60068-2-1 GB2423.2
2	Low Temperature Storage	$-30 \pm 2^{\circ}\text{C}/240$ hours	IEC60068-2-1 GB2423.1
3	High Temperature Operating	$70 \pm 2^{\circ}\text{C}/240$ hours	IEC60068-2-1 GB2423.2
4	Low Temperature Operating	$-20 \pm 2^{\circ}\text{C}/240$ hours	IEC60068-2-1 GB2423.1
5	Temperature Cycle storage	$-30 \pm 2^{\circ}\text{C} \sim 25 \sim 80 \pm 2^{\circ}\text{C} \times 20$ cycles (30min.) (5min.) (30min.)	Start with cold temperature, End with high temperature, IEC60068-2-14 GB2423.22
6	Damp proof Test operating	$60^{\circ}\text{C} \pm 5^{\circ}\text{C} \times 90\%\text{RH}/240$ hours	IEC60068-2-78 GB/T2423.3
7	Vibration Test (non-operation)	Frequency range:10Hz~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2hours for each direction of X,Y,Z(6 hours for total)	IEC60068-2-6 GB/T2423.10
8	Package drop test	Height:80 cm,1 corner,3 edges,6 surfaces	IEC60068-2-32,GB2423.8
9	ESD test (operation)	C=150pF,R=330Ω,5points/panel Air: ±8KV,5times Contact: ±4KV,5times(Environment: 15°C~35°C,30%~60%,86Kpa~106Kpa)	IEC61000-4-2 GB/T17626.2
10	Shock(non-operation)	60G 6ms, ±X, ±Y, ±Z 3times each direction	IEC60068-2-27 GB/T2423.5

Note 1:Ts is the temperature of panel's surface.

Note 2:Ta is the ambient temperature of sample.

**■ INSPECTION CRITERION**

	OUTGOING QUALITY STANDARD	PAGE 1 OF 4
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TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA
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This specification is made to be used as the standard acceptance/rejection criteria for Color mobile phone LCM.

1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993, normal level 2 and based on:

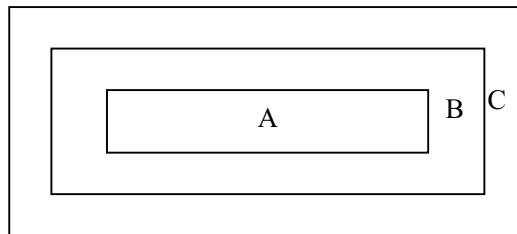
Major defect: AQL 0.65

Minor defect: AQL 1.5

2. Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45° against perpendicular line.

3. Definition of inspection zone in LCD.




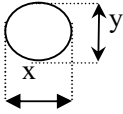
Zone A: character/Digit area


Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

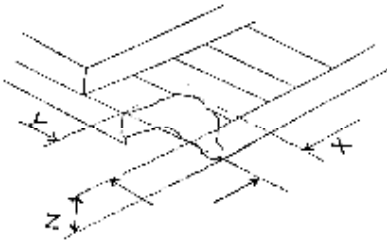
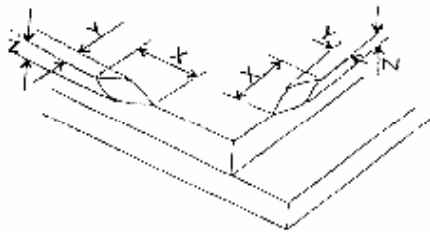
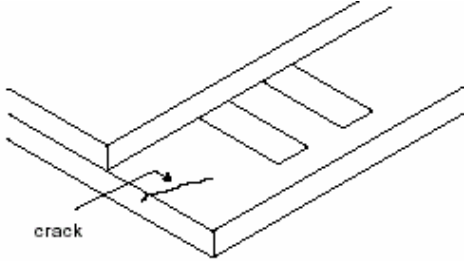
Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD.

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

 OUTGOING QUALITY STANDARD		PAGE 2 OF 4																												
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA																														
<b>4. Inspection standards</b>																														
<b>4.1 Major Defect</b>																														
Item No	Items to be inspected	Inspection Standard	Classification of defects																											
4.1.1	All functional defects	1) No display 2) Display abnormally 3) Missing vertical, horizontal segment 4) Short circuit 5) Back-light no lighting, flickering and abnormal lighting.	Major																											
4.1.2	Missing	Missing component																												
4.1.3	Outline dimension	Overall outline dimension beyond the drawing is not allowed.																												
<b>4.2 Cosmetic Defect</b>																														
Item No	Items to be inspected	Inspection Standard	Classification of defects																											
4.2.1	Clear Spots	For dark/white spot, size $\Phi$ is defined as $\Phi = \frac{(x+y)}{2}$ 	Minor																											
	Black and white Spot defect Pinhole, Foreign Particle, Dirt under polarizer	1. <table border="1" data-bbox="470 1243 1161 1608"> <thead> <tr> <th rowspan="2">Zone Size(mm)</th> <th colspan="3">Acceptable Qty</th> </tr> <tr> <th>A</th> <th>B</th> <th>C</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.10</math></td> <td colspan="3">Ignore</td> </tr> <tr> <td><math>0.10 &lt; \Phi \leq 0.15</math></td> <td colspan="3">2</td> </tr> <tr> <td><math>0.15 &lt; \Phi \leq 0.20</math></td> <td colspan="3">1</td> </tr> <tr> <td><math>\Phi &gt; 0.20</math></td> <td colspan="3">0</td> </tr> </tbody> </table>		Zone Size(mm)	Acceptable Qty			A	B	C	$\Phi \leq 0.10$	Ignore			$0.10 < \Phi \leq 0.15$	2			$0.15 < \Phi \leq 0.20$	1			$\Phi > 0.20$	0						
Zone Size(mm)	Acceptable Qty																													
	A	B	C																											
$\Phi \leq 0.10$	Ignore																													
$0.10 < \Phi \leq 0.15$	2																													
$0.15 < \Phi \leq 0.20$	1																													
$\Phi > 0.20$	0																													
	Dim Spots Circle shaped and dim edged defects	2. <table border="1" data-bbox="454 1691 1141 2083"> <thead> <tr> <th rowspan="2">2. Zone Size(mm)</th> <th colspan="3">Acceptable Qty</th> </tr> <tr> <th>A</th> <th>B</th> <th>C</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.2</math></td> <td colspan="3">Ignore</td> </tr> <tr> <td><math>0.20 &lt; \Phi \leq 0.40</math></td> <td colspan="3">3</td> </tr> <tr> <td><math>0.40 &lt; \Phi \leq 0.60</math></td> <td colspan="3">2</td> </tr> <tr> <td><math>0.60 &lt; \Phi \leq 0.80</math></td> <td colspan="3">1</td> </tr> <tr> <td><math>0.80 &lt; \Phi</math></td> <td colspan="3">0</td> </tr> </tbody> </table>	2. Zone Size(mm)	Acceptable Qty			A	B	C	$\Phi \leq 0.2$	Ignore			$0.20 < \Phi \leq 0.40$	3			$0.40 < \Phi \leq 0.60$	2			$0.60 < \Phi \leq 0.80$	1			$0.80 < \Phi$	0			Minor
2. Zone Size(mm)	Acceptable Qty																													
	A	B	C																											
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 OUTGOING QUALITY STANDARD		PAGE 3 OF 4					
TITLE: FUNCTIONAL TEST & INSPECTION CRITERIA							
4.2. Cosmetic Defect							
Item No	Items to be inspected	Inspection Standard			Classification of defects		
4.2.2	Line defect Black line, White line, Foreign material under polarizer,	Size(mm)		Acceptable Qty		Minor	
		L(Length)	W(Width)	Zone			
				A	B		C
		Ignore	$W \leq 0.02$	Ignore			Ignore
		$L \leq 3.0$	$0.02 < W \leq 0.03$	2			
		$L \leq 2.0$	$0.03 < W \leq 0.05$	1			
	$0.05 < W$	Define as spot defect					
4.2.3	Polarizer scratch	If the Polarizer scratch can be seen after mobile phone cover assembling or in the operating condition, judge by the line defect of 4.2.2. If the Polarizer scratch can be seen only in non-operating condition or some special angle, judge by the following.			Minor		
		Size(mm)		Acceptable Qty			
		L(Length)	W(Width)	Zone			
				A		B	C
		Ignore	$W \leq 0.03$	Ignore		Ignore	
		$5.0 < L \leq 10.0$	$0.03 < W \leq 0.05$	2			
$L \leq 5.0$	$0.05 < W \leq 0.08$	1					
	$0.08 < W$	0					
4.2.4	Polarize Air bubble	Air bubbles between glass & polarizer			Minor		
		Size(mm)	2. Zone Acceptable Qty				
			A	B		C	
		$\Phi \leq 0.2$	Ignore			Ignore	
		$0.20 < \Phi \leq 0.30$	2				
$0.30 < \Phi \leq 0.50$	1						
$0.50 < \Phi$	0						

OUTGOING QUALITY STANDARD		PAGE 4 OF 4							
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA									
4.3. Cosmetic Defect									
Item No	Items to be inspected	Inspection Standard	Classification of defects						
4.3.5	Glass defect	(i) Chips on corner  <table border="1" data-bbox="544 860 1145 952"> <tr> <td>X</td> <td>Y</td> <td>Z</td> </tr> <tr> <td>≤2.0</td> <td>≤S</td> <td>Disregard</td> </tr> </table> Notes: S=contact pad length Chips on the corner of terminal shall not be allowed to extend into the ITO pad or expose perimeter seal.	X	Y	Z	≤2.0	≤S	Disregard	Minor
		X	Y	Z					
		≤2.0	≤S	Disregard					
(ii) Usual surface cracks  <table border="1" data-bbox="523 1364 1166 1456"> <tr> <td>X</td> <td>Y</td> <td>Z</td> </tr> <tr> <td>≤3.0</td> <td>&lt;Inner border line of the seal</td> <td>Disregard</td> </tr> </table>	X	Y	Z	≤3.0	<Inner border line of the seal	Disregard	Minor		
X	Y	Z							
≤3.0	<Inner border line of the seal	Disregard							
(iii) Crack Cracks tend to break are not allowed. 	Major								
4.3.6	Parts alignment	1) Not allow IC and FPC/heat-seal lead width is more than 50% beyond lead pattern. 2) Not allow chip or solder component is off center more than 50% of the pad outline.	Minor						
4.3.7	SMT	According to the <Acceptability of electronic assemblies> IPC-A-610C class 2 standard. Component missing or function defect are Major defect, the others are Minor defect.							

## ■ PRECAUTIONS FOR USING LCD MODULES

### Handing Precautions

(1) The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.

(2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.

(3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).

(4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.

(5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents

- Isopropyl alcohol
- Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

(6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.

- Water
- Ketone
- Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.

(7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.

(8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.

(9) Do not attempt to disassemble or process the LCD module.

(10) NC terminal should be open. Do not connect anything.

(11) If the logic circuit power is off, do not apply the input signals.

(12) Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.

- Tools required for assembling, such as soldering irons, must be properly grounded. make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.

- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential

- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated



(13) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- Do not damage or modify the pattern writing on the printed circuit board.
- Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- Do not drop, bend or twist LCM.

### **Storage Precautions**

When storing the LCD modules, the following precaution is necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped).

### **Others**

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.
- Terminal electrode sections.

**Handling precaution for LCM**

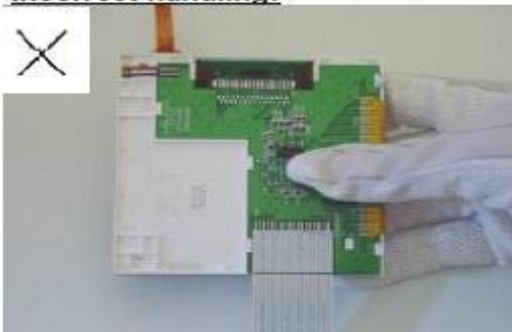
LCM is easy to be damaged.  
Please note below and be careful for handling!

**Correct handling:**



As above picture, please handle with anti-static gloves around LCM edges.

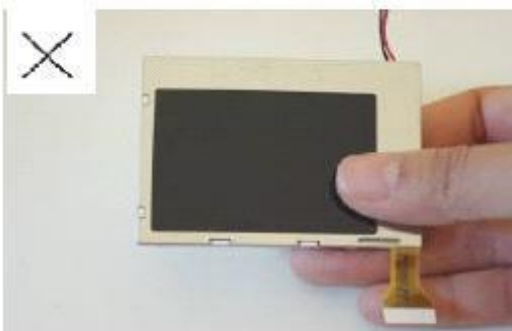
**Incorrect handling:**



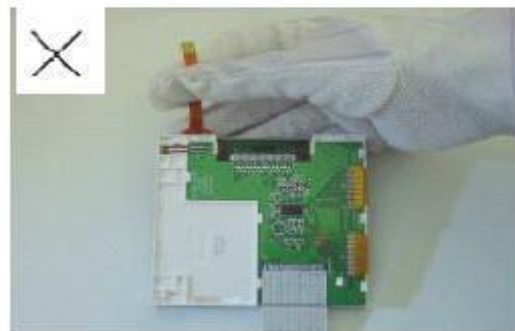
Please don't touch IC directly.



Please don't stack LCM.



Please don't hold the surface of panel.

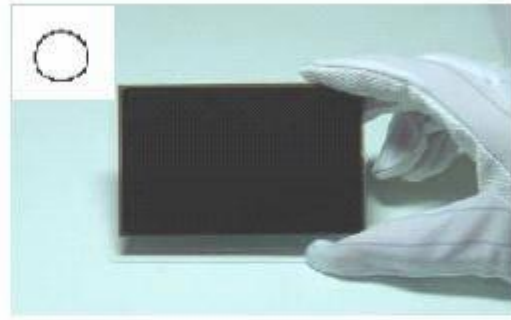


Please don't stretch interface of output, such as FPC cable.

### Handling precaution for LCD

LCD is easy to be damaged.  
Please note below and be careful for handling!

#### Correct handling:

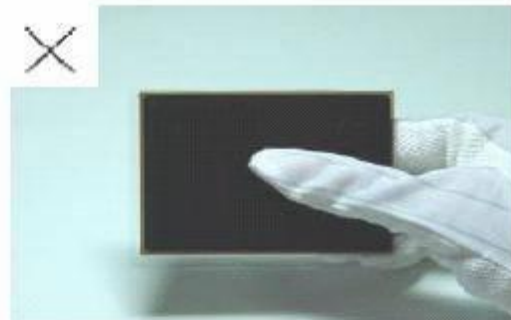


As above photo, please handle with anti-static gloves around LCD edges.

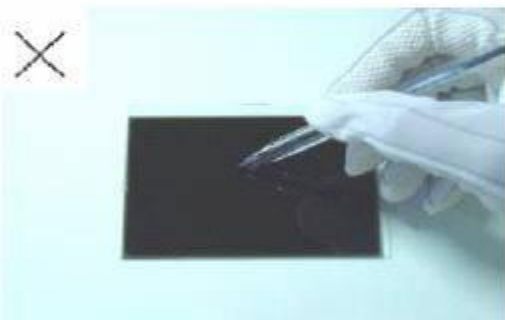
#### Incorrect handling:



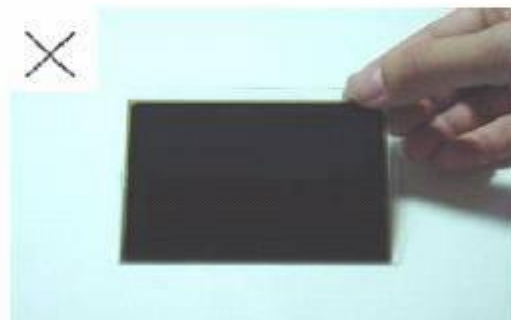
Please don't stack the LCDS.



Please don't hold the surface of LCD.



Please don't operate with sharp stick such as pens.



Please don't touch ITO glass without anti-static gloves.

### Storage Precautions

When storing the LCD modules, the following precaution is necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the anti-static electricity container in which they were shipped.

### Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

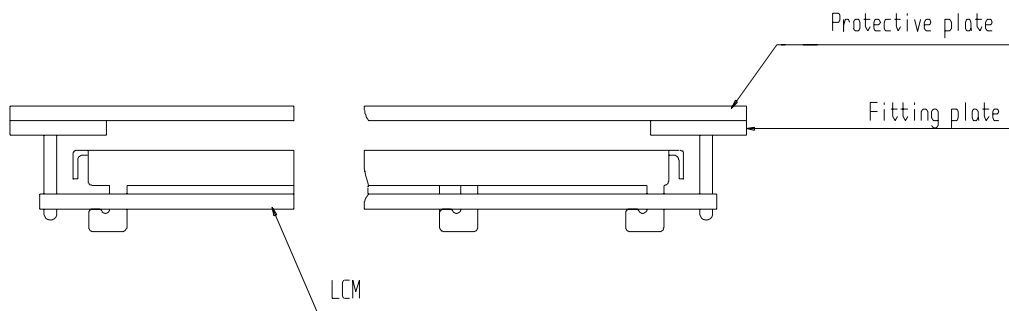
- Exposed area of the printed circuit board.
- Terminal electrode sections.

### USING LCD MODULES

#### Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

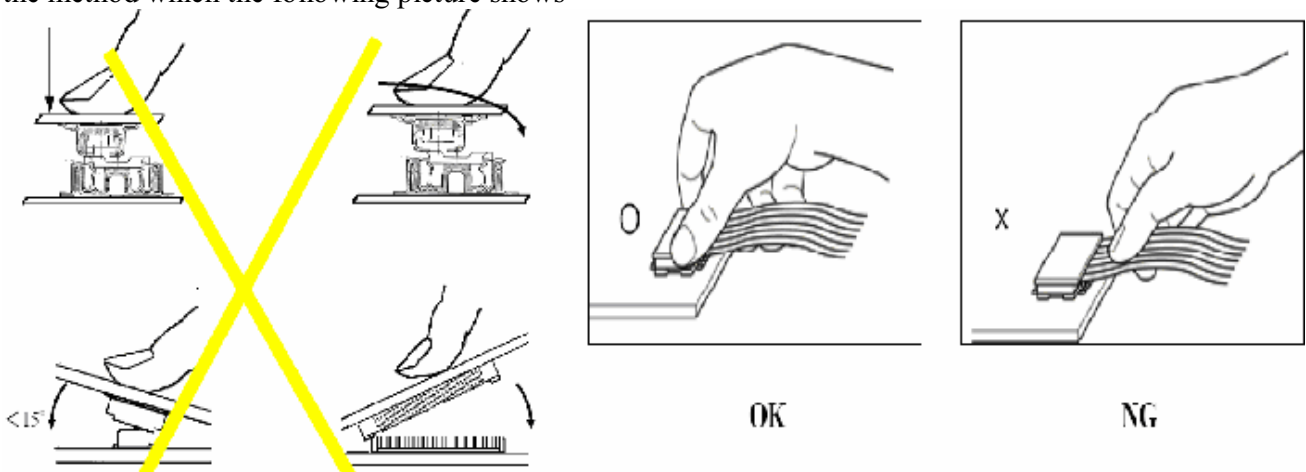
- (1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



- (2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be  $\pm 0.1$ mm.

#### Precaution for assemble the module with BTB connector:

Please note the position of the male and female connector position, don't assemble or assemble like the method which the following picture shows



### Precaution for soldering to the LCM

	Hand soldering	Machine drag soldering	Machine press soldering
No ROHS product	290°C ~350°C. Time : 3-5S.	330°C ~350°C. Speed : 4-8 mm/s.	300°C ~330°C. Time : 3-6S. Press: 0.8~1.2Mpa
ROHS product	340°C ~370°C. Time : 3-5S.	350°C ~370°C. Time : 4-8 mm/s.	330°C ~360°C. Time : 3-6S. Press: 0.8~1.2Mpa

(1) If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.

(2) When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.

(3) When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

### Precautions for Operation

(1) Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.

(2) It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.

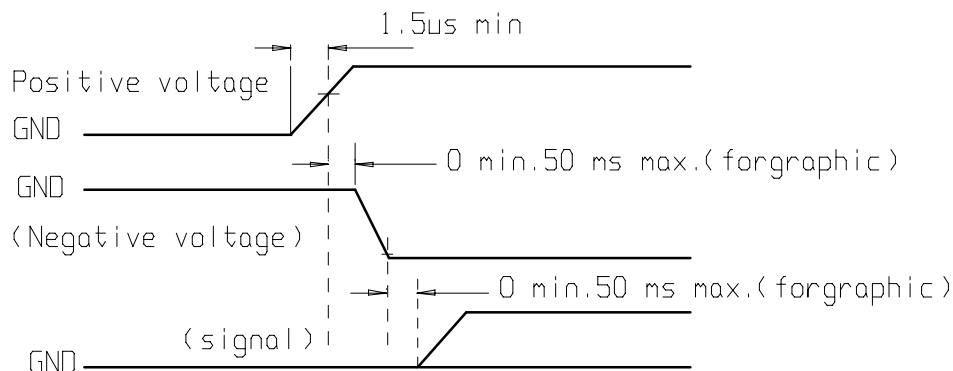
(3) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, Which will come back in the specified operating temperature.

(4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.

(5) A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50%RH or less is required.

(6) Input each signal after the positive/negative voltage becomes stable.

(7) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.



**Safety**

(1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.

(2) If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

**Limited Warranty**

Unless agreed between Multi-Inno and customer, Multi-Inno will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with Multi-Inno LCD acceptance standards (copies available upon request) for a period of one year from date of production. Cosmetic/visual defects must be returned to Multi-Inno within 90 days of shipment. Confirmation of such date shall be based on data code on product. The warranty liability of Multi-Inno limited to repair and/or replacement on the terms set forth above. Multi-Inno will not be responsible for any subsequent or consequential events.

**Return LCM under warranty**

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

- Broken LCD glass.
- PCB eyelet is damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- Soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet, conductors and terminals.

**■ PRIOR CONSULT MATTER**

1. ① For Multi-Inno standard products, we keep the right to change material, process ... for improving the product property without notice on our customer.  
② For OEM products, if any change needed which may affect the product property, we will consult with our customer in advance.
2. If you have special requirement about reliability condition, please let us know before you start the test on our samples.