

LCD MODULE SPECIFICATION

Model: DF-TFN1209FW-M1

This module uses ROHS materials

For customer acceptance

| | omer weekpramer | |
|----------|-----------------|------|
| Customer | | date |
| Approved | | |
| Comments | | |

The standard product specification may change without prior notice in order to improve performance or quality. Please contact Display Future Ltd for updated specification and product status before design for the standard product or release of the order.

| Revision | 1.0 |
|---------------|-----------|
| Engineering | |
| Date | 2018/01/4 |
| Our Reference | |

REVISION RECORD

| REV NO. | REV DATE | CONTENTS | REVISED PAGE NO. |
|---------|------------|---------------|---------------------|
| 1.0 | 2014-09-11 | First Release | |
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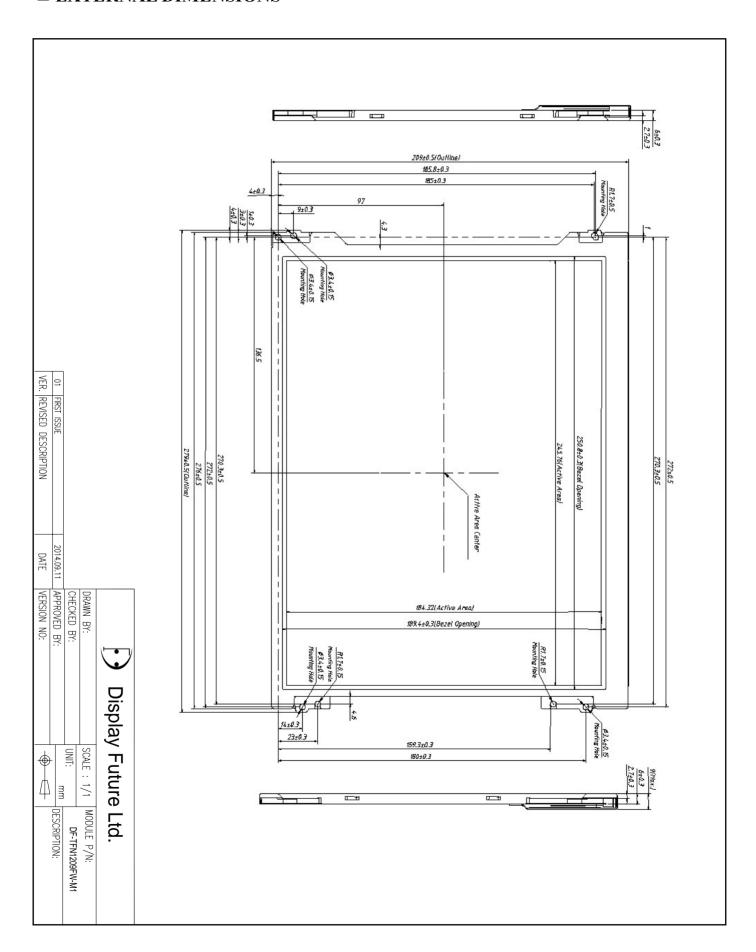
■ GENERAL INFORMATION

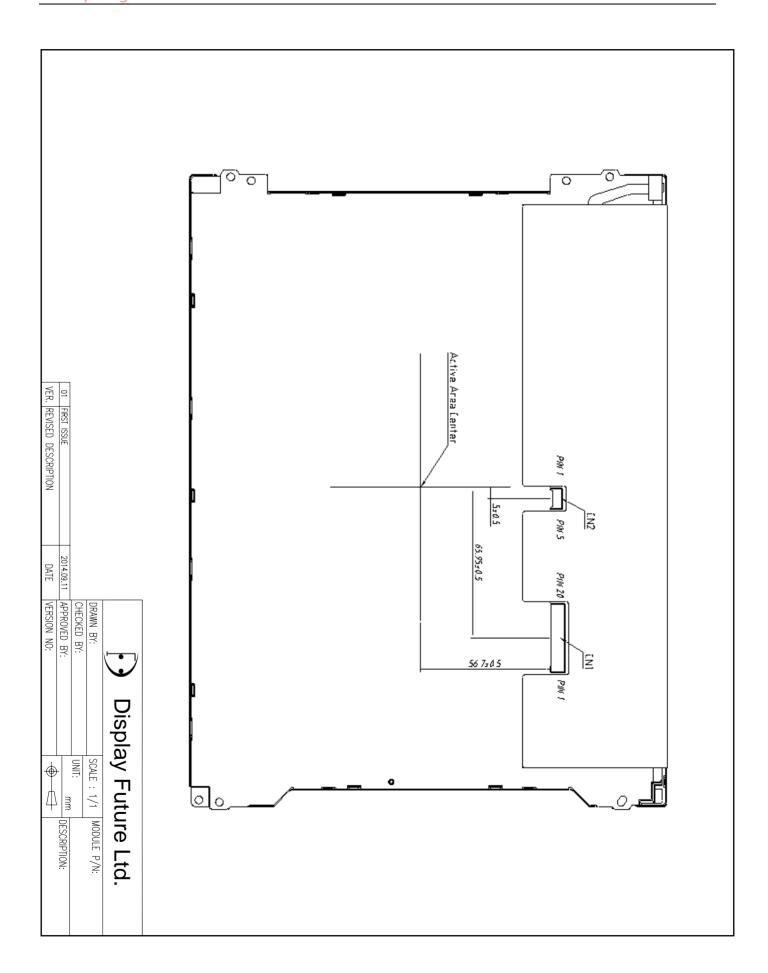
| Item Contents | | Unit |
|----------------------------|--------------------------|-----------------|
| LCD type | TFT/Normally white | / |
| Size | 12.1 | Inch |
| Viewing direction | Full viewing angle | O' Clock |
| $LCM(W \times H \times D)$ | 279.0×209.0×9.0 | mm ³ |
| Active area (W×H) | 246.76×184.32 | mm ² |
| Pixel pitch (W×H) | 0.24×0.24 | mm ² |
| Number of dots | 1024(RGB) × 768 | / |
| Backlight type | LED | / |
| Interface type | LVDS | / |
| Color depth | 16.7M | / |
| Pixel configuration | R.G.B vertical stripe | / |
| Surface treatment | Anti-glare & hardness 3H | / |
| Power consumption | 6.925 | W |
| Input voltage | 3.3 | V |
| With/Without TSP | Without TSP | / |
| Weight | 545 | g |

Note 1: RoHS compliant;

Note 2: LCM weight tolerance: ± 5%.

■ EXTERNAL DIMENSIONS



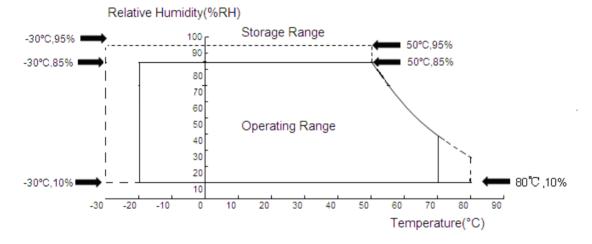


■ABSOLUTE MAXIMUM RATINGS

| Parameter | Symbol | Min | Max | Unit | Remark |
|-----------------------|--------|------|-----|------|--------------|
| Supply voltage | VDD | -0.5 | 5.0 | V | (1) |
| Operating temperature | Тор | -20 | 70 | °C | (1)(2)(3)(4) |
| Operating humidity | Нор | 10 | 85 | %RH | , |
| Storage temperature | Тѕт | -30 | 80 | °C | , |
| Storage Humidity | Нѕт | 10 | 95 | %RH | |

- Note (1): Humidity: 85%RH Max. (T<=40°C) Note static electricity.

 Maximum wet bulb temperature at 39°C or less. (T>40°C) No condensation.
- Note (2): There is a possibility of causing deterioration in the irregularity and others of the screen and the display fineness though the liquid crystal module doesn't arrive at destruction when using it at $80\sim85^{\circ}$ C or -20° C.
- Note (3): There is a possibility of causing the fineness deterioration by the prolonged use in the (high temperature) humidity environment (60% or more).
- Note (4): In the operating temperature item, the low temperature side is the ambient temperature regulations. The high temperature side is the panel surface temperature regulations.
- Note (5): Storage Range&Operating Range Picture:

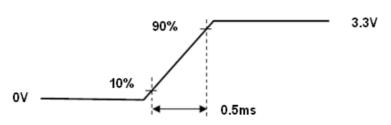


■ELECTRICAL CHARACTERISTICS

| Parameter | Symbol | Min | Тур | Max | Unit | Note |
|--|--------|-----|-----|-------|------|-----------|
| LCD drive voltage(logic) | VDD | 3.0 | 3.3 | 3.6 | V | (2)(4) |
| VDD current(Black pattern) | IDD | - | - | 250 | mA | |
| VDD power consumption (black pattern) | PDD | - | - | 0.825 | W | (3)(4)(6) |
| Rush current | Irush | - | - | 3 | A | (1)(4)(5) |
| Allowable logic/LCD drive ripple voltage | VDDrp | - | _ | 200 | mV | (4) |

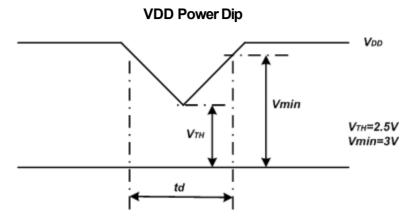
Note (1) Measure Condition

VDD Rising Time



Note (2) VDD Power Dip Condition

If VTH<VDD≤Vmin, then td≤10ms When the voltage returns to normal our panel must revive automatically.



Note (3) Frame Rate=60Hz, VDD=3.3V,DC Current.

Note (4) Operating temperature 25°C, humidity 55%RH.

■ BACKLIGHT CHARACTERISTICS

| Item | Symbol | Min. | Тур. | Max. | Unit | Condition |
|---------------------|--------|-------|-------|------|------|-----------|
| LED voltage | VL | 10.8 | 12.0 | 12.6 | V | |
| LED current | IL | - | 240 | - | mA | |
| LED forward voltage | VF | 2.8 | 3.3 | 3.6 | V | Ta=25°C |
| LED forward current | IF | - | 60 | - | mA | |
| Power consumption | PL | - | - | 6.1 | mW | |
| LED life time | - | 30000 | 50000 | - | Hrs | Note (1) |

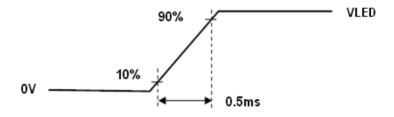
Note (1) The LED life time define as the estimated time to 50% degradation of initial luminous.

Note (1) The LED life time define as the estimated time to 50% degradation of initial luminous.

Note (2) Operating temperature 25°C, humidity 55%RH.

Note (3) A higher LED power supply voltage will result in better power efficiency. Keep the V_{LED} between 12V and 12.6V is strongly recommended.

LED Rush Current Measure Condition



■ELECTRO-OPTICAL CHARACTERISTICS

| Item | | Symbol | Condition | Min | Тур | Max | Unit | Remark | Note |
|--------------------|---------------------|------------|----------------|-------|-------|-------|-------------------|--------|------|
| Response | time | Tr+Tf | | - | 16 | - | ms | FIG 1. | 4 |
| Contrast r | atio | Cr | θ=0° | 720 | 800 | - | | FIG 2. | 1 |
| Luminar uniform | | δ WHITE | Ø=0° Ta=25℃ | 75 | 80 | - | % | FIG 2. | 3 |
| Surface Lum | inance | Lv | | 315 | 350 | - | cd/m ² | FIG 2. | 2 |
| | | | Ø = 90° | 70 | 80 | - | deg | FIG 3. | |
| Viouving and | Viewing angle range | | Ø = 270° | 70 | 80 | - | deg | FIG 3. | 6 |
| viewing angi | | | | 70 | 80 | - | deg | FIG 3. | |
| | | | Ø = 180° | 70 | 80 | - | deg | FIG 3. | |
| | Red | X | | ı | - | 1 | | | |
| | Reu | у | | • | - | - | | | |
| | Green | X | θ=0° | - | - | - | | | |
| chromaticity Blue | Green | у | Ø=0° | - | - | - | | FIG 2. | 5 |
| | Rhie | X | Ta=25℃ | - | - | - | | 110 2. | |
| | Blue | у | 1 a-23 C | - | - | - | | | |
| | White | X | | 0.255 | 0.305 | 0.355 | | | |
| | VV IIIC | у | | 0.275 | 0.325 | 0.375 | | | |

Note 1. Contrast Ratio(CR) is defined mathematically as For more information see FIG 2.

Contrast Ratio = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3. The uniformity in surface luminance $, \delta$ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see FIG 2.

δ WHITE = Minimum Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)

Maximum Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)

- Note 4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1. The test equipment is Autronic-Melchers's ConoScope. Series.
- Note 5. CIE (x, y) chromaticity, The x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.
- Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the conrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.
- Note 7. For viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments For contrast ratio, Surface Luminance, Luminance uniformity, CIE The test data is base on TOPCON's BM-5 photo detector.

FIG. 1 The definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

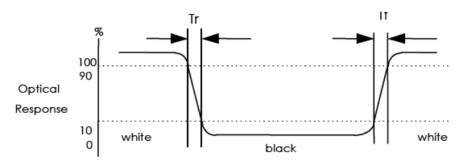
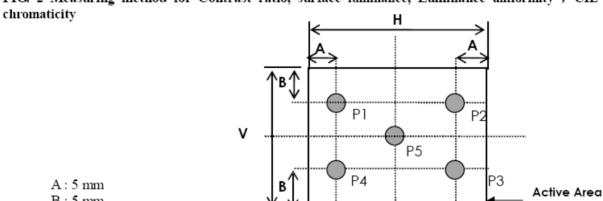


FIG. 2 Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y)

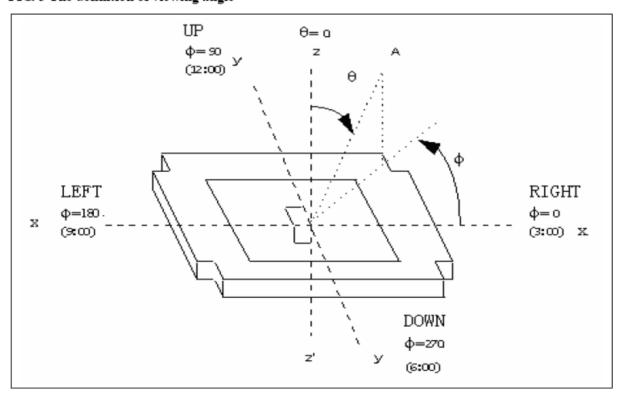


B:5 mm

H,V: Active Area

Light spot size ∅=7mm, 500mm distance from the LCD surface to detector lens measurement instrument is TOPCON's luminance meter BM-5

FIG. 3 The definition of viewing angle



■ INTERFACE DESCRIPTION

1.Interface connector

Connector Name/Designation

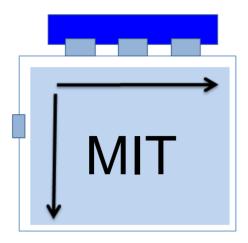
| Item | Description |
|------------------------|--|
| FPC Down Connector | Connector recommended model: MSB240420HE |
| (20pin pitch=1.25mm) | Manufactured by STM |

Signal Pin Assignment

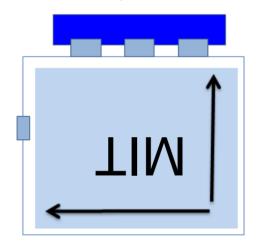
| Pin # | Symbol | Description |
|-------|--------|--|
| 1 | VDD | Power Supply, 3.3V (typical) |
| 2 | VDD | Power Supply, 3.3V (typical) |
| 3 | VSS | Ground |
| 4 | REV | Reverse Scan selection |
| 4 | KEV | {High:2.5(min), 3.3(typ),3.6(max); Low: 0.5(max)} |
| 5 | Rin1- | -LVDS differential data input (R0-R5,G0) |
| 6 | Rin1+ | +LVDS differential data input (R0-R5,G0) |
| 7 | VSS | Ground |
| 8 | Rin2- | -LVDS differential data input (G1-G5,B0-B1) |
| 9 | Rin2+ | +LVDS differential data input (G1-G5,B0-B1) |
| 10 | VSS | Ground |
| 11 | Rin3- | -LVDS differential data input (B2-B5,HS,VS,DE) |
| 12 | Rin3+ | +LVDS differential data input (B2-B5,HS,VS,DE) |
| 13 | VSS | Ground |
| 14 | CIKIN- | -LVDS differential clock input |
| 15 | CIkIN+ | +LVDS differential clock input |
| 16 | GND | Ground |
| 17 | Rin4- | -LVDS differential data input (R6-R7,G6-G7,B6-B7) |
| 18 | Rin4+ | +VDS differential data input (R6-R7,G6-G7,B6-B7) |
| 19 | SEL68 | 6/8 bits LVDS data input selection(H:8bit L/NC:6bit) |
| 20 | Bist | Internal use |

Note(1): All input signals shall be low or Hi-resistance state when VDD is off.

Note (2) REV = LOW/NC



Note (3) REV = High



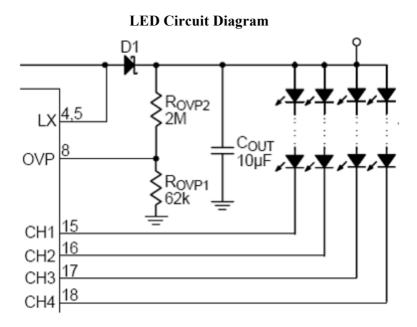
2. LED Interface Connector

Connector Name/Designation

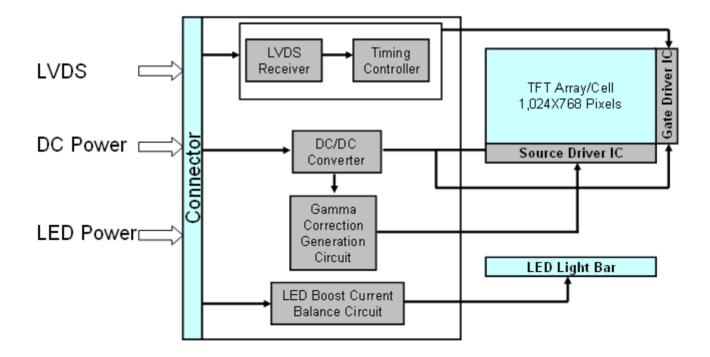
| Item | Description | | |
|-----------|------------------------------|--|--|
| Input LED | Connector model: MSB24038P5A | | |
| | Manufactured by STM | | |

Input LED Signal Pin Assignment

| Pin # | Function |
|-------|----------------------|
| 1 | VCC(12V input) |
| 2 | GND |
| 3 | On/Off(5V-ON,0V-OFF) |
| 4 | Dimming(PWM) |
| 5 | NC |



■ BLOCK DIAGRAM



■ APPLICATION NOTES

- 1. LVDS Receiver
 - 1.1 Signal Electrical Characteristics For LVDS Receiver

The built-in LVDS receiver is compatible with (ANSI/TIA/TIA-644) standard.

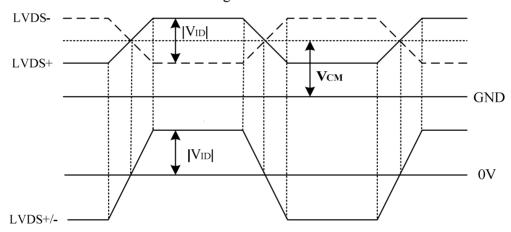
LVDS Receiver Eletrical Characteristics

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Conditions |
|--------------------------------------|-----------------|------------|------|-------------|------|------------------------|
| Differential Input High Threshold | Vth | - | - | +100 | mV | V _{CM} =+1.2V |
| Differential Input Low Threshold | VtI | -100 | - | - | mV | V _{CM} =+1.2V |
| Magnitude Differential Input Voltage | V _{ID} | 100 | - | 600 | mV | - |
| Common Mode Voltage | V_{CM} | VID /2+0.6 | 1.2 | 1.8- VID /2 | V | - |
| Common Mode Voltage Offset | ΔV_{CM} | - | - | 50 | mV | V _{CM} =+1.2V |

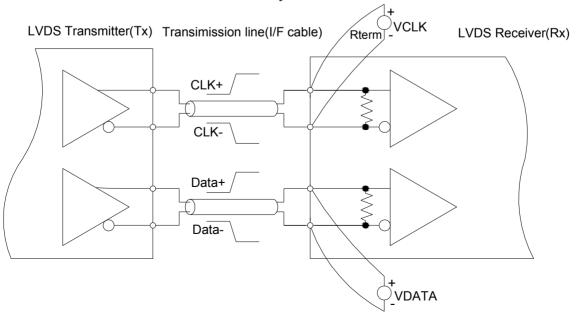
Note: (1) Input signals shall be low or Hi- resistance state when VDD is off.

(2) All electrical characteristics for LVDS signal are defined and shall be measured at the interface connector of LCD.

Voltage Definitions



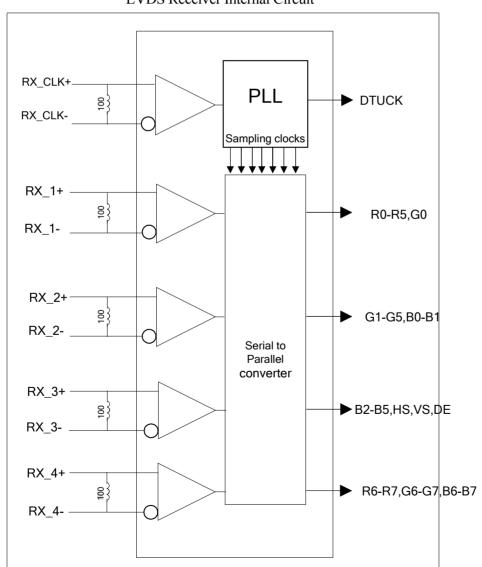
Measurement System



Data Mapping OLVCLKP **OLVCLKN** OLV0P R[0] G[0] R[5] **R[3]** R[0] R[1] **R[4]** R[2] R[1] G[0] **OLVON** OLV1P G[2] G[1] B[1] B[0] G[5] G[4] G[3] G[2] G[1] B[1] OLV1N OLV2P B[3] B[2] DE VS HS B[5] B[4] B[3] B[2] DE OLV2N OLV3P B[7] R[7]R[6] B[6] G[7] G[6] **R[7] R[6]** OLV3N Previous cycle Next cycle Current cycle

1.2 LVDS Receiver Internal Circuit

LVDS Receiver Internal Circuit shows the internal block diagram of the LVDS receiver. This LCD module equips termination resistors for LVDS link.

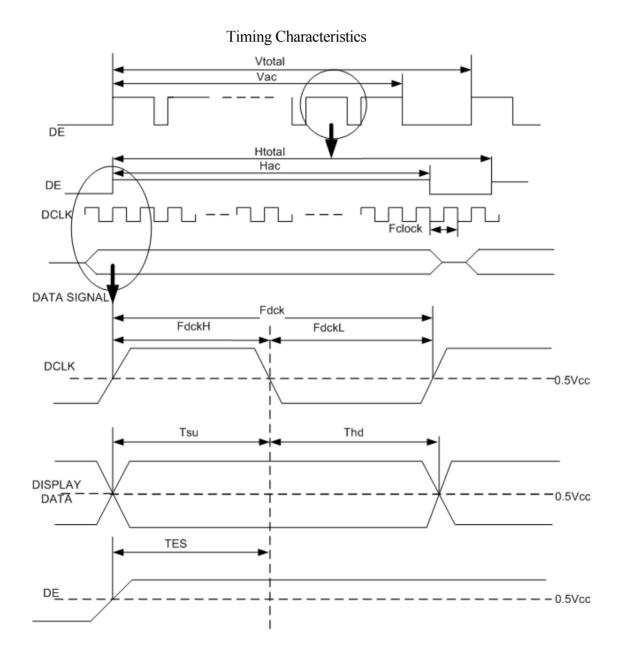


LVDS Receiver Internal Circuit

2. Interface Timings

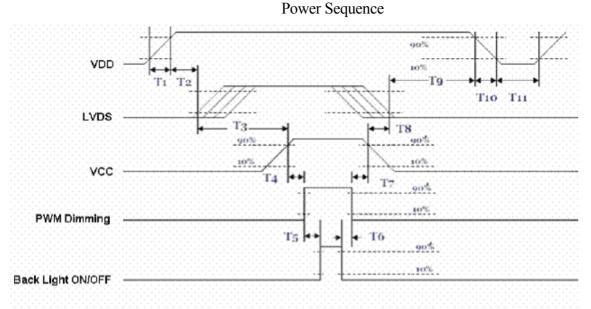
| Parameter | Symbol | Unit | Min. | Тур. | Max. |
|----------------------|--------|--------|------|------|------|
| LVDS Clock Frequency | Fclk | MHz | 50 | 65 | 80 |
| H Total Time | HT | Clocks | 1054 | 1344 | 2047 |
| H Active Time | HA | Clocks | 1024 | 1024 | 1024 |
| H Blanking Time | HBL | Clocks | 40 | 320 | 1023 |
| V Total Time | VT | Lines | 776 | 806 | 1023 |
| V Active Time | VA | Lines | 768 | 768 | 768 |
| V Blanking Time | VBL | Lines | 8 | 38 | 255 |
| Frame Rate | Vsync | Hz | 55 | 60 | 65 |

Note: H Blanking Time and V Blanking Time can not be changed at every frame.



3. Power ON/OFF Sequence

Power on/off sequence is as follows. Interface signals are also shown in the chart. Signals from any system shall be Hi-resistance state or low level when VDD is off.



Power ON/OFF sequence timing

| Parameter | Min. | Тур. | Max. | Units |
|-----------|------|------|------|-------|
| T1 | 0.5 | - | 10 | [ms] |
| T2 | 30 | 40 | 50 | [ms] |
| Т3 | 200 | - | - | [ms] |
| T4 | 10 | ۴ | - | [ms] |
| T5 | 10 | | - | [ms] |
| Т6 | 0 | - | - | [ms] |
| T7 | 10 | - | - | [ms] |
| Т8 | 100 | - | - | [ms] |
| Т9 | 0 | 16 | 50 | [ms] |
| T10 | - | - | 10 | [ms] |
| T11 | 1000 | - | - | [ms] |

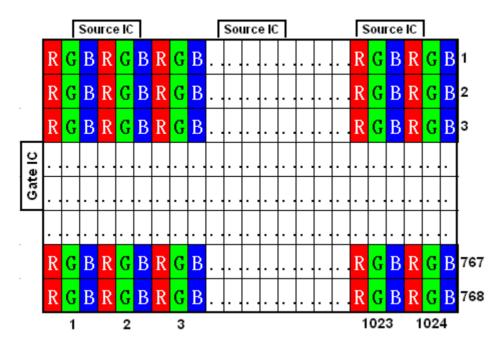
Note (1) Power On Sequence: VCC-> AVDD -> VGL -> VGH -> Data -> B/L

(2) Power Off Sequence: B/L-> Data -> VGH -> VGL -> AVDD -> VCC

4. Pixel Format Image

Shows the relationship of the input signals and LCD pixel format image.

Pixel Format



R+G+B dots=1 pixel

■ RELIABILITY TEST

| Item | | Package Test Conditions | | Note | |
|---------------------------------|-------------------------|-------------------------|---|------------------|-------------|
| High Temperature Operation Test | | Module | 70°C, 500hrs | | 1,4,5,6,7,8 |
| Low Temperature | e Operating Test | Module | -20°C, 500hrs | | 1,4,5,6,7,8 |
| High Temp./High Operating Test | Humidity | Module | 50℃, 85%, 500h | 50℃, 85%, 500hrs | |
| High Temp./High Storage Test | Humidity | Module | 50℃, 90%, 500h | rs | 1,5,6,7,8 |
| Thermal Shock N | lon-operation | Module | -30°C~80°C, 1h cycle,100cycles (200cycles for re | | 1,5,6,7,8 |
| Shock(single chip) | | Module | 3 shock in each direction Peak acceleration:981m/s2 Half Sine Wave; 6ms | | 1,7,8 |
| Vibration (single | Vibration (single chip) | | 1.5G , 10~500 Hz , x、y、z each axis/1h | | 1,7,8 |
| Drop Test | Drop Test | | 65cm, 1corner,3 arris,6 side | | 1,8 |
| Vibration Test | | With package | 1.5G , 10~500 Hz , x、y、z each axis/1h | | 1,8 |
| | operating | | contact | ± 8 KV | |
| ESD Test | | Madula | air | ± 15 KV | 04570 |
| ESD Test | | Module | contact | ± 10 KV | 2,4,5,7,8 |
| | non-operating | | air | ± 20 KV | |
| Image Sticking test | | Module | 5*7 Chess pattern; 1. Normal temperature: 50% Grayscale, 2h 10s/4h 10s/8h 2min/24h 5min ND 8%OK 2. high temperature 70°C: 50% Grayscale, 2h 10s/4h 10s/8h 2min/24h 10min ND 8% OK | | 3,4,6,7,8 |

Note:

- 1. There is no function defect and occurrence of any new defective shall not be allowed.
- 2. In case of malfunction defect caused by ESD damage. If it would be recovered to normal state after resetting, it would be judge as pass.
- 3. 25°C: Image Sticking is not visible through 8% ND filter after 5 min with pattern L127. 70°C: Image Sticking is not visible through 8% ND filter after 10 min with pattern L127.
- 4. In Operating test, the B/L voltage and current must be in spec.

- 5. All the judgments are under normal temperature and the sample need to be static more than 2 hours in the normal temperature before judge.
- 6. During measurement, the condensation water or remains shall not be allowed.
- 7. The minimum sample quantity of test is 3pcs.
- 8. There is no display function fail issue occurred, all the cosmetic specification is judged before the reliability stress.

■ INSPECTION CRITERION

| OUTGOING QUALITY STANDARD | PAGE 1 OF 5 |
|---|-------------|
| TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA | |

This specification is made to be used as the standard acceptance/rejection criteria for TFT module.

1 Sample plan

1.1 Lot size: Quantity per shipment lot per model

1.2 Sampling type: Normal inspection, Single sampling

1.3 Inspection level: II

1.4 Sampling table: MIL-STD-105D1.5 Acceptable quality level (AQL)

Major defect: AQL=0.65 Minor defect: AQL=1.50

2. Inspection condition

2.1 Ambient conditions:

a. Temperature: Room temperature $25\pm5^{\circ}$ C

b. Humidity: (60± 10) %RH

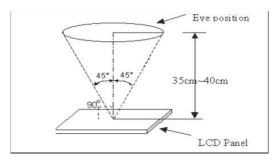
c. Illumination: Single fluoresænt lamp non-directive (300 to 700 Lux)

2.2 Viewing distance:

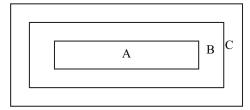
The distance between the LCD and the inspector's eyes shall be at least $35\pm$ 5cm.

2.3 Viewing Angle

U/D: 45° /45°, L/R: 45° /45°



- 3. Definition of Inspection Item.
 - 3.1 Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD.

| OUTGOING QUALITY STANDARD | PAGE 2 OF 5 |
|---|-------------|
| TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA | |

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.
4. Inspection standards

Defects are classified as majot defects and minor defects according to the degree of defectiveness defined herein.

4.1 Major defect

| Item No | Items to be inspected | Inspection Standard | |
|---------|------------------------|--|--|
| 4.1.1 | All functional defects | No display Display abnormally Short circuit Line defect Excess power consumption | |
| 4.1.2 | Missing | Missing function component | |
| 4.1.3 | Crack | Glass crack | |

4.2 Minor defect

| Item No | Items to be inspected | Inspection standard | | |
|------------|-------------------------------------|--|-----------------------------|--|
| 4.2.1 | Spot Defect Including Black spot | For dark/white spot is def $\varphi = (\mathbf{x} + \mathbf{y}) / 2$ $\longrightarrow \mathbf{X} \qquad \qquad$ | ined | |
| | White spot Pinhole Foreign particle | Size φ(mm) φ≤0.15 2mm(min) apart | Acceptable Quantity Ignore | |
| | Polarizer dirt | 0.15 < φ≤ 0.25 5mm(min) apart | 3 | |
| | | 0.25<φ | Not allowed | |

| | OUTGOIN | PAGE 3 OF 5 | |
|---|----------------------------------|---|-------------------------------|
| ΓΙΤLE:FUNCTIONAL TEST & INSPECTION CRITERIA | | | |
| | | Define: | Vidth |
| 4.2.2 | Line Defect Including Black line | Width(mm) Length(mm) | Acceptable Quantity |
| | White line | W≤0.05 and L≤10 | Ignore |
| | Scratch | 0.05 < W≤0.08 and L≤10 3mm(min) apart | 3 |
| | | 0.08 < W≤0.10 andL≤5 3mm(min) apart | 1 |
| | | 0.10< W or 10 <l< td=""><td>Not allowed</td></l<> | Not allowed |
| | | Size φ(mm) | Acceptable Quantity |
| | Polarizer | φ≤0.25 | Ignore |
| 4.2.3 | Dent/Bubble | Non visible area | Ignore |
| | | 0.25<φ≤ 0.40 5mm(min) apart | 2 |
| | | 0.40< φ | Not allowed |
| | | Bright and Black dot defi | and |
| 4.2.4 | 4.2.4 Electrical Dot Defect | Inspection pattern: Full and blue screens | white, Full black, Red, green |
| | | Item | Acceptable Quantity |
| | | Black dot defect | 2 |
| | | Bright dot defect | 0 |
| | | Total Dot | 2 |

| ITLE:FUNC | | G QUALITY STANDARD NSPECTION CRITERIA | PAGE 4 OF 5 |
|-------------------|---------------------------|--|---|
| 4.2.5 Touch panel | 1.Corner chips: Size(mm) | Acceptable Quantity Ignore | |
| | X≤3mm Y≤3mm Z≤T | T: Glass thickness X: Length Y: Width Z: thickness | |
| | chips | 2. Side chips: | X Z |
| | | Size(mm) | Acceptable Quantity |
| | | X≤5mm Y ≤3mm Z≤T | Ignore T: Glass thickness X: Length Y: Width Z: thickness |

| OUTGOING QUALITY STANDARD | PAGE 5 OF 5 | |
|---|-------------|--|
| TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA | | |

Note: 1. Dot defect is defined as the defective area of the dot area is larger than 50% of the dot area.

- 2. The distance between black dot defects or black and bright dot defects should be more than 5mm apart. The distance between two bright dot defects should be more than 15mm apart
- 3. Polarizer bubble is defined as the bubble appears on active display area. The defect of polarizer bubble shall be ignored if the polarizer bubble appears on the outside of active display area.
- 4. Mura is checker by 6% ND filter.
- 5. Foreign particle on the surface of the LCM should be ignore.

■ PRECAUTIONS FOR USING LCD MODULES

Handing Precautions

- (1) The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

- (6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.

- (7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
 - (9) Do not attempt to disassemble or process the LCD module.
 - (10) NC terminal should be open. Do not connect anything.
 - (11) If the logic circuit power is off, do not apply the input signals.
- (12) Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
- Tools required for assembling, such as soldering irons, must be properly grounded. make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential
- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated

- (13) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
 - Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - Do not damage or modify the pattern writing on the printed circuit board.
 - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
 - Do not drop, bend or twist LCM.

Handling precaution for LCM

LCM is easy to be damaged.

Please note below and be careful for handling!

Correct handling:



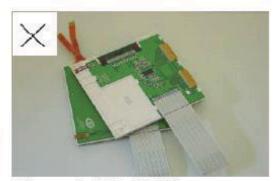


As above picture, please handle with anti-static gloves around LCM edges.

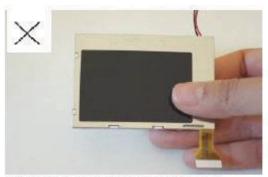
Incorrect handling:



Please don't touch IC directly.



Please don't stack LCM.



Please don't hold the surface of panel.



Please don't stretch interface of output, such as FPC cable.

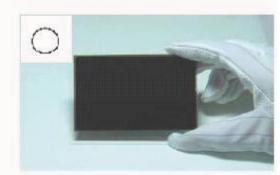
Handling precaution for LCD

LCD is easy to be damaged.

Please note below and be careful for handling!

Correct handling:





As above photo, please handle with anti-static gloves around LCD edges.

Incorrect handling:



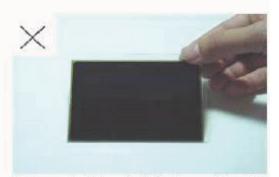
Please don't stack the LCDS.



Please don't operate with sharp stick such as pens.



Please don't hold the surface of LCD.



Please don't touch ITO glass without anti-static gloves.

Storage Precautions

When storing the LCD modules, the following precaution is necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the anti-static electricity container in which they were shipped. Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

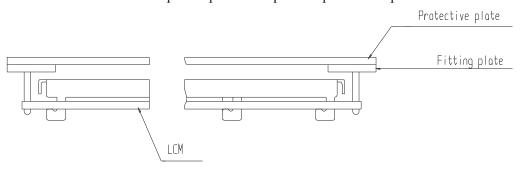
- Exposed area of the printed circuit board.
- -Terminal electrode sections.

USING LCD MODULES

Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

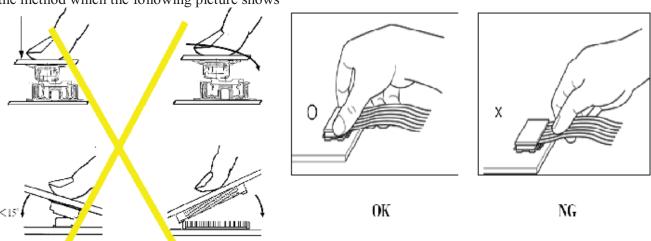
(1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



(2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be ± 0.1 mm.

Precaution for assemble the module with BTB connector:

Please note the position of the male and female connector position,don't assemble or assemble like the method which the following picture shows



Precaution for soldering to the LCM

| | Hand soldering | Machine drag soldering | Machine press soldering |
|---------|----------------|------------------------|-------------------------|
| No ROHS | 290°C ~350°C. | 330°C ~350°C. | 300°C ~330°C. |
| product | Time : 3-5S. | Speed: 4-8 mm/s. | Time : 3-6S. |
| product | | | Press: 0.8~1.2Mpa |
| ROHS | 340°C ~370°C. | 350°C ~370°C. | 330°C ~360°C. |
| product | Time : 3-5S. | Time: 4-8 mm/s. | Time : 3-6S. |
| product | | | Press: 0.8~1.2Mpa |

- (1) If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- (2) When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- (3) When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

Precautions for Operation

- (1) Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.
- (2) It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- (3) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, Which will come back in the specified operating temperature.
- (4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- (5) A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature,50%RH or less is required.
 - (6) Input each signal after the positive/negative voltage becomes stable.
- (7) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

Safety

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

Safety

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Limited Warranty

Unless agreed otherwise between Display Future Ltd and customer, Display Future will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with LCD acceptance standards (copies available upon request) for a period of one year from date of production. Cosmetic/visual defects must be returned within 90 days of the shipment. Confirmation of such date shall be based on data code on the product.

The warranty liability of Display Future limited to repair and/or replacement on the terms set forth above. Display Future will not be responsible for any subsequent or consequential events.

Return LCM under warranty

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

- Broken LCD glass.
- PCB eyelet is damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- Soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet, conductors and terminals.

■ PRIOR CONSULT MATTER

- 1. ①For Display Future standard products, we keep the right to change material and processes for improving the product, without notice to our customers.
 - ②For OEM products, if any change is needed, which may affect the product property, we will consult with our customer in advance.
- 2. If you have special requirement about reliability condition, please let us know before you start the test on our samples.

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