

LCD MODULE SPECIFICATION

Model: DF-TFN0350FW-M1

This module uses ROHS materials

For customer acceptance

	omer weekpramer	
Customer		date
Approved		
Comments		

The standard product specification may change without prior notice in order to improve performance or quality. Please contact Display Future Ltd for updated specification and product status before design for the standard product or release of the order.

Revision	1.0
Engineering	
Date	2018/01/4
Our Reference	

REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2012-10-09	First Release	

CONTENTS

- GENERAL INFORMATION
- EXTERNAL DIMENSIONS
- ABSOLUTE MAXIMUM RATINGS
- ELECTRICAL CHARACTERISTICS
- BACKLIGHT CHARACTERISTICS
- ELECTRO-OPTICAL CHARACTERISTICS
- INTERFACE DESCRIPTION
- BLOCK DIAGRAM
- APPLICATION NOTES
- RELIABILITY TEST
- INSPECTION CRITERION
- PRECAUTIONS FOR USING LCD MODULES
- PRIOR CONSULT MATTER

■ GENERAL INFORMATION

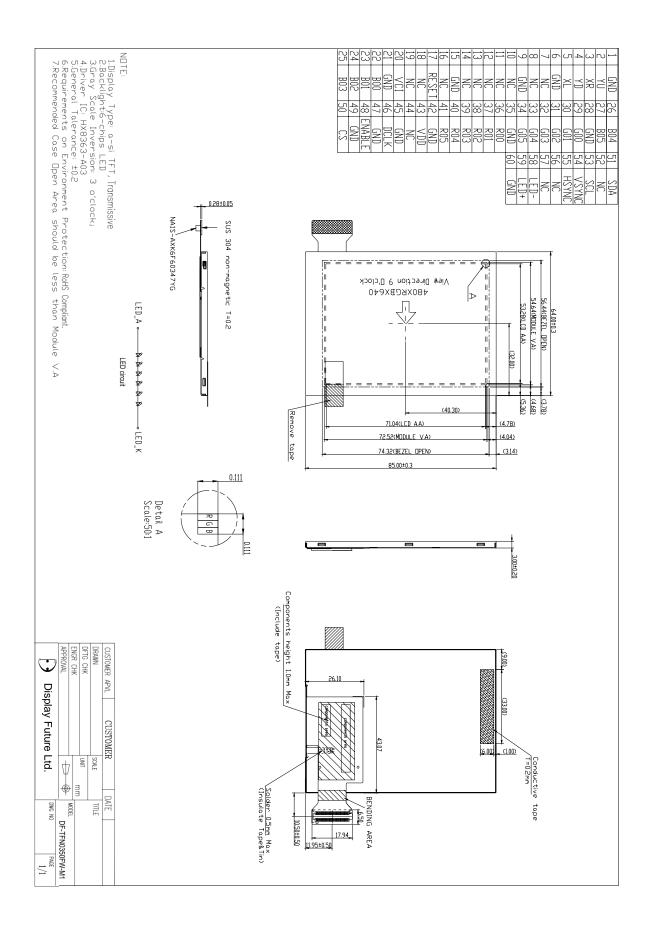
Item	Contents	Unit
LCD type	TN TFT/Transmissive/Normally white	/
Size	3.5	Inch
Viewing direction	9:00	O' Clock
Gray scale inversion direction	3:00	O' Clock
$LCM(W \times H \times D)$	64.00×85.00×3.00	mm ³
Active area (W×H)	53.28×71.04	mm^2
Pixel pitch (W×H)	0.111×0.111	mm ²
Number of dots	480 (RGB) × 640	/
Driver IC	HX8363-A03	/
Backlight type	6 LEDs	/
Interface type	RGB 18 bits+SPI	/
Color depth	262K	/
Pixel configuration	R.G.B vertical stripe	/
Surface treatment(Up polarizer)	AG	/
Input voltage	3.3	V
With/Without TSP	Without TSP	/
Weight	TBD	g

Note 1: Viewing direction for best image quality is different from TFT definition, there is a 180 degree shift.

Note 2 : RoHS compliant;

Note 3: LCM weight tolerance: ± 5%.

■ EXTERNAL DIMENSIONS



■ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit
Power voltage	VDD	-0.3	4.6	V
Input signal voltage	VIN	GND	VDD	V
Operating temperature	Тор	-20	60	°C
Storage temperature	Tst	-30	70	°C
Humidity	RH	-	90%(Max60°C)	RH

■ELECTRICAL CHARACTERISTICS

DC CHARACTERISTICS

Parameter	Symbol	Min	Тур	Max	Unit
Power supply voltage	VDD	3.0	3.3	3.3	V
Input voltage 'H' level	V _{IH}	0.7VDD	-	VDD	V
Input voltage 'L' level	VIL	GND	-	0.3VDD	V
Output voltage 'H' level	Voh	0.8VDD	-	VDD	V
Output voltage ' L ' level	Vol	GND	-	0.2VDD	V

■ BACKLIGHT CHARACTERISTICS

Item	Symbol	Min.	Тур.	Max.	Unit	Condition
Forward voltage	Vf	-	19.2	-	V	
Forward current	If	-	20	-	mA	
Power consumption	WBL	-	384	-	mW	

Note1: The LED driving condition is defined for each LED module

Note2: Under LCM operating, the stable forward current should be inputted. And forward voltage is for reference only.

Note3: Optical performance should be evaluated at $Ta=25^{\circ}C$ only If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

Note4: The LED driving condition is defined for each LED module.



NTSC

Item		Symbol	Condition	Min	Тур	Max	Unit	Remark	Note
Response time		Tr+Tf			25	35	ms	FIG 1.	4
Contrast r	atio	Cr	θ=0°	400	500			FIG 2.	1
Luminar uniform		δ WHITE	Ø=0° Ta=25℃	70	75		%	FIG 2.	3
Surface Lum	inance	Lv		220	300		cd/m ²	FIG 2.	2
			Ø = 90°	60	70		deg	FIG 3.	
Viewin a en al	Viewing angle range		$\emptyset = 270^{\circ}$ $\emptyset = 0^{\circ}$	60	70		deg	FIG 3.	6
viewing angi				60	70		deg	FIG 3.	
			Ø = 180°	50	60		deg	FIG 3.]
	Red	X		0.530	0.580	0.630			
	Reu	у		0.270	0.320	0.370			
	Green	X	$\theta=0^{\circ}$	0.280	0.330	0.380			
CIE (x, y)	Often	у	Ø=0°	0.450	0.500	0.550		FIG 2.	5
chromaticity	Blue	X	Ta=25℃	0.110	0.160	0.210		110 2.	
	Biue	у] 1a-23 C	0.100	0.150	0.200			1
	White	X		0.251	0.301	0.351			
	W IIIC	у		0.271	0.321	0.371			

■ELECTRO-OPTICAL CHARACTERISTICS

Note 1. Contrast Ratio(CR) is defined mathematically as For more information see FIG 2.

Contrast Ratio = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

50

Note 3. The uniformity in surface luminance $, \delta$ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see FIG 2.

δ WHITE = Minimum Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)

Maximum Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)

- Note 4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1. The test equipment is Autronic-Melchers's ConoScope. Series.
- Note 5. CIE (x, y) chromaticity, The x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.
- Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the conrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.
- Note 7. For viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments For contrast ratio, Surface Luminance, Luminance uniformity, CIE The test data is base on TOPCON's BM-5 photo detector.

FIG. 1 The definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

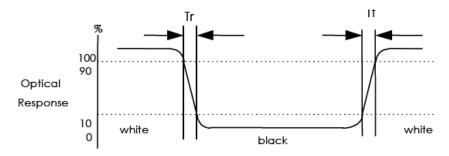
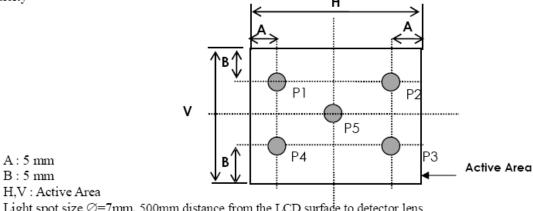
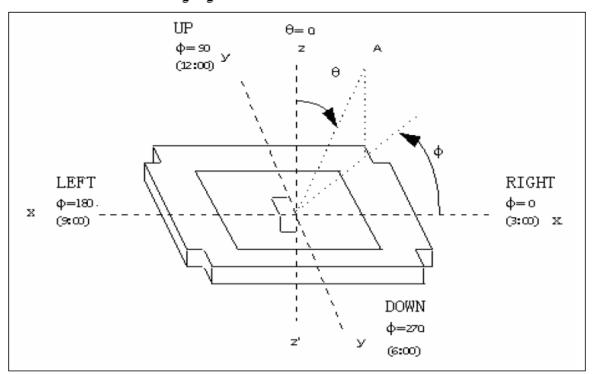


FIG. 2 Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity



Light spot size ∅=7mm, 500mm distance from the LCD surface to detector lens measurement instrument is TOPCON's luminance meter BM-5

FIG. 3 The definition of viewing angle



■INTERFACE DESCRIPTION

Pin	Symbol	I/O	Description	Remark
1	GND	Р	Ground	
2	YU	1	Touch Panel Upper side(No connection)	
3	XR	1	Touch Panel Right side(No connection)	
4	YD	1	Touch Panel Lower side(No connection)	
5	XL	1	Touch Panel Left side(No connection)	
6	GND	Р	Ground	
7	NC	1	Ne connection	
8	NC	1	No connection	
9	GND	Р	Ground	
10	NC	1		
11	NC	1		
12	NC	1	No connection	
13	NC	1		
14	NC	1		
15	GND	Р	Ground	
16	NC	1	No connection	
17	RESET	ı	Reset pin to Initial the IC, when RESET="L", reset enable	
18	NC	1	Ne connection	
19	NC	1	No connection	
20	VDD	Р	Power supply (+3.3V)	
21	GND	Р	Ground	
22~27	B00~B05	I	6bits Blue Data	
28	GND	Р	Ground	
29~34	G00~G05	I	6bits Green Data	
35	GND	Р	Ground	
36~41	R00~R05	I	6bits Red Data	
42	GND	Р	Ground	
43	VDD	Р	Power supply (+3.3V)	

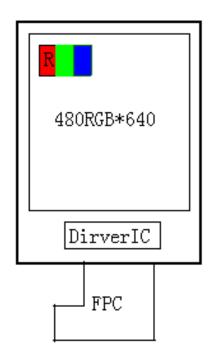
44	NC	1	No connection
45	GND	Р	Ground
46	MCLK	I	No connection
47	GND	Р	Ground
48	ENAB	I	Data Enable for RGB interface operation
49	GND	Р	Ground
50	CS	I	Chip select
51	SDA	1	Serial Data signal
52	NC	1	No connection
53	SCL	I	Serial clock signal
54	VS	I	Vertical Sync Signal
55	HS	Ī	Horizontal Sync Signal
56	NC	1	No connection
57	NC	1	No connection
58	LED-	Р	LED light cathode
59	LED+	Р	LED light anode (+19.2V)
60	GND	Р	Ground

Note1: I/O definition: I-----Input O---Output P----Power/Ground

Note2: Internal connection, PSWAP=1, DSWAP=00; Please refer to NT35516's spec.

■ BLOCK DIAGRAM

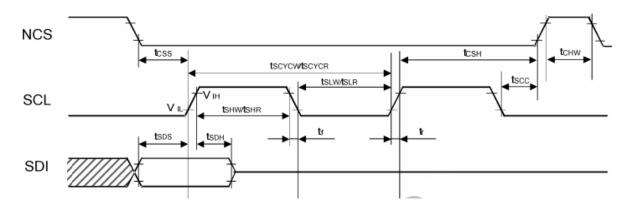
LCD module diagram



■ APPLICATION NOTES

1 INTERFACE TIMING

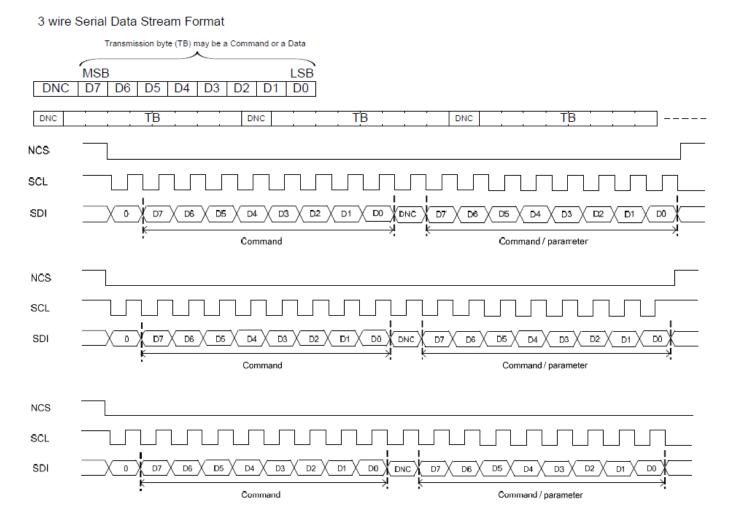
1.1 SERIAL INTERFACE CHARACTERISTICS(3-PIN SERIAL)



Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Serial clock cycle (Write)	tscycw		80	-	-	
SCL "H" pulse width (Write)	tshw	SCL	30	-	-	ns
SCL "L" pulse width (Write)	tsLw		30	-	-	
Data setup time (Write)	tsps	SDI	10	-	-	200
Data hold time (Write)	tspH	301	10	-	-	ns
Serial clock cycle (Read)	tscycr		150	-	-	
SCL "H" pulse width (Read)	tshr	SCL	60	-	-	ns
SCL "L" pulse width (Read)	tslr		60	-	-	
SCL to Chip select	tscc	NCS	30	-	-	ns
NCS "H" pulse width	tchw	NCS	60	-	-	ns
NCS-SCL time (write)	tcss	NCS	30	-		20
NCS-SCL time (write	tcsн	NC3	30	-	-	ns
NCS-SCL time (Read)	tcss	NCS	60	-		ne
NCS-SCL time (Read)	tcsн	NCS	65	-	-	ns

1.2 SERIAL DATA TRANSFER INTERFACE

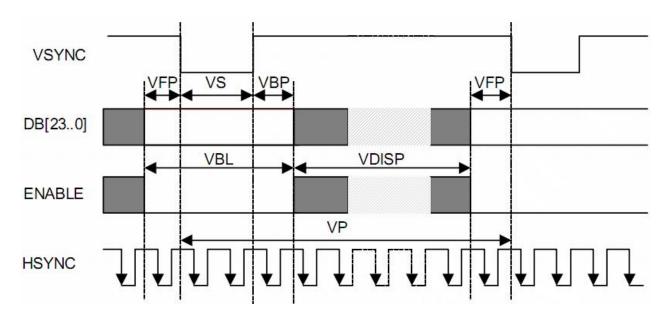
The 3-Pin serial data packet contains a control bit DNC and a transmission byte. If DNC is low, the transmission byte is command byte. If DNC is high, the transmission byte is stored to command register. The MSB is transmitted first. The serial interface is initialized when NCS is high. In this state, SCL clock pulse or SDI/SDO data have no effect. A falling edge on NCS enables the serial interface and indicates the start of data transmission.



Serial Interface protocol 3 wire serial interface (write mode)

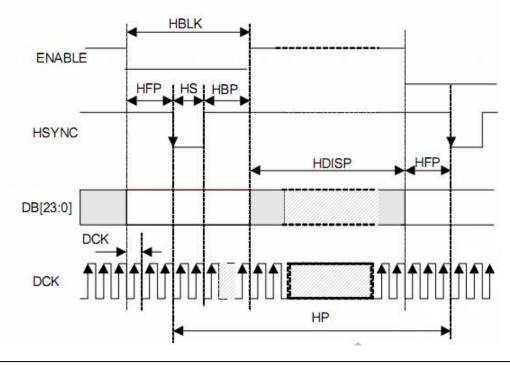
2. RGB INTERFACE CHARACTERISTICS

2.1 VERTICAL TIMING FOR RGB I/F



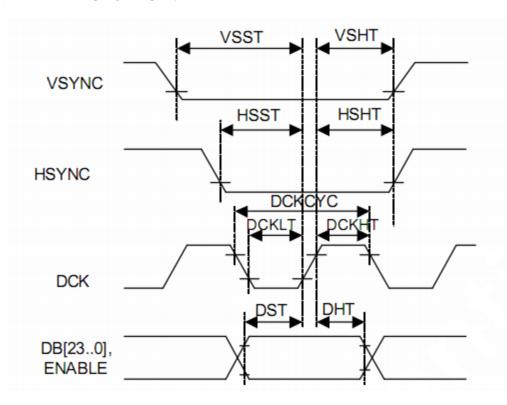
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Vertical cycle	VP	-	646	-	650	Line
Vertical low pulse width	VS	-	2	-	4	Line
Vertical front porch	VFP	-	2	-	4	Line
Vertical back porch	VBP	-	2	-	4	Line
Vertical data start point	-	VS+VBP	4	-	8	Line
Vertical blanking period	VBL	VS+VBP+VFP	6	-	10	Line
Vertical active area	-	VDISP	-	640	-	Line
Vertical Refresh rate	VRR	-	50	-	70	Hz

2.21 HORIZONTAL TIMING FOR RGB I/F



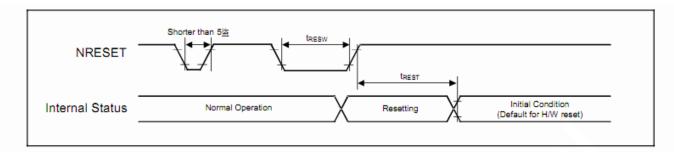
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
HS cycle	HP	Note	504	-	568	DCK
HS low pulse width	HS	-	5	-	78	DCK
Horizontal back porch	HBP	-	5	•	78	DCK
Horizontal front porch	HFP	-	5	-	78	DCK
Horizontal data start point		HS+HBP	19	-	83	DCK
Horizontal data start point	_	ПОТПОР	700	-	-	ns
Horizontal blanking period	HBLK	HS+HBP+HFP	24	-	88	DCK
Horizontal active area	HDISP	-	-	480	-	DCK
Pixel clock frequency	DCK	VRR = Min. 50Hz	16.3	22.2	25.8	MHz
When RGB I/F is running	DCK	- Max. 70Hz	38.7	-	61.3	ns

2.3 GENERAL TIMING FOR RGB I/F



Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Vertical sync. Setup time	VSST	-	5	-	-	ns
Vertical sync. Hold time	VSHT	-	5	-	-	ns
Horizontal sync. Setup time	HSST	-	5	-	-	ns
Horizontal sync. Hold time	HSHT	-	5	-	-	ns
Pixel clock low time	DCKLT	-	5	-	-	ns
Pixel clock high time	DCKHT	-	5	-	-	ns
Data setup time DB[23:0]	DST	-	5	-	-	ns
Data Hold time DB[23:0]	DHT	-	5	-	-	ns

3. RESET INPUT TIMING



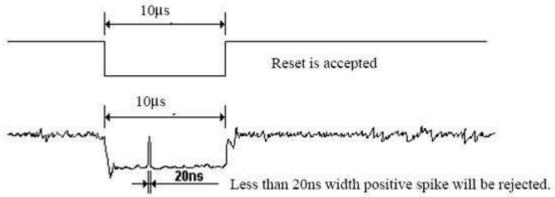
Symbol	Parameter	Related Pins	Min.	Тур.	Max.	Note	Unit
tRESW	Reset low pulse width ⁽¹⁾	NRESET	10	-	-		μs
tREST	Reset complete time	-	-	-	5	When reset is applied during Sleep In mode	ms
INEST	Reset complete time	-		-	120	When reset is applied during Sleep Out mode	ms

Note : (1). Spike due to an electrostatic discharge on NRESET line does not cause irregular system reset

according to the table below:

NRESET Pulse	Action
Shorter than 5µs	Reset Rejected
Longer than 10µs	Reset
Between 5µs and 10µs	Reset Start

(2) Spike Rejection also applies during a valid reset pulse as shown below:



(3)It is necessary to wait 5msec after releasing NRESET before sending commands. Also Sleep Out $\begin{tabular}{ll} \end{tabular}$

command cannot be sent for 120msec.

4. POWER ON/OFF TIMING

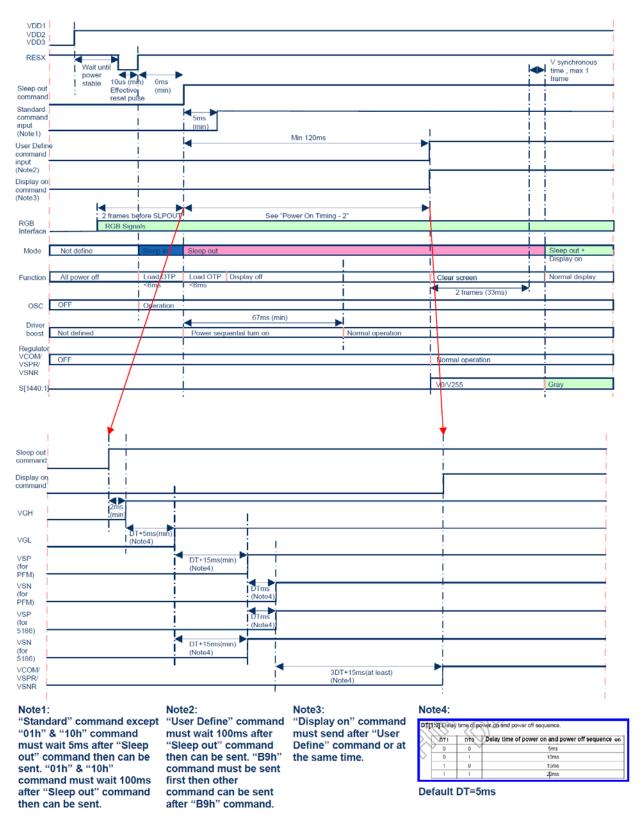
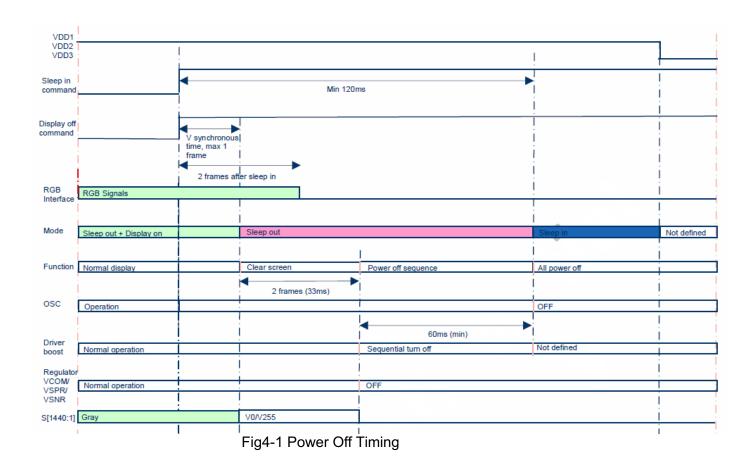


Fig4 Power ON Timing



Page 17 of 29

■ RELIABILITY TEST

No.	Test Item	Test Condition	Remark
1	High Temperature Storage	70±2°C/120 hours	IEC60068-2-1 GB2423.2
2	Low Temperature Storage	-30±2℃/120 hours	IEC60068-2-1 GB2423.1
3	High Temperature Operating	60 ± 2 °C/120 hours	IEC60068-2-1 GB2423.2
4	Low Temperature Operating	-20±2℃/120 hours	IEC60068-2-1 GB2423.1
5	Temperature Cycle storage	-30±2°C~25~70±2°C × 20cycles (30min.) (5min.) (30min.)	Start with cold temperature, with high temperature, IEC60068-2-14 GB2423.22
6	Damp proof Test operating	$60^{\circ}\text{C} \pm 5^{\circ}\text{C} \times 90\%\text{RH}/120 \text{ hours}$	IEC60068-2-78 GB/T2423.3
7	Vibration Test (non-operation)	Frequency range:10Hz~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2hours for each direction of X,Y,Z(6 hours for total)	IEC60068-2-6 GB/T2423.10
8	Package drop test	Height:80 cm,1 corner,3 edges,6 surfaces	IEC60068-2-32,GB2423.8
9	ESD test (operation)	C=150pF,R=330 Ω ,5points/panel Air: ±10KV,5times Contact: ±4KV,5times(Environment: 15°C~35°C,30%~60%,86Kpa~106Kpa)	IEC61000-4-2 GB/T17626.2
10	Shock(non-operation)	60G 6ms, ±X,±Y,±Z 3times each direction	IEC60068-2-27 GB/T2423.5

Note 1:Ts is the temperature of panel's surface. Note 2:Ta is the ambient temperature of sample.

■ INSPECTION CRITERION

OUTGOING QUALITY STANDARD	PAGE 1 OF 5
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA	

This specification is made to be used as the standard acceptance/rejection criteria for TFT module.

1 Sample plan

1.1 Lot size: Quantity per shipment lot per model

1.2 Sampling type: Normal inspection, Single sampling

1.3 Inspection level: II

1.4 Sampling table: MIL-STD-105D1.5 Acceptable quality level (AQL)

Major defect: AQL=0.65 Minor defect: AQL=1.50

2. Inspection condition

2.1 Ambient conditions:

a. Temperature: Room temperature $25\pm5^{\circ}$ C

b. Humidity: (60± 10) %RH

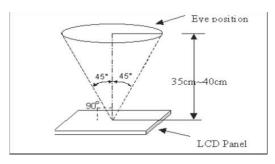
c. Illumination: Single fluoresænt lamp non-directive (300 to 700 Lux)

2.2 Viewing distance:

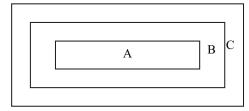
The distance between the LCD and the inspector's eyes shall be at least $35\pm$ 5cm.

2.3 Viewing Angle

U/D: 45° /45° , L/R: 45° /45°



- 3. Definition of Inspection Item.
 - 3.1 Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD.

OUTGOING QUALITY STANDARD	PAGE 2 OF 5
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA	

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.
4. Inspection standards

Defects are classified as major defects and minor defects according to the degree of defectiveness defined herein.

4.1 Major defect

Item No	Items to be inspected	Inspection Standard
4.1.1	All functional defects	 No display Display abnormally Short circuit Line defect Excess power consumption
4.1.2	Missing	Missing function component
4.1.3	Crack	Glass crack

4.2 Minor defect

Item No	Items to be inspected	Inspection standard		
4.2.1	Spot Defect Including Black spot	For dark/white spot is define $\varphi = (\mathbf{x} + \mathbf{y}) / 2$ $X \leftarrow \frac{\mathbf{x}}{\mathbf{x}}$ \mathbf{y}	ined	
	White spot Pinhole Foreign particle	Size φ(mm) φ≤0.15 2mm(min) apart	Acceptable Quantity Ignore	
	Polarizer dirt	0.15 < φ≤ 0.25 5mm(min) apart	3	
		0.25<φ	Not allowed	

OUTGOING QUALITY STANDARD			PAGE 3 OF 5
ITLE:FUNCTIONAL TEST & INSPECTION CRITERIA			
		Define:	Vidth
4.2.2	Line Defect Including Black line	Width(mm) Length(mm)	Acceptable Quantity
	White line	W≤0.05 and L≤10	Ignore
	Scratch	0.05 < W≤0.08 and L≤10 3mm(min) apart	3
		0.08 < W≤0.10 andL≤5 3mm(min) apart	1
		0.10< W or 10 <l< td=""><td>Not allowed</td></l<>	Not allowed
		Size φ(mm)	Acceptable Quantity
	Polarizer	φ≤0.25	Ignore
4.2.3	Dent/Bubble	Non visible area	Ignore
		0.25<φ≤0.40 5mm(min) apart	2
		0.40< φ	Not allowed
		Bright and Black dot def	and
4.2.4	Electrical Dot Defect	Inspection pattern: Full and blue screens	white, Full black, Red, green
		Item	Acceptable Quantity
		Black dot defect	2
		Bright dot defect	0
		Total Dot	2

TITLE:FUNG	OUTGOIN CTIONAL TEST & IN	PAGE 4 OF 5	
		1.Corner chips:	X Y Y
4.2.5	Touch panel chips	Size(mm) X≤3mm Y≤3mm Z≤T	Ignore T: Glass thickness X: Length Y: Width Z: thickness
		2. Side chips:	A contribution
		Size(mm) X≤5mm Y≤3mm Z≤T	Ignore T: Glass thickness X: Length Y: Width Z: thickness

OUTGOING QUALITY STANDARD	PAGE 5 OF 5
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA	

Note: 1. Dot defect is defined as the defective area of the dot area is larger than 50% of the dot area.

- 2. The distance between black dot defects or black and bright dot defects should be more than 5mm apart. The distance between two bright dot defects should be more than 15mm apart
- 3. Polarizer bubble is defined as the bubble appears on active display area. The defect of polarizer bubble shall be ignored if the polarizer bubble appears on the outside of active display area.
- 4. Mura is checker by 6% ND filter.
- 5. Foreign particle on the surface of the LCM should be ignore.

■ PRECAUTIONS FOR USING LCD MODULES

Handing Precautions

- (1) The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

- (6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.

- (7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
 - (9) Do not attempt to disassemble or process the LCD module.
 - (10) NC terminal should be open. Do not connect anything.
 - (11) If the logic circuit power is off, do not apply the input signals.
- (12) Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
- Tools required for assembling, such as soldering irons, must be properly grounded. make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential
- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated

- (13) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
 - Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - Do not damage or modify the pattern writing on the printed circuit board.
 - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
 - Do not drop, bend or twist LCM.

Handling precaution for LCM

LCM is easy to be damaged.

Please note below and be careful for handling!

Correct handling:



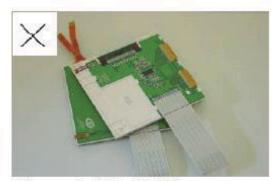


As above picture, please handle with anti-static gloves around LCM edges.

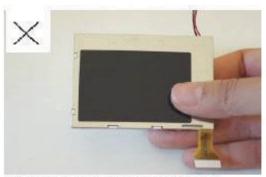
Incorrect handling:



Please don't touch IC directly.



Please don't stack LCM.

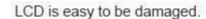


Please don't hold the surface of panel.



Please don't stretch interface of output, such as FPC cable.

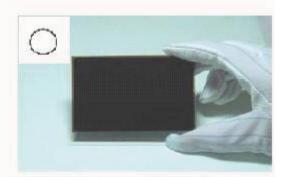
Handling precaution for LCD



Please note below and be careful for handling!

Correct handling:





As above photo, please handle with anti-static gloves around LCD edges.

Incorrect handling:

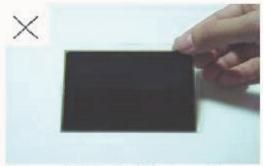


Please don't stack the LCDS.





Please don't operate with sharp stick such as pens.



Please don't touch ITO glass without anti-static gloves.

Storage Precautions

When storing the LCD modules, the following precaution is necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the anti-static electricity container in which they were shipped. Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

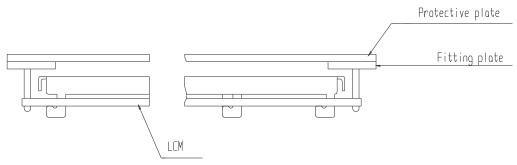
- Exposed area of the printed circuit board.
- -Terminal electrode sections.

USING LCD MODULES

Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

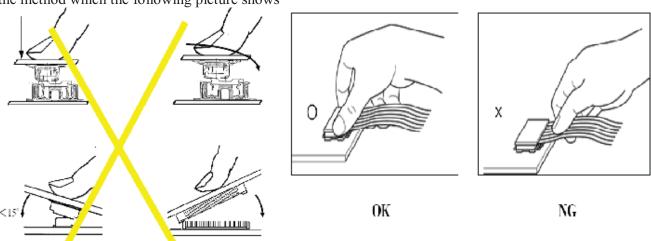
(1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



(2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be ± 0.1 mm.

Precaution for assemble the module with BTB connector:

Please note the position of the male and female connector position,don't assemble or assemble like the method which the following picture shows



Precaution for soldering to the LCM

	Hand soldering	Machine drag soldering	Machine press soldering
No ROHS product	290°C ~350°C.	330°C ~350°C.	300°C ~330°C.
	Time : 3-5S.	Speed: 4-8 mm/s.	Time : 3-6S.
			Press: 0.8~1.2Mpa
ROHS product	340°C ~370°C.	350°C ~370°C.	330°C ~360°C.
	Time : 3-5S.	Time : 4-8 mm/s.	Time : 3-6S.
			Press: 0.8~1.2Mpa

- (1) If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- (2) When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- (3) When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

Precautions for Operation

- (1) Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.
- (2) It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- (3) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, Which will come back in the specified operating temperature.
- (4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- (5) A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature,50%RH or less is required.
 - (6) Input each signal after the positive/negative voltage becomes stable.
- (7) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

Safety

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

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Limited Warranty

Unless agreed otherwise between Display Future Ltd and customer, Display Future will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with LCD acceptance standards (copies available upon request) for a period of one year from date of production. Cosmetic/visual defects must be returned within 90 days of the shipment. Confirmation of such date shall be based on data code on the product.

The warranty liability of Display Future limited to repair and/or replacement on the terms set forth above. Display Future will not be responsible for any subsequent or consequential events.

Return LCM under warranty

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

- Broken LCD glass.
- PCB eyelet is damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- Soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet, conductors and terminals.

■ PRIOR CONSULT MATTER

- 1. ①For Display Future standard products, we keep the right to change material and processes for improving the product, without notice to our customers.
 - ②For OEM products, if any change is needed, which may affect the product property, we will consult with our customer in advance.
- 2. If you have special requirement about reliability condition, please let us know before you start the test on our samples.

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