

LCD MODULE SPECIFICATION

Model: DF-TFN0096FW-M1

This module uses ROHS materials

For customer acceptance

Customer	
Approved	
Comments	

The standard product specification may change without prior notice in order to improve performance or quality. Please contact Display Future Ltd for updated specification and product status before design for the standard product or release of the order.

Revision	1.0
Engineering	
Date	2017/09/18
Our Reference	

REVISION RECORD

REV NO.	REV DATE	CONTENTS	REVISED PAGE NO.
1.0	2017-09-18	First Release	

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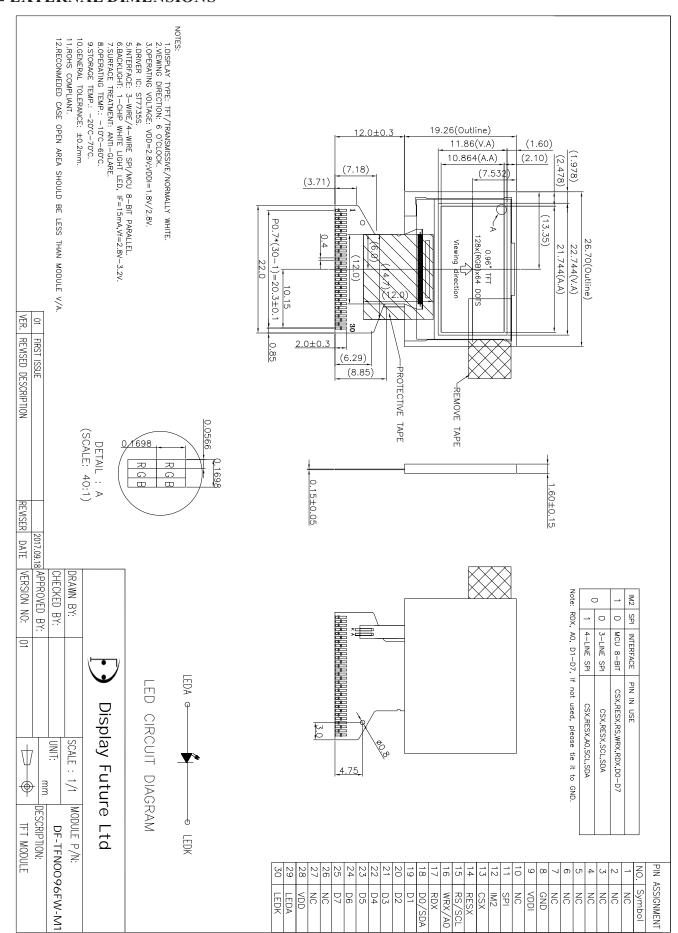
■ GENERAL INFORMATION

Item	Contents	Unit
LCD type	TFT/Transmissive/Normally white	/
Size	0.96	Inch
Viewing direction	6:00(without image inversion and least brightness change)	O' Clock
Gray scale inversion direction	12:00(contrast peak located at)	O' Clock
$LCM(W \times H \times D)$	26.70×19.26×1.60	mm ³
Active area (W×H)	21.744×10.864	mm ²
Pixel pitch (W×H)	0.1698×0.1698	mm ²
Number of dots	128 (RGB) × 64	/
Driver IC	ST7735S	/
Backlight type	1 LED	/
Lutanfaaatuu	(1)3-wire/4-wire SPI	,
Interface type	(2)8-bit parallel MCU	/
Color depth	262K	/
Pixel configuration	R.G.B stripe	/
Top polarizer surface treatment	Anti-glare	/
Input voltage	2.8	V
With/Without TSP	Without TSP	/
Weight	TBD	g

Note 1: RoHS compliant;

Note 2: LCM weight tolerance: $\pm 5\%$.

■ EXTERNAL DIMENSIONS



■ ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit
Power supply for analog	VDD	-0.3	4.6	V
Power supply for logic	VDDI	-0.3	4.6	V
Input voltage	VIN	-0.3	VDDI+0.3	V
Operating temperature	Тор	-10	60	°C
Storage temperature	Tst	-20	70	°C

■ ELECTRICAL CHARACTERISTICS

DC CHARACTERISTICS

Parameter	Symbol	Min	Тур	Max	Unit
Power supply for analog	VDD	2.6	2.8	3.6	V
Power supply for logic	VDDI	1.65	1.8/2.8	3.6	V
Input voltage 'H' level	V _{IH}	0.7VDDI	-	VDDI	V
Input voltage 'L' level	VIL	GND	-	0.3VDDI	V
Output voltage 'H' level	Voh	0.8VDDI	-	VDDI	V
Output voltage 'L' level	Vol	0	-	0.2VDDI	V

■ BACKLIGHT CHARACTERISTICS

Item	Symbol	Min.	Тур.	Max.	Unit	Condition
Forward voltage	Vf	2.8	3.0	3.2	V	Ta=25±2°C,
Forward current	If	-	15	-	mA	,
Power consumption	WBL	-	45	-	mW	60%RH±5%
Operating life time	-	30,000	-	-	Hrs	

Note:

Operating life time means brightness goes down to 50% initial brightness;

The life time of LED will be reduced if LED is driven by high current, high ambient temperature and humidity conditions.

■ ELECTRO-OPTICAL CHARACTERISTICS

Item		Symbol	Condition	Min	Тур	Max	Unit	Remark	Note
Response time		Tr+Tf		-	25	35	ms	FIG 1.	4
Contrast ratio		Cr	θ=0°	70	87	-	-	FIG 2.	1
Luminance uniformity		δ WHITE	Ø=0° Ta=25°C	75	-	-	%	FIG	2.3
Surface Lum	ninance	Lv		190	240	-	cd/m ²	FIG 2.	2
			Ø = 90°	40	50	-	deg	FIG 3.	
Viorving and	Viewing angle range		Ø = 270°	30	40	-	deg	FIG 3.	6
viewing angi			$\emptyset = 0$ °	40	50	-	deg	FIG 3.	
			Ø = 180°	40	50	-	deg	FIG 3.	
	Red	X		0.5294	0.5794	0.6294			
	Keu	у		0.3234	0.3734	0.4234			
	Green	X	θ=0°	0.3032	0.3532	0.4032			
CIE (x, y)	Green	у	Ø=0°	0.5396	0.5896	0.6396		FIG 2.	5
chromaticity	Blue	X	Ta=25℃	0.0977	0.1477	0.1977		110 2.	3
	Diuc	у	1 a-25 C	0.0488	0.0988	0.1488			1
	White	X		0.2365	0.2865	0.3365			
	vv iiite	у		0.2693	0.3193	0.3693			

Note 1. Contrast Ratio(CR) is defined mathematically as For more information see FIG 2.

Contrast Ratio = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3. The uniformity in surface luminance $, \delta$ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see FIG 2.

δ WHITE = Minimum Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)

Maximum Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)

- Note 4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1. The test equipment is Autronic-Melchers's ConoScope. Series.
- Note 5. CIE (x, y) chromaticity, The x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.
- Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the conrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.
- Note 7. For viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments For contrast ratio, Surface Luminance, Luminance uniformity, CIE The test data is base on TOPCON's BM-5 photo detector.

FIG. 1 The definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

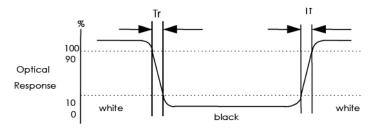
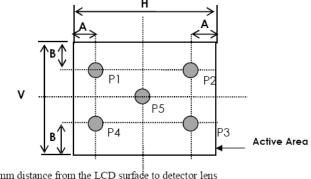


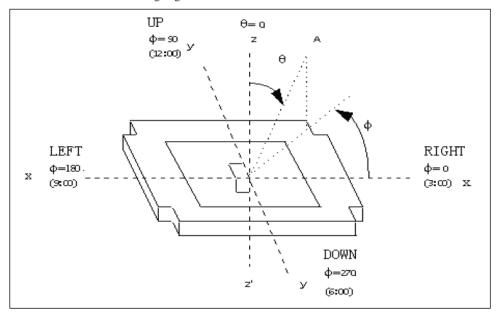
FIG. 2 Measuring method for Contrast ratio, surface luminance, Luminance uniformity , CIE (x, y) chromaticity



A: 5 mm B: 5 mm H,V: Active Area

Light spot size \varnothing =7mm, 500mm distance from the LCD surface to detector lens measurement instrument is TOPCON's luminance meter BM-5

FIG. 3 The definition of viewing angle



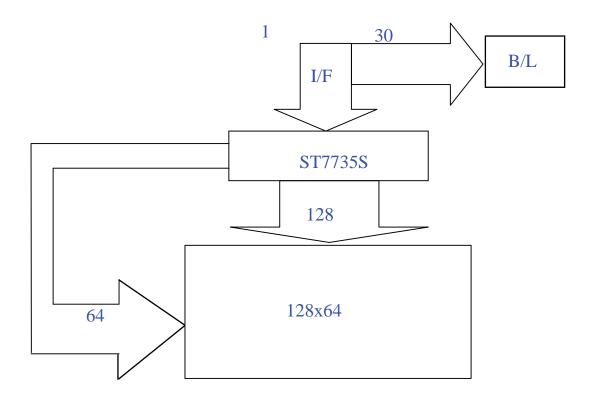
■ INTERFACE DESCRIPTION

NO.	SYMBOL	DESCRIPTION
1	NC	No connection.
2	NC	No connection.
3	NC	No connection.
4	NC	No connection.
5	NC	No connection.
6	NC	No connection.
7	NC	No connection.
8	GND	Ground.
9	VDDI	Power supply for logic circuits.
10	NC	No connection.
11	SPI	SPI="0", 3-line SPI enable. SPI="1", 4-line SPI enable. If not used, please fix this pin at GND level.
12	IM2	MCU Parallel Interface Bus and Serial Interface select . IM2='1', Parallel Interface . IM2='0', Serial Interface .
13	CSX	Chip select signal input. Active low.
14	RESX	This signal will reset the device and it must be applied to properly initialize the chip. Active low.
15	RS/SCL	Display data/command selection pin in MCU interface. In serial interface, this is used as SCL. If not used, please fix this pin at VDDI or GND level.
16	WRX/AO	Write Enable in MCU parallel interface. In 4-line SPI, this pin is used as AO(data/commad selection). If not used, please fix this pin at VDDI or GND level.
17	RDX	Read enable in MCU parallel interface. If not used, please fix this pin at VDDI or GND level.
18	D0/SDA	Data bus(LSB). D0 is the serial input/output signal in serial interface mode.
19	D1	Data bus.
20	D2	Data bus.
21	D3	Data bus.
22	D4	Data bus.
23	D5	Data bus.
24	D6	Data bus.
25	D7	Data bus(MSB).
26	NC	No connection.
27	NC	No connection.
28	VDD	Power supply for analog circuits.
29	LEDA	Backlight power supply Anode.
30	LEDK	Backlight power supply Cathode.

IM2	SPI	INTERFACE	PIN IN USE
1	0	MCU 8-BIT	CSX,RESX,RS,WRX,RDX,D0-D7
0	0	3-LINE SPI	CSX,RESX,SCL,SDA
	1	4-LINE SPI	CSX,RESX,A0,SCL,SDA

Note: RDX, A0, D1-D7, If not used, please tie it to GND.

■ BLOCK DIAGRAM



■ APPLICATION NOTES

1. Serial Interface Characteristics (3-line Serial)

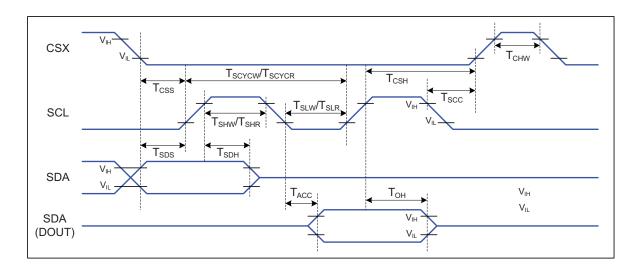


Figure 6 3-line Serial Interface Timing

Ta=25 °C, VDDI=1.65~3.7V, VDD=2.5~4.8V

Signal	Symbol	Parameter	Min	Max	Unit	Description
	TCSS	Chip Select Setup Time (Write)	15		ns	
	TCSH	Chip Select Hold Time (Write)	15		ns	
CSX	TCSS	Chip Select Setup Time (Read)	60		ns	
	TSCC	Chip Select Hold Time (Read)	65		ns	
	TCHW	Chip Select "H" pulse width	40		ns	
	TSCYCW	Serial Clock Cycle (Write)	66		ns	
	TSHW	SCL "H" Pulse Width (Write)	15		ns	
SCL	TSLW	SCL "L" Pulse Width (Write)	15		ns	
SCL	TSCYCR	Serial Clock Cycle (Read)	150		ns	
	TSHR	SCL "H" Pulse Width (Read)	60		ns	
	TSLR	SCL "L" Pulse Width (Read)	60		ns	
00.4	TSDS	Data Setup Time	10		ns	
SDA	TSDH	Data Hold Time	10		ns	For Maximum CL=30pF
(DIN) (DOUT)	TACC	Access Time	10	50	ns	For Minimum CL=8pF
(DOOT)	ТОН	Output Disable Time	15	50	ns	

Table 6 3-line Serial Interface Characteristics

2. Serial Interface Characteristics (4-line Serial)

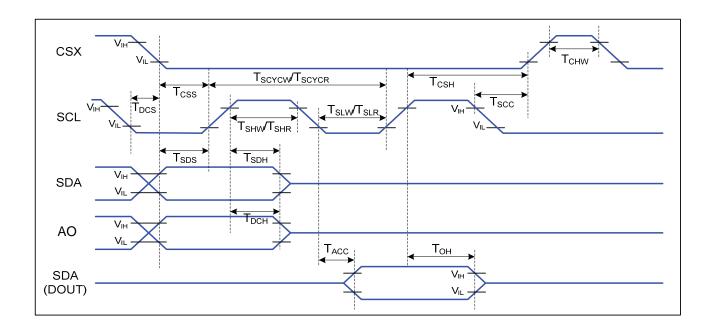


Figure 7 4-line Serial Interface Timing

Ta=25 °C, VDDI=1.65~3.7V, VDD=2.5~4.8V

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
	TCSS	Chip Select Setup Time (Write)	45		ns	
	TCSH	Chip Select Hold Time (Write)	45		ns	
CSX	TCSS	Chip Select Setup Time (Read)	60		ns	
	TSCC	Chip Select Hold Time (Read)	65		ns	
	TCHW	Chip Select "H" Pulse Width	40		ns	
	TSCYCW	Serial Clock Cycle (Write)	66		ns	-Write Command &
	TSHW	SCL "H" Pulse Width (Write)	15		ns	Data Ram
SCL	TSLW	SCL "L" Pulse Width (Write)	15		ns	Data Naiii
SCL	TSCYCR	Serial Clock Cycle (Read)	150		ns	-Read Command &
	TSHR	SCL "H" Pulse Width (Read)	60		ns	Data Ram
	TSLR	SCL "L" Pulse Width (Read)	60		ns	Dala Kalli
AO	TDCS	D/CX Setup Time	10		ns	
AO	TDCH	D/CX Hold Time	10		ns	
CDA	TSDS	Data Setup Time	10		ns	
SDA	TSDH	Data Hold Time	10		ns	For Maximum CL=30pF
(DIN) (DOUT)	TACC	Access Time	10	50	ns	For Minimum CL=8pF
(5001)	ТОН	Output Disable Time	15	50	ns	

Table 7 4-line Serial Interface Characteristics

3. Parallel Interface Characteristics: 8-bit Bus(8080 MCU Interface)

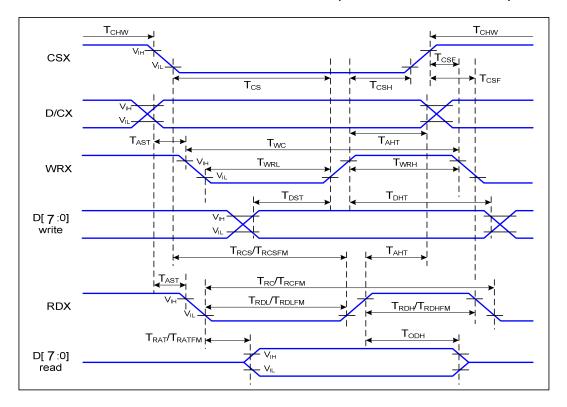


Figure 1 Parallel Interface Timing Characteristics (8080 Ceries MCU Interface)

Ta=25 °C, VDDI=1.65~3.7V, VDD=2.5~4.8V

Signal	Symbol	Symbol Parameter		Max	Unit	Description
D/CV	TAST	Address Setup Ttime	0		ns	
D/CX	TAHT	Address Hold Time (Write/Read)	10		ns	-
	TCHW	Chip Select "H" Pulse Width	0		ns	
	TCS	Chip Select Setup Time (Write)	15		ns	
CSX	TRCS	Chip Select Setup Time (Read ID)	45		ns	
CSX	TRCSFM	Chip Select Setup time (Read FM)	355		ns	-
	TCSF	Chip Select Wait Time (Write/Read)	10		ns	
	TCSH	Chip Select Hold Time	10		ns	
	TWC	Write Cycle	66		ns	
WRX	TWRH	Control Pulse "H" Duration	15		ns	
	TWRL	Control Pulse "L" Duration	15		ns	
	TRC	Read Cycle	160		ns	
RDX	TRDH	Control Pulse "H" Duration	90		ns	When Read Data
	TRDL	Control Pulse "L" Duration	45		ns	

RDX (FM)	TRCFM	Read Cycle (FM)			ns	When Read from	
	TRDHFM	Control Pulse "H" Duration (FM)	90		ns	Frame Memory	
	TRDLFM	Control Pulse "L" Duration (FM)	355		ns	Traine Memory	
	TDST	Data Setup Time	10		ns		
	TDHT	Data Hold Time	10		ns		
D[7:0]	TRAT	Read Access Time (ID)		40	ns	For CL=30pF	
	TRATFM	Read Access Time (FM)		340	ns		
	TODH	Output Disable Time	20	80	ns		

Table 4 8080 Parallel Interface Characteristics

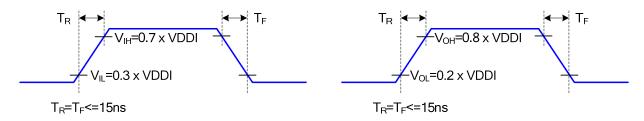


Figure 2 Rising And Falling Timing for Input And Output Signal

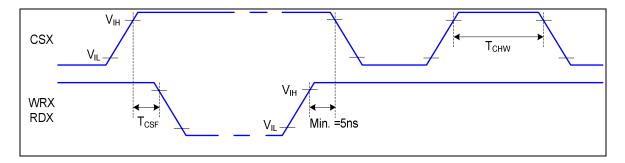


Figure 3 Chip Selection (CSX) Timing

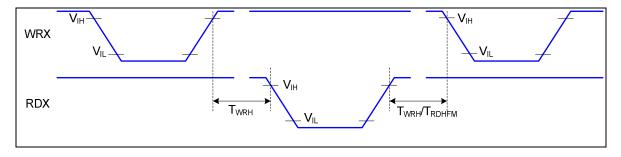


Figure 4 Write-to-Read And Read-to-Write Timing

■ RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition	Remarks
1	High Temperature Storage Test	70°C±2°C/96Hrs.	Note2
2	Low Temperature Storage Test	orage Test $-20^{\circ}\text{C} \pm 2^{\circ}\text{C}/96\text{Hrs}$.	
3	High Temperature Operation Test	60°C±2°C/96Hrs.	
4	Low Temperature Operation Test	-10°C±2°C/96Hrs.	Note1
5	High Temperature and High Humidity Operation Test	60±5°C, 90%RH 96Hrs.	Note1,2
6	Thermal Shock Test	-20±2°C(30Min.)~25±2°C(5Min.)~70±2°C(30Min.)	
0	(Non-operating)	10Cycles	
7	Vibration Test (Non-operating)	Frequency:10~55Hz Amplitude: 1.5mm Sweep Time: 11Mins Test Period: 6 Cycles For Each Direction Of X, Y, Z (Packing Condition)	
8	Shock Test (Non-operating)	100G, 6Ms Direction: ±X, ±Y, ±Z Cycle: 3 Times	
9	Electro Static Discharge Test (Non-operating)	Voltage: ±4KV, R:330Ω, C:150pF, Air Discharge, 10 Times. (Packing Condition)	

Note 1: Without water condensation

Note 2: The function test shall be conducted after 2 hours storage at the room temperature and humidity after removed from the test chamber.

■ INSPECTION CRITERION

OUTGOING QUALITY STANDARD	PAGE 1 OF 5
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA	

This specification is made to be used as the standard acceptance/rejection criteria for TFT module.

1 Sample plan

1.1 Lot size: Quantity per shipment lot per model

1.2 Sampling type: Normal inspection, Single sampling

1.3 Inspection level: II

1.4 Sampling table: MIL-STD-105D1.5 Acceptable quality level (AQL)

Major defect: AQL=0.65 Minor defect: AQL=1.50

2. Inspection condition

2.1 Ambient conditions:

a. Temperature: Room temperature 25±5℃

b. Humidity: (60± 10) %RH

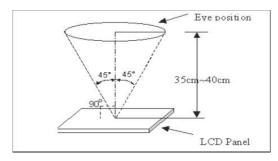
c. Illumination: Single fluoresænt lamp non-directive (300 to 700 Lux)

2.2 Viewing distance:

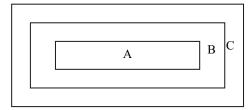
The distance between the LCD and the inspector's eyes shall be at least 35 ± 5 cm.

2.3 Viewing Angle

U/D: 45° /45° , L/R: 45° /45°



- 3. Definition of Inspection Item.
 - 3.1 Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD.

OUTGOING QUALITY STANDARD	PAGE 2 OF 5
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA	

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product. 4. Inspection standards

Defects are classified as majot defects and minor defects according to the degree of defectiveness defined herein.

4.1 Major defect

Item No	Items to be inspected	Inspection Standard
4.1.1	All functional defects	 No display Display abnormally Short circuit Line defect Excess power consumption
4.1.2	Missing	Missing function component
4.1.3	Crack	Glass crack

4.2 Minor defect

Item No	Items to be inspected	Inspe	ection standard
4.2.1	Spot Defect Including Black spot	For dark/white spot is def $\varphi = (\mathbf{x} + \mathbf{y}) / 2$ $\longrightarrow \mathbf{X} \qquad \qquad$	ined
	White spot	Size φ(mm)	Acceptable Quantity
	Pinhole Foreign particle	φ≤0.15 2mm(min) apart	Ignore
	Polarizer dirt	0.15 < φ≤ 0.25 5mm(min) apart	3
		0.25<φ	Not allowed

	OUTGOIN	G QUALITY STANDARD	PAGE 3 OF 5
ITLE:FUNCTIONAL TEST & INSPECTION CRITERIA			
		Define:	Vidth
4.2.2	Line Defect Including Black line	Width(mm) Length(mm)	Acceptable Quantity
	White line	W≤0.05 and L≤10	Ignore
	Scratch	0.05 < W≤0.08 and L≤10 3mm(min) apart	3
		0.08 < W≤0.10 andL≤5 3mm(min) apart	1
		0.10< W or 10 <l< td=""><td>Not allowed</td></l<>	Not allowed
	Polarizer Dent/Bubble	Size φ(mm)	Acceptable Quantity
		φ≤0.25	Ignore
4.2.3		Non visible area	Ignore
		0.25<φ≤ 0.40 5mm(min) apart	2
		0.40< φ	Not allowed
	Electrical Dot Defect	Bright and Black dot def	and
4.2.4		Inspection pattern: Full and blue screens	white, Full black, Red, green
		Item	Acceptable Quantity
		Black dot defect	2
		Bright dot defect	0
		Total Dot	2

ΓLE:FUN(IG QUALITY STANDARE	PAGE 4 OF 5
		1.Corner chips:	X Y Y
		Size(mm)	Acceptable Quantity
4.2.5	Touch panel chips	X≤3mm Y≤3mm Z≤T	Ignore T: Glass thickness X: Length Y: Width Z: thickness
		2. Side chips:	X Z
		Size(mm)	Acceptable Quantity
		X≤5mm Y≤3mm Z≤T	Ignore T: Glass thickness X: Length Y: Width Z: thickness

OUTGOING QUALITY STANDARD	PAGE 5 OF 5
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA	

Note: 1. Dot defect is defined as the defective area of the dot area is larger than 50% of the dot area.

- 2. The distance between black dot defects or black and bright dot defects should be more than 5mm apart. The distance between two bright dot defects should be more than 15mm apart
- 3. Polarizer bubble is defined as the bubble appears on active display area. The defect of polarizer bubble shall be ignored if the polarizer bubble appears on the outside of active display area.
- 4. Mura is checker by 6% ND filter.
- 5. Foreign particle on the surface of the LCM should be ignore.

■ PRECAUTIONS FOR USING LCD MODULES

Handing Precautions

- (1) The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

- (6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.

- (7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
 - (9) Do not attempt to disassemble or process the LCD module.
 - (10) NC terminal should be open. Do not connect anything.
 - (11) If the logic circuit power is off, do not apply the input signals.
- (12) Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
- Tools required for assembling, such as soldering irons, must be properly grounded. make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential
- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated

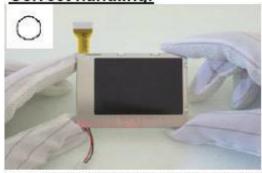
- (13) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
 - Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - Do not damage or modify the pattern writing on the printed circuit board.
 - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
 - Do not drop, bend or twist LCM.

Handling precaution for LCM

LCM is easy to be damaged.

Please note below and be careful for handling!

Correct handling:



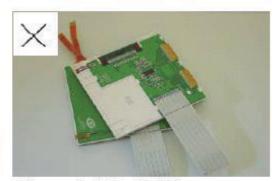


As above picture, please handle with anti-static gloves around LCM edges.

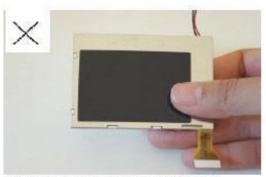
Incorrect handling:



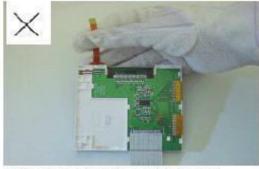
Please don't touch IC directly.



Please don't stack LCM.



Please don't hold the surface of panel.



Please don't stretch interface of output, such as FPC cable.

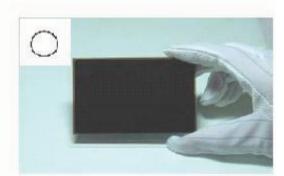
Handling precaution for LCD

LCD is easy to be damaged.

Please note below and be careful for handling!

Correct handling:





As above photo, please handle with anti-static gloves around LCD edges.

Incorrect handling:



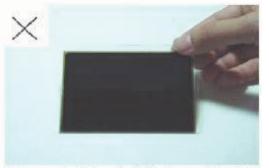
Please don't stack the LCDS.



Please don't hold the surface of LCD.



Please don't operate with sharp stick such as pens.



Please don't touch ITO glass without anti-static gloves.

Storage Precautions

When storing the LCD modules, the following precaution is necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the anti-static electricity container in which they were shipped.

 Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

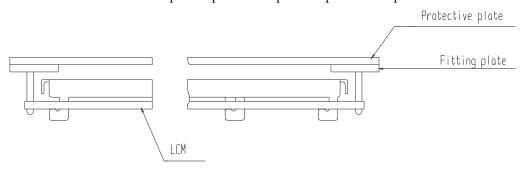
- Exposed area of the printed circuit board.
- -Terminal electrode sections.

USING LCD MODULES

Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

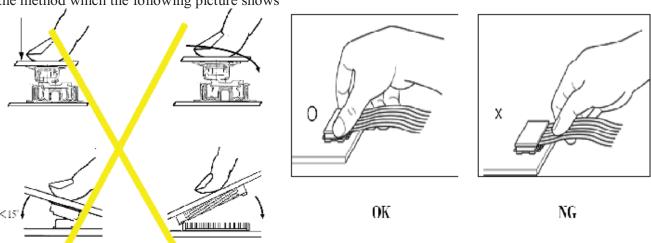
(1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



(2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be ± 0.1 mm.

Precaution for assemble the module with BTB connector:

Please note the position of the male and female connector position,don't assemble or assemble like the method which the following picture shows



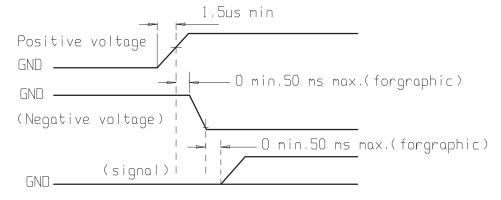
Precaution for soldering to the LCM

	Hand soldering	Machine drag soldering	Machine press soldering
No ROHS	290°C ~350°C.	330°C ~350°C.	300°C ~330°C.
product	Time : 3-5S.	Speed: 4-8 mm/s.	Time : 3-6S.
product			Press: 0.8~1.2Mpa
ROHS	340°C ~370°C.	350°C ~370°C.	330°C ~360°C.
product	Time : 3-5S.	Time: 4-8 mm/s.	Time : 3-6S.
product			Press: 0.8~1.2Mpa

- (1) If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- (2) When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- (3) When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

Precautions for Operation

- (1) Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.
- (2) It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- (3) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, Which will come back in the specified operating temperature.
- (4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- (5) A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50%RH or less is required.
 - (6) Input each signal after the positive/negative voltage becomes stable.
- (7) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.



Safety

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

Limited Warranty

Unless agreed otherwise between Display Future Ltd and customer, Display Future will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with LCD acceptance standards (copies available upon request) for a period of one year from date of production. Cosmetic/visual defects must be returned within 90 days of the shipment. Confirmation of such date shall be based on data code on the product.

The warranty liability of Display Future limited to repair and/or replacement on the terms set forth above. Display Future will not be responsible for any subsequent or consequential events.

Return LCM under warranty

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

- Broken LCD glass.
- PCB eyelet is damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- Soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet, conductors and terminals.

■ PRIOR CONSULT MATTER

- 1. ①For Display Future standard products, we keep the right to change material and processes for improving the product, without notice to our customers.
 - ②For OEM products, if any change is needed, which may affect the product property, we will consult with our customer in advance.
- 2. If you have special requirement about reliability condition, please let us know before you start the test on our samples.

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